

## **General Description**

The SJZ011N04 uses SGT technology to provide excellent Rds(on), low gate charge and fast switching characteristics. This device is suitable for use as a wide variety of applications.

#### Features

- Low Gate Charge
- 100% UIS Tested, 100% DVDS Tested
- High Power and current handing capability
- Lead free product is acquired

### Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

## Key Performance Parametes

Parameter	Value	Unit
V <sub>DS</sub>	40	V
R <sub>DS(ON)_TYP</sub>	0.83	mΩ
ID	390	А
Q <sub>G</sub>	92.6	nC



Schematic Diagram

TOLL top&bottom view

#### Package Marking and Ordering Information

Device/Ordering Code	Marking	Package	Packing	Reel Size	Tape width	Quantity
SJZ011N04	SJZ011N04	TOLL	Таре	/	١	2000 Pcs

## Table 1. Absolute Maximum Ratings ( $T_c=25^{\circ}$ unless otherwise noted)

Symbol	Parameter	Limit	Unit
V <sub>DS</sub>	Drain-Source Voltage (V <sub>GS</sub> =0V)	40	V
V <sub>GS</sub>	Gate-Source Voltage (V <sub>DS</sub> =0V)	±20	V
	Drain Current-Continuous(Tc=25℃)	390	А
ID	Drain Current-Continuous(T <sub>C</sub> =100 ℃)	247	А
DM (pluse)	Drain Current-Continuous@ Current-Pulsed (Note 1)	1560	А
P	Maximum Power Dissipation( $T_C=25^{\circ}C$ )	250	W
PD	Maximum Power Dissipation(Tc=100°C)	100	W
E <sub>AS</sub>	Avalanche energy (Note 2)	1406	mJ
TJ, TSTG	Operating Junction and Storage Temperature Range	-55 To 150	C

### Table 2. Thermal Characteristic

Symbol	Parameter	Тур	Max	Unit
R <sub>θ</sub> JC	Thermal Resistance, Junction-to-Case		0.5	°C/W



# SJZ011N04

## **40V N-Channel SGT Power MOSFET**

## Table 3. Electrical Characteristics (T\_J=25 $^{\circ}$ C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
On/Off States						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V I <sub>D</sub> =250µA	40			V
	$\frac{V_{DS}=40V, V_{GS}=0V T_{J}=25^{\circ}C}{V_{DS}=40V, V_{GS}=0V T_{J}=125^{\circ}C}$			1	μA	
IDSS		$V_{DS}$ =40V, $V_{GS}$ =0V T <sub>J</sub> =125 $^{\circ}$ C			100	μA
lgss	Gate-Body Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$			±100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250µA	2		4	V
gfs	Forward Transconductance	V <sub>DS</sub> =5V, I <sub>D</sub> =20A		39		S
Rds(on)	Drain-Source On-State Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =20A T <sub>J</sub> =25℃		0.83	1	mΩ
Dynamic Chara	acteristics			•		
Ciss	Input Capacitance			6530		pF
Coss	Output Capacitance	V <sub>DS</sub> =20V,V <sub>GS</sub> =0V, f=1.0MHz		3070		pF
Crss	Reverse Transfer Capacitance			166		pF
Rg	Gate resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1.0MHz		1.1		Ω
Switching Para	meters				L	
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>GS</sub> =10V, V <sub>DS</sub> =20V,		40		nS
tr	Turn-on Rise Time			38		nS
$t_{d(off)}$	Turn-Off Delay Time	$R_L=1\Omega$ , $R_{GEN}=6\Omega$		140		nS
tr	Turn-Off Fall Time			68		nS
Qg	Total Gate Charge			92.6		nC
Q <sub>gs</sub>	Gate-Source Charge	V <sub>GS</sub> =10V, V <sub>DS</sub> =20V, I <sub>D</sub> =20A		24.6		nC
Q <sub>gd</sub>	Gate-Drain Charge			28		nC
Source-Drain D	biode Characteristics					
I <sub>SD</sub>	Source-Drain Current (Body Diode)				390	Α
V <sub>SD</sub>	Forward on Voltage (Note 3)	V <sub>GS</sub> =0V, I <sub>S</sub> =20A			1.2	V
trr	Reverse Recovery Time	l⊧=20A, dl/dt=500A/μs		55		ns
Qrr	Reverse Recovery Charge	I⊧=20A, dI/dt=500A/μs		180		nC

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.

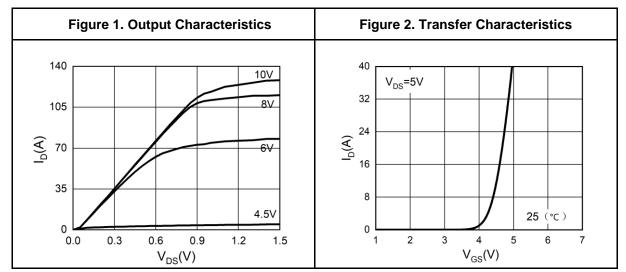
Notes 2.E<sub>AS</sub> condition:  $T_J=25^{\circ}C$ ,  $V_{DD}=40V$ ,  $V_G=10V$ ,  $Rg=25\Omega$ , L=0.5mH.

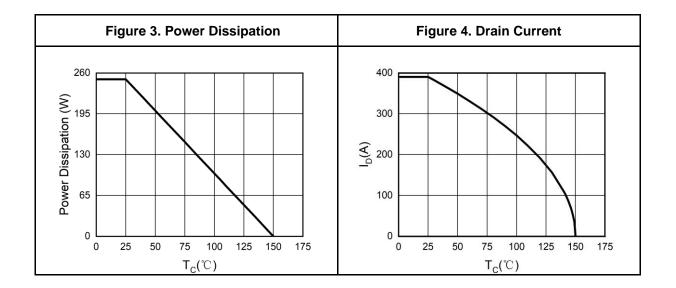
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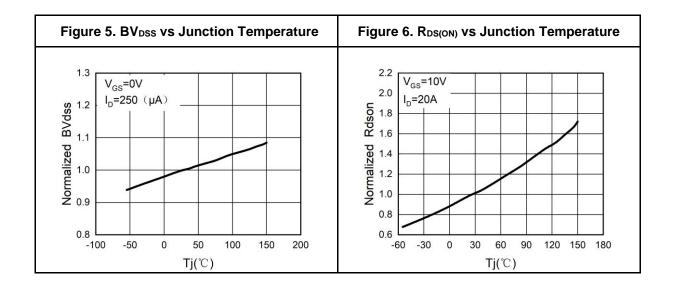


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## Typical Electrical And Thermal Characteristics (Curves)



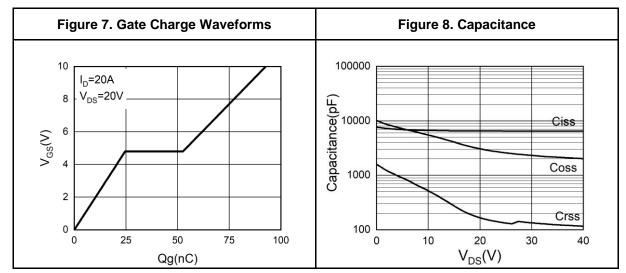


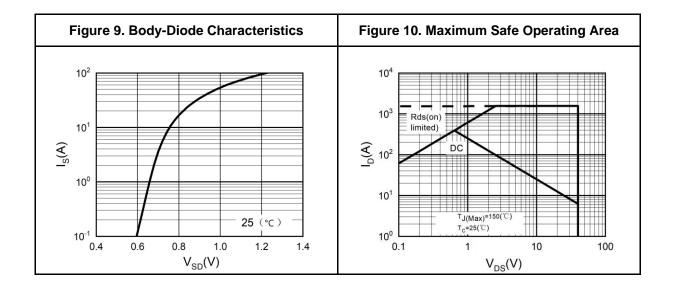




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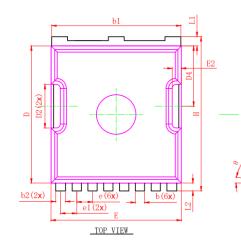
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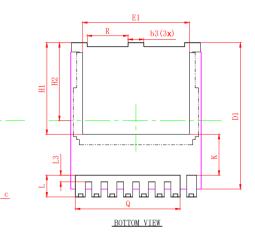


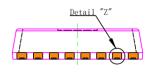




# **TOLL Package Information**









CVMDOL	MILLIMETER			
SYMBOL	MIN.	NOM.	MAX.	
A	2.200	2.300	2.400	
A1	1.700	1.800	1.900	
b	0.600	0.700	0.800	
b1	9.700	9.800	9.900	
b2	0.650	0.750	0.850	
b3	1.100	1.200	1.300	
с	0.400	0.500	0.600	
D	10.300	10.400	10.500	
D1	11.000	11.100	11.200	
D2	3.200	3.300	3.400	
D4	4.470	4.570	4.670	
E	9.800	9.900	10.000	
E1	8.000	8.100	8.200	
E2	0.500	0.600	0.700	
е	1.200 BSC			
e1		1.225 BSC		
Н	11.600	11.700	11.800	
H1		6.950 BSC		
H2		5.900 BSC		
i		0.100 REF.		
j		0.350 REF.		
K		3.100 REF.		
L	1.550	1.650	1.750	
L1	0.600			
L2	0.500	0.600	0.700	
L3	0.400			
Q	7.950 REF.			
R	3.000 3.100 3.200			
θ	10° REF.			



## Attention

This product described in this document can not be used in life support devices or systems, aircraft's control systems, and other applications whose failure can be reasonably expected to result in serious physical and/or material damage, apart from that when an application agreement is signed between customer and Wuxi Shangjia Semiconductor.

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