



40V N-Channel Trench Power MOSFET

General Description

The SJS40N240 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a wide variety of applications.

Features

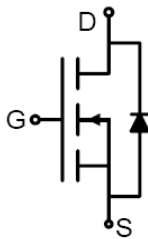
- Low Gate Charge
- High Power and current handling capability
- Lead free product is acquired

Application

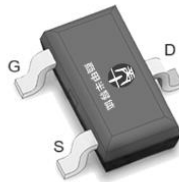
- PWM Applications
- Load Switch
- Power Management

Key Performance Parametes

Parameter	Value	Unit
V_{DS}	40	V
$R_{DS(ON_TYP)}$	23.5	mΩ
I_D	5.4	A
Q_G	14	nC



Schematic Diagram



SOT-23 top view



Package Marking and Ordering Information

Device/Ordering Code	Marking	Package	Packing	Reel Size	Tape width	Quantity
SJS40N240	4004	SOT-23	Tape	\	\	3000 Pcs

Table 1. Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Limit	Unit
V_{DS}	Drain-Source Voltage ($V_{GS}=0\text{V}$)	40	V
V_{GS}	Gate-Source Voltage ($V_{DS}=0\text{V}$)	± 20	V
I_D	Drain Current-Continuous($T_A=25^{\circ}\text{C}$)	5.4	A
	Drain Current-Continuous($T_A=100^{\circ}\text{C}$)	3.4	A
$I_{DM}(\text{pluse})$	Drain Current-Continuous@ Current-Pulsed (Note 1)	21.6	A
P_D	Maximum Power Dissipation($T_A=25^{\circ}\text{C}$)	1.5	W
	Maximum Power Dissipation($T_A=100^{\circ}\text{C}$)	0.6	W
E_{AS}	Avalanche energy (Note 2)	25	mJ
T_J, T_{STG}	Operating Junction and Storage Temperature Range	-55 To 150	$^{\circ}\text{C}$

Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient		85	$^{\circ}\text{C/W}$



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Table 3. Electrical Characteristics ($T_J=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V I _D =250μA	40			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =40V, V _{GS} =0V T _J =25°C			1	μA
		V _{DS} =40V, V _{GS} =0V T _J =125°C			100	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V			±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	1		2.5	V
g _{FS}	Forward Transconductance	V _{DS} =5V, I _D =3A		6.5		S
R _{DS(ON)}	Drain-Source On-State Resistance	V _{GS} =10V, I _D =3A T _J =25°C		23.5	30.6	mΩ
R _{DS(ON)}	Drain-Source On-State Resistance	V _{GS} =4.5V, I _D =2A T _J =25°C		30	39.9	mΩ
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =20V, V _{GS} =0V, f=1.0MHz		631		pF
C _{oss}	Output Capacitance			54		pF
C _{rss}	Reverse Transfer Capacitance			39		pF
R _g	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1.0MHz		1.6		Ω
Switching Parameters						
t _{d(on)}	Turn-on Delay Time	V _{GS} =10V, V _{DS} =20V, R _L =5Ω, R _{GEN} =3Ω		6		nS
t _r	Turn-on Rise Time			4		nS
t _{d(off)}	Turn-Off Delay Time			20		nS
t _f	Turn-Off Fall Time			3		nS
Q _g	Total Gate Charge	V _{GS} =10V, V _{DS} =20V, I _D =3A		14		nC
Q _{gs}	Gate-Source Charge			2.5		nC
Q _{gd}	Gate-Drain Charge			3		nC
Source-Drain Diode Characteristics						
I _{SD}	Source-Drain Current (Body Diode)				5.4	A
V _{SD}	Forward on Voltage (Note 3)	V _{GS} =0V, I _S =3A			1.2	V
t _{rr}	Reverse Recovery Time	I _F =3A, dI/dt=100A/μs		9		ns
Q _{rr}	Reverse Recovery Charge	I _F =3A, dI/dt=100A/μs		4		nC

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.

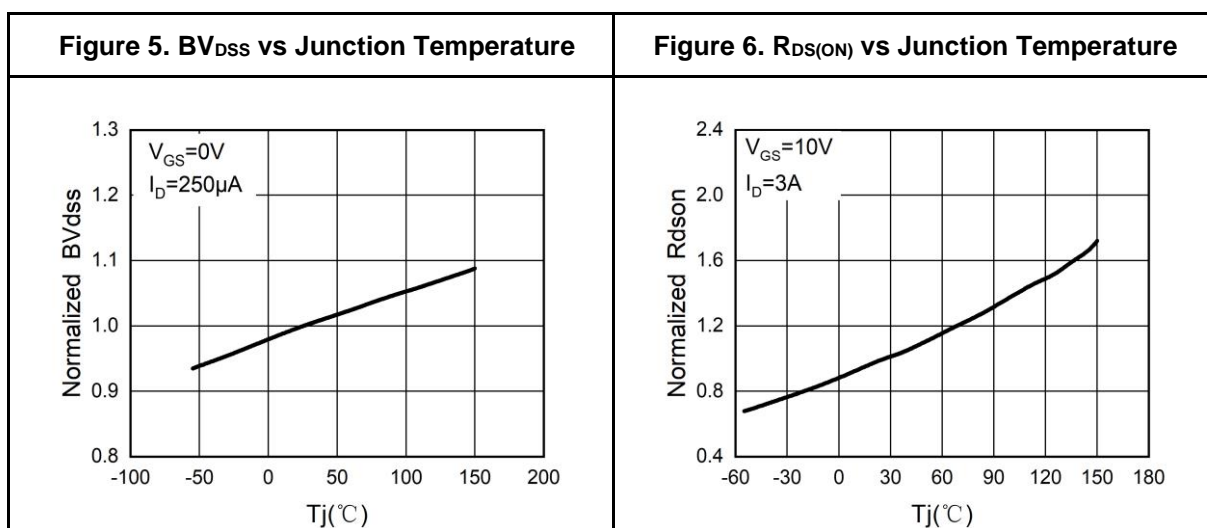
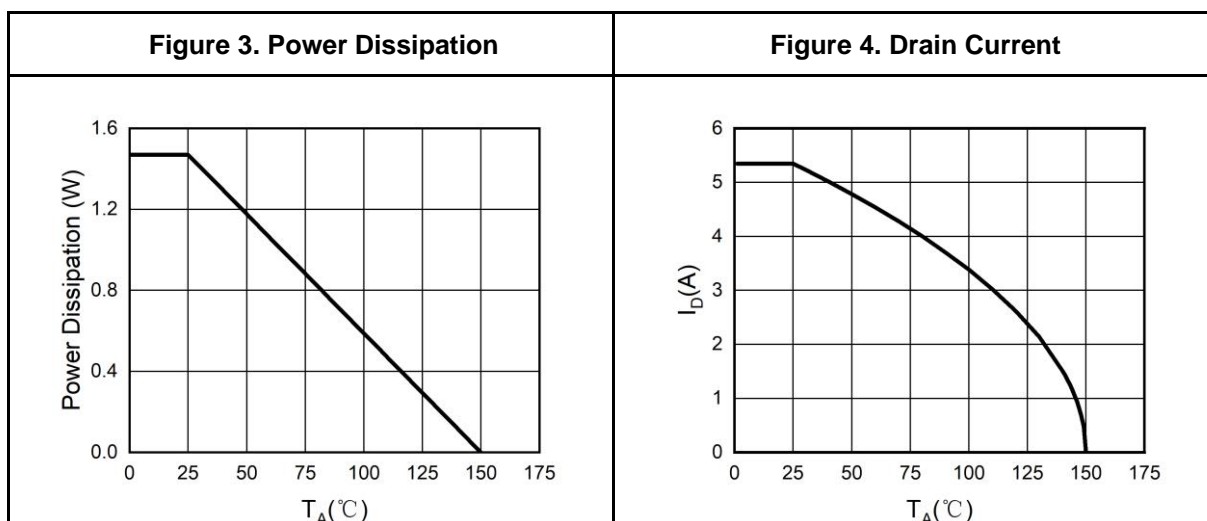
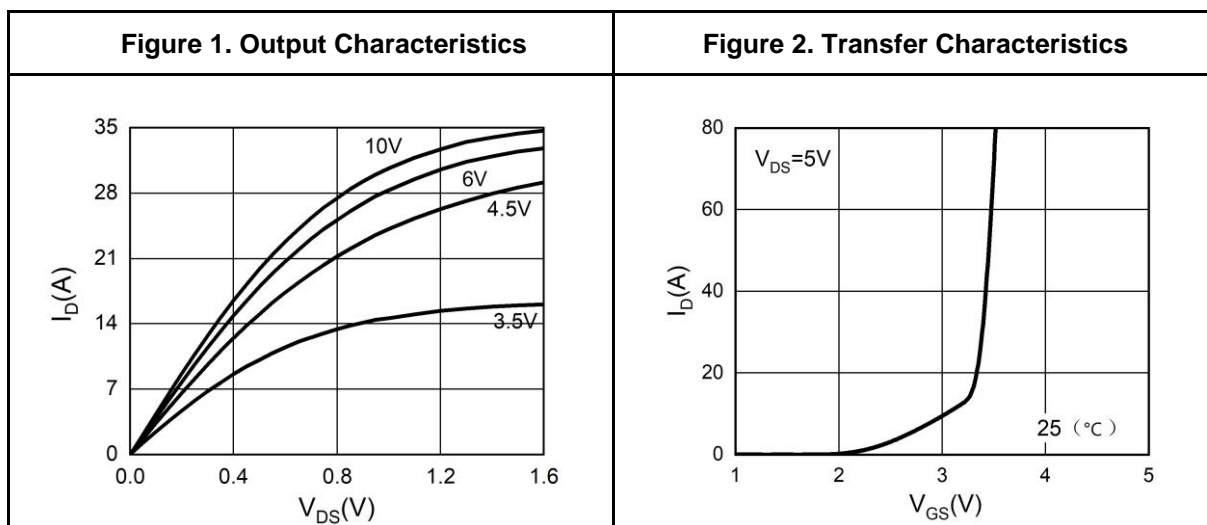
Notes 2.EAS condition: $T_J=25^{\circ}\text{C}, V_{DD}=30V, V_G=10V, R_g=25\Omega, L=0.5\text{mH}$.

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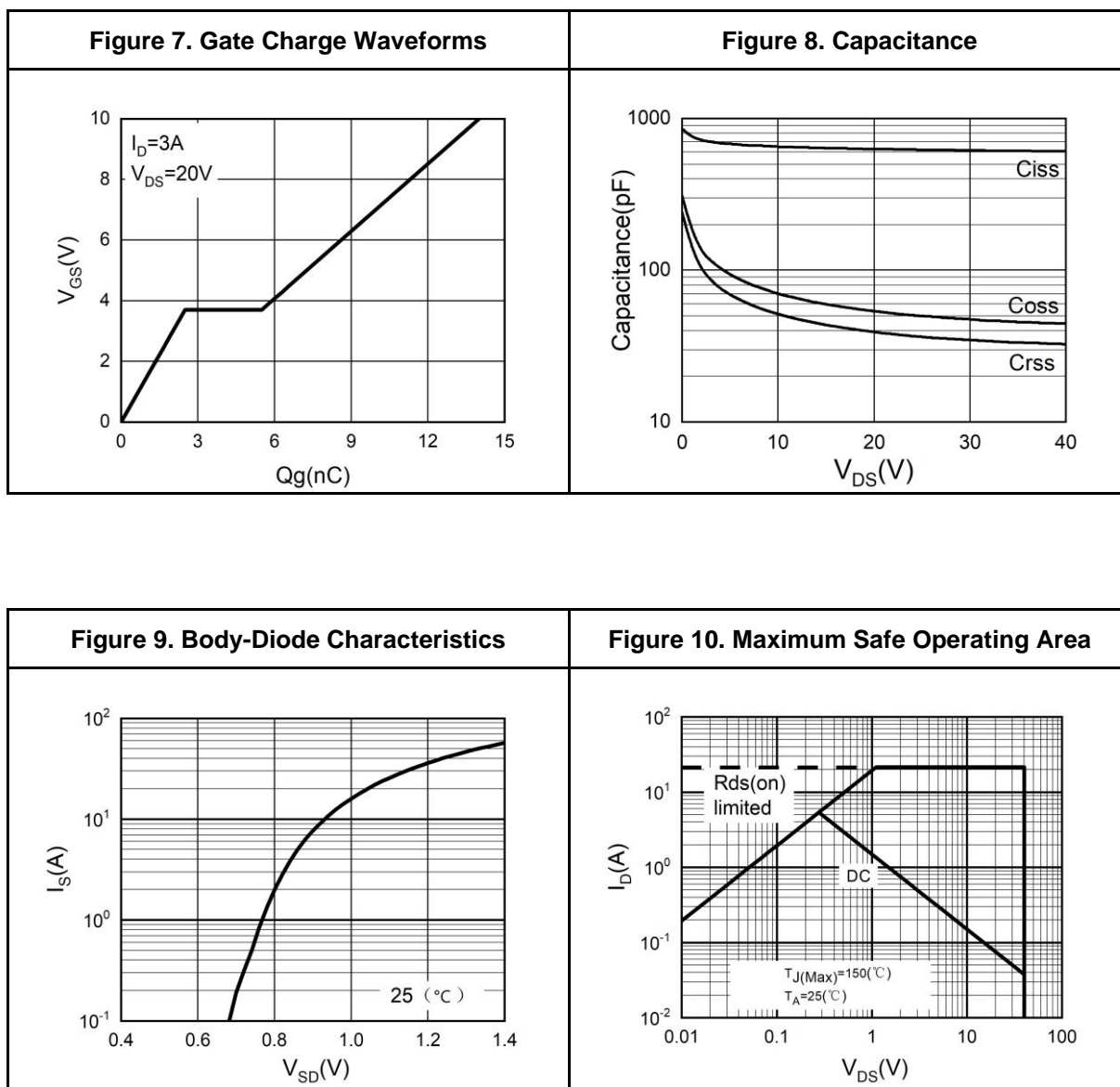
Typical Electrical And Thermal Characteristics (Curves)





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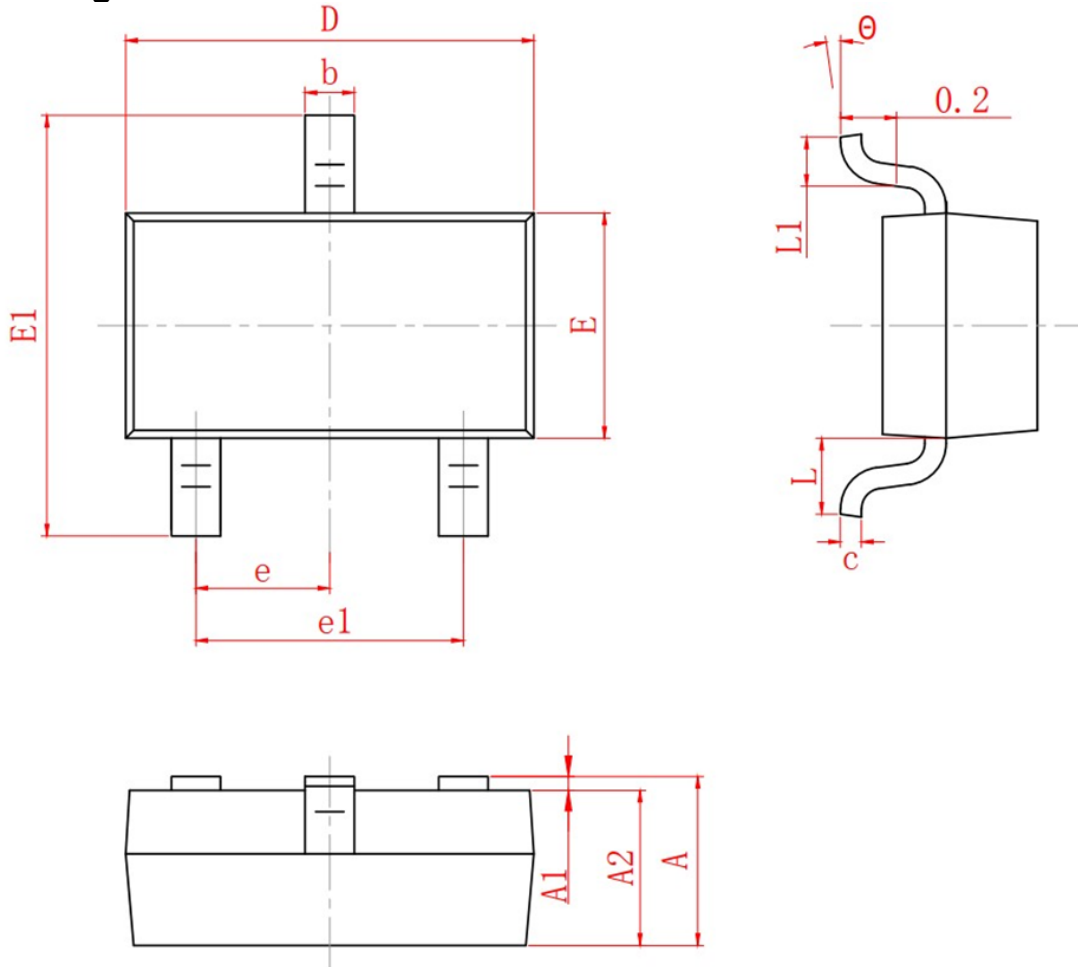
Typical Electrical And Thermal Characteristics (Curves)





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SOT-23 Package Information



SYMBOL	MIN	NOM	MAX
A	0.90	1.05	1.20
A1	0.00	0.05	0.10
A2	0.90	1.00	1.10
b	0.30	0.40	0.50
c	0.08	0.10	0.15
D	2.80	2.90	3.00
E	1.20	1.30	1.40
E1	2.30	2.40	2.50
L	0.30	0.40	0.50
θ	0°	5°	10°
L1	0.55 REF		
e	0.95 BSC		
e1	1.90 REF		



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