

**General Description**

The SJP60NP930 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as $\pm 4.5V$. This device is suitable for use as a wide variety of applications.

Features

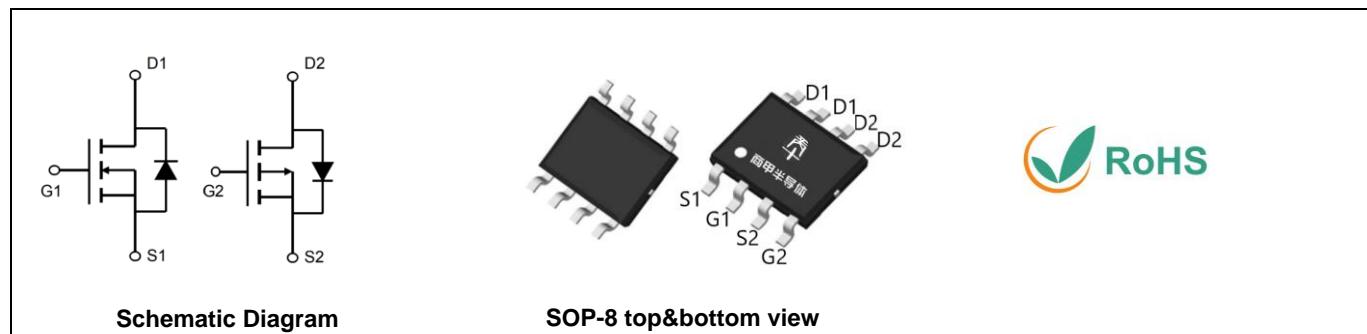
- Low Gate Charge
- High Power and current handing capability
- Lead free product is acquired

Application

- Battery Protection
- Power Management
- Load Switch

Key Performance Parametes

Parameter	Value	Value	Unit
V_{DS}	60	-60	V
$R_{DS(ON)}_{TYP}$	24	69	$m\Omega$
I_D	5.7	-3.6	A
Q_G	23	16	nC

**Package Marking and Ordering Information**

Device/Ordering Code	Marking	Package	Packing	Reel Size	Tape width	Quantity
SJP60NP930	SJP60NP930	SOP-8	Tape	\	\	4000 Pcs

Table 1. Absolute Maximum Ratings ($T_A=25^\circ C$ unless otherwise noted)

Symbol	Parameter	N Limit	P Limit	Unit
V_{DS}	Drain-Source Voltage ($V_{GS}=0V$)	60	-60	V
V_{GS}	Gate-Source Voltage ($V_{DS}=0V$)	± 20	± 20	V
I_D	Drain Current-Continuous($T_A=25^\circ C$)	5.7	-3.6	A
	Drain Current-Continuous($T_A = 100^\circ C$)	3.6	-2.3	A
I_{DM} (pulse)	Drain Current-Continuous@ Current-Pulsed (Note 1)	22.8	-14.4	A
P_D	Maximum Power Dissipation($T_A = 25^\circ C$)	2.1	1.95	W
	Maximum Power Dissipation($T_A = 100^\circ C$)	0.8	0.78	W
E_{AS}	Avalanche energy (Note 2)	56	49	mJ
T_J, T_{STG}	Operating Junction and Storage Temperature Range	-55 To 150		°C

Table 2. Thermal Characteristic

Symbol	Parameter	N Max	P Max	Unit
$R_{\theta JA}$	Thermal Resistance, Junction-to- Ambient	60.5	64	°C/W



60V N&P-Channel Trench Power MOSFET

Table 3. N-Channel Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ $I_{\text{D}}=250\mu\text{A}$	60			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=60\text{V}$, $V_{\text{GS}}=0\text{V}$ $T_J=25^\circ\text{C}$			1	μA
		$V_{\text{DS}}=60\text{V}$, $V_{\text{GS}}=0\text{V}$ $T_J=125^\circ\text{C}$			100	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$			± 100	nA
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_{\text{D}}=250\mu\text{A}$	1.0		2.5	V
g_{FS}	Forward Transconductance	$V_{\text{DS}}=5\text{V}$, $I_{\text{D}}=10\text{A}$		15		S
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance	$V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=10\text{A}$ $T_J=25^\circ\text{C}$		24	30	$\text{m}\Omega$
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance	$V_{\text{GS}}=4.5\text{V}$, $I_{\text{D}}=8\text{A}$ $T_J=25^\circ\text{C}$		29	38.6	$\text{m}\Omega$
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{\text{DS}}=30\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1.0\text{MHz}$		1000		pF
C_{oss}	Output Capacitance			58		pF
C_{rss}	Reverse Transfer Capacitance			50		pF
R_g	Gate resistance	$V_{\text{GS}}=0\text{V}$, $V_{\text{DS}}=0\text{V}$, $f=1.0\text{MHz}$		1		Ω
Switching Parameters						
$t_{\text{d}(\text{on})}$	Turn-on Delay Time	$V_{\text{GS}}=10\text{V}$, $V_{\text{DS}}=30\text{V}$, $R_L=3\Omega$, $R_{\text{GEN}}=3\Omega$		4		nS
t_r	Turn-on Rise Time			3.5		nS
$t_{\text{d}(\text{off})}$	Turn-Off Delay Time			15.8		nS
t_f	Turn-Off Fall Time			2		nS
Q_g	Total Gate Charge	$V_{\text{GS}}=10\text{V}$, $V_{\text{DS}}=30\text{V}$, $I_{\text{D}}=10\text{A}$		23		nC
Q_{gs}	Gate-Source Charge			3.5		nC
Q_{gd}	Gate-Drain Charge			5.4		nC
Source-Drain Diode Characteristics						
I_{SD}	Source-Drain Current (Body Diode)				5.7	A
V_{SD}	Forward on Voltage (Note 3)	$V_{\text{GS}}=0\text{V}$, $I_{\text{S}}=10\text{A}$			1.2	V
t_{rr}	Reverse Recovery Time	$I_F=10\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$		27		ns
Q_{rr}	Reverse Recovery Charge	$I_F=10\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$		30		nC

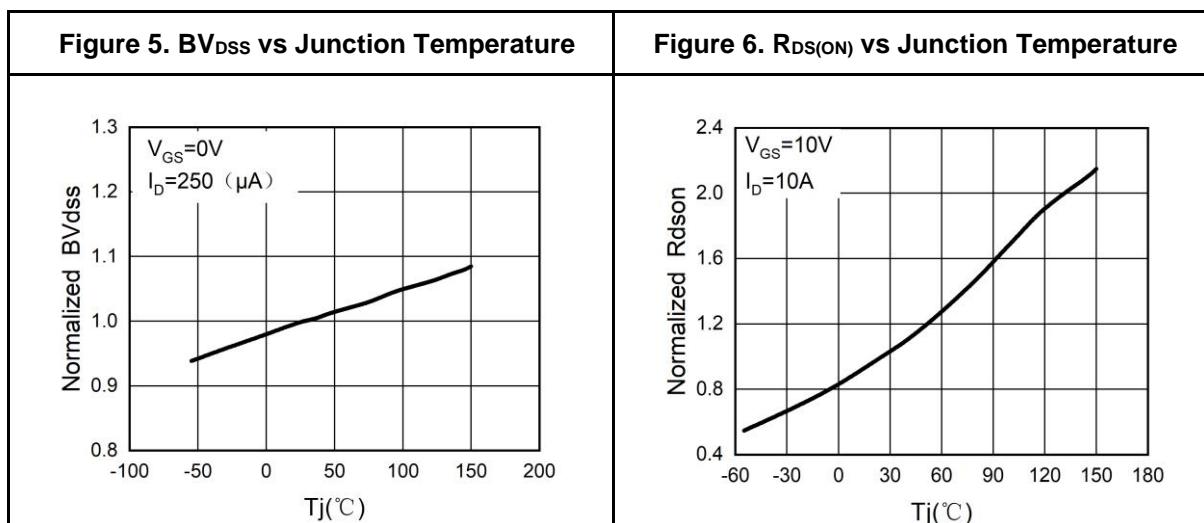
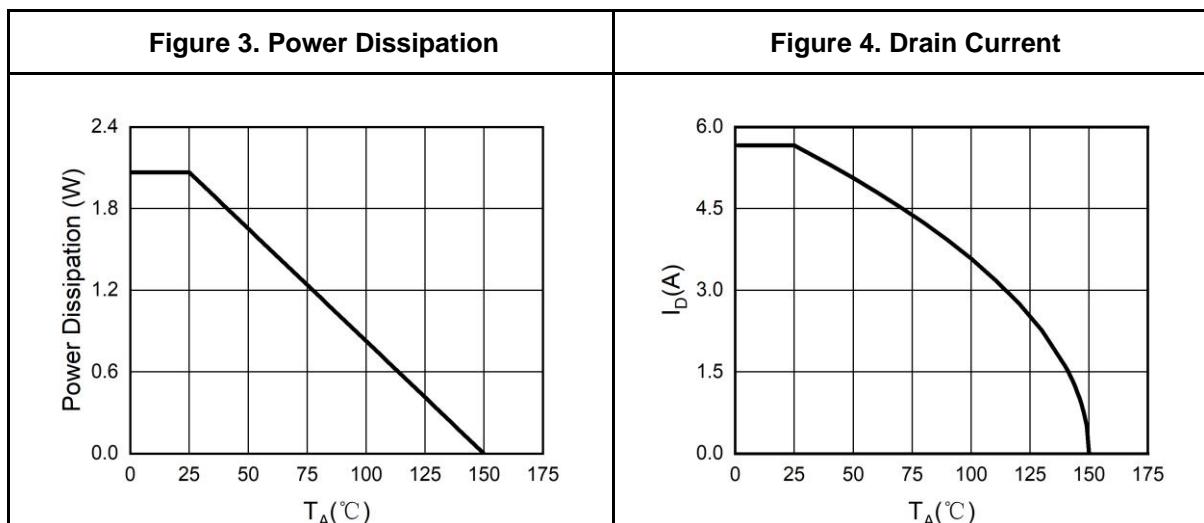
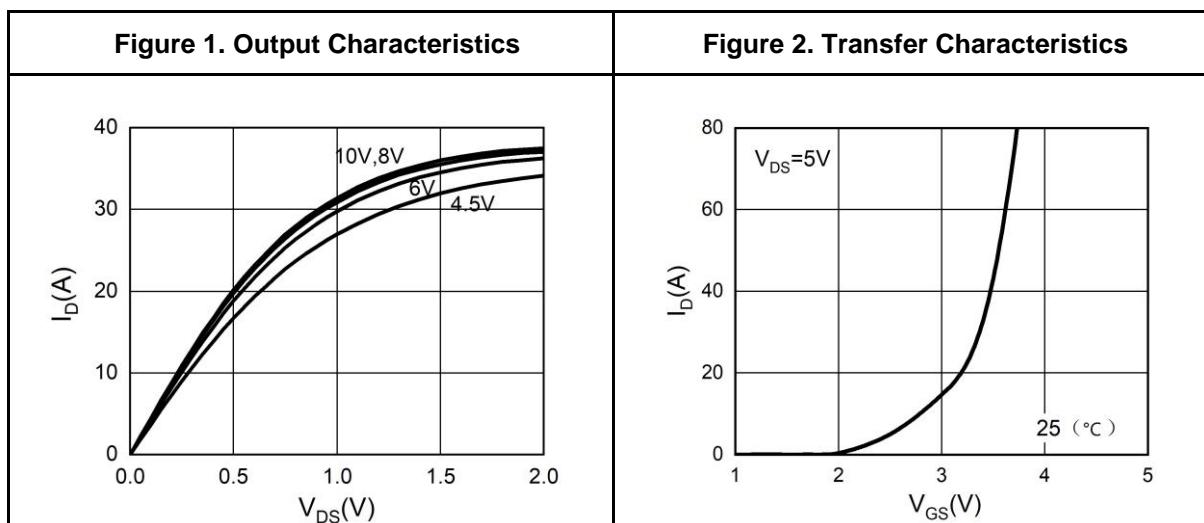
Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.

Notes 2.E_{AS} condition: $T_J=25^\circ\text{C}$, $V_{\text{DD}}=40\text{V}$, $V_G=10\text{V}$, $R_g=25\Omega$, $L=0.5\text{mH}$.

Notes 3.Repetitive Rating: Pulse width limited by maximum junction temperature.

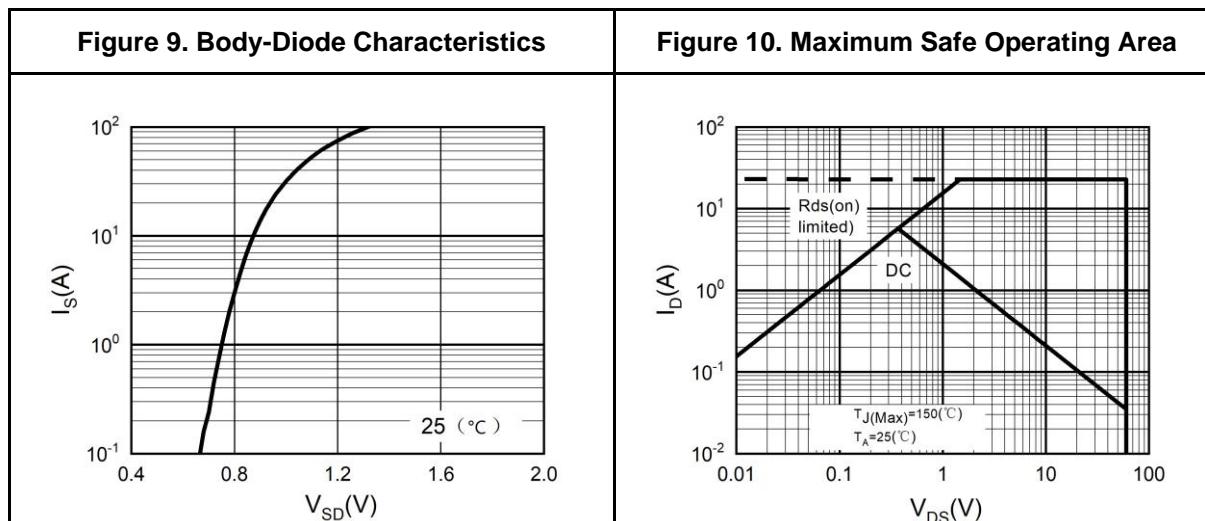
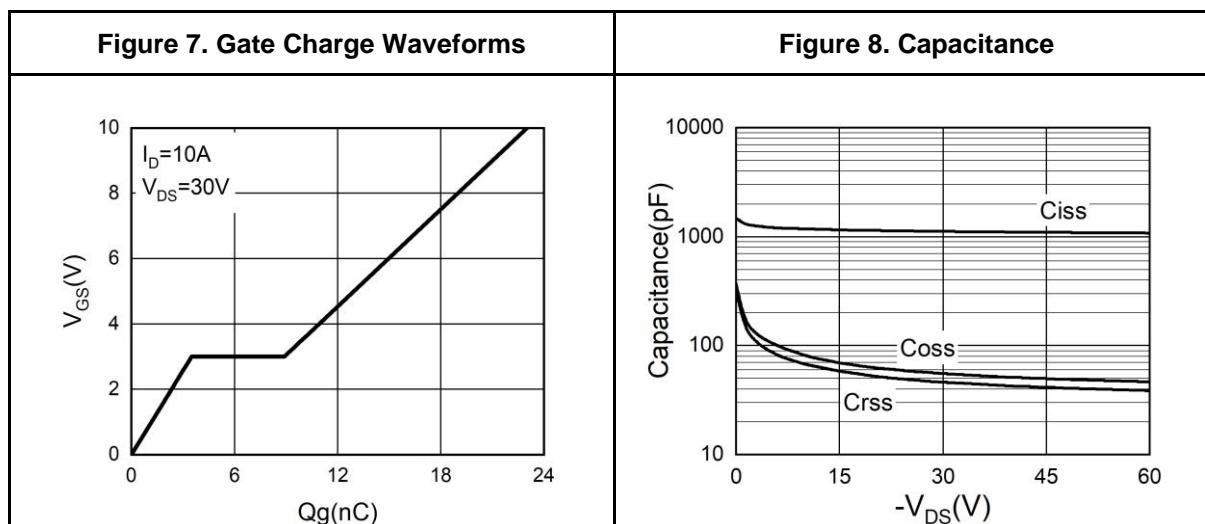


N-Channel Typical Electrical And Thermal Characteristics (Curves)





N-Channel Typical Electrical And Thermal Characteristics (Curves)





60V N&P-Channel Trench Power MOSFET

Table 4. P-Channel Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ $I_{\text{D}}=-250\mu\text{A}$	-60			V
$I_{\text{DS}(\text{SS})}$	Zero Gate Voltage Drain Current	$V_{\text{DS}}=-60\text{V}$, $V_{\text{GS}}=0\text{V}$ $T_J=25^\circ\text{C}$			-1	μA
		$V_{\text{DS}}=-60\text{V}$, $V_{\text{GS}}=0\text{V}$ $T_J=125^\circ\text{C}$			-100	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm20\text{V}$, $V_{\text{DS}}=0\text{V}$			±100	nA
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_{\text{D}}=-250\mu\text{A}$	-1		-2.5	V
g_{FS}	Forward Transconductance	$V_{\text{DS}}=-5\text{V}$, $I_{\text{D}}=-7\text{A}$		12		S
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance	$V_{\text{GS}}=-10\text{V}$, $I_{\text{D}}=-7\text{A}$ $T_J=25^\circ\text{C}$		69	89	$\text{m}\Omega$
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance	$V_{\text{GS}}=-4.5\text{V}$, $I_{\text{D}}=-5\text{A}$ $T_J=25^\circ\text{C}$		85	113	$\text{m}\Omega$
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{\text{DS}}=-30\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1.0\text{MHz}$		1120		pF
C_{oss}	Output Capacitance			57		pF
C_{rss}	Reverse Transfer Capacitance			46		pF
R_g	Gate resistance	$V_{\text{GS}}=0\text{V}$, $V_{\text{DS}}=0\text{V}$, $f=1.0\text{MHz}$		6.4		Ω
Switching Parameters						
$t_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{GS}}=-10\text{V}$, $V_{\text{DS}}=-30\text{V}$, $R_L=4\Omega$, $R_{\text{GEN}}=3\Omega$		8		nS
t_r	Turn-on Rise Time			10		nS
$t_{\text{d(off)}}$	Turn-Off Delay Time			26		nS
t_f	Turn-Off Fall Time			10		nS
Q_g	Total Gate Charge	$V_{\text{GS}}=-10\text{V}$, $V_{\text{DS}}=-30\text{V}$, $I_{\text{D}}=-7\text{A}$		16		nC
Q_{gs}	Gate-Source Charge			3		nC
Q_{gd}	Gate-Drain Charge			3.5		nC
Source-Drain Diode Characteristics						
I_{SD}	Source-Drain Current (Body Diode)				-3.6	A
V_{SD}	Forward on Voltage (Note 3)	$V_{\text{GS}}=0\text{V}$, $I_{\text{S}}=-7\text{A}$			-1.2	V
t_{rr}	Reverse Recovery Time	$I_F=-7\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$		27.5		ns
Q_{rr}	Reverse Recovery Charge	$I_F=-7\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$		30		nC

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.

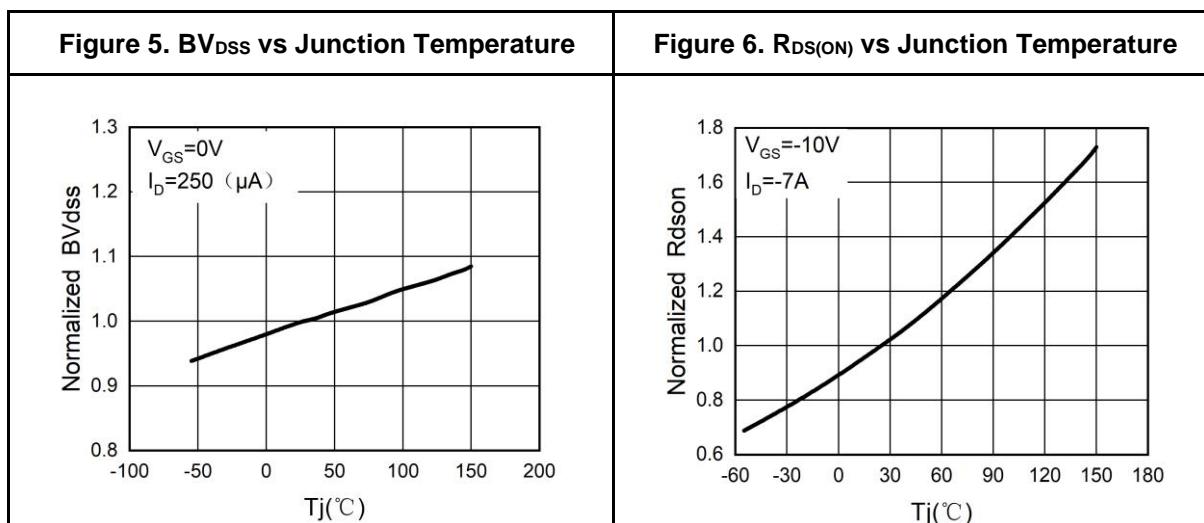
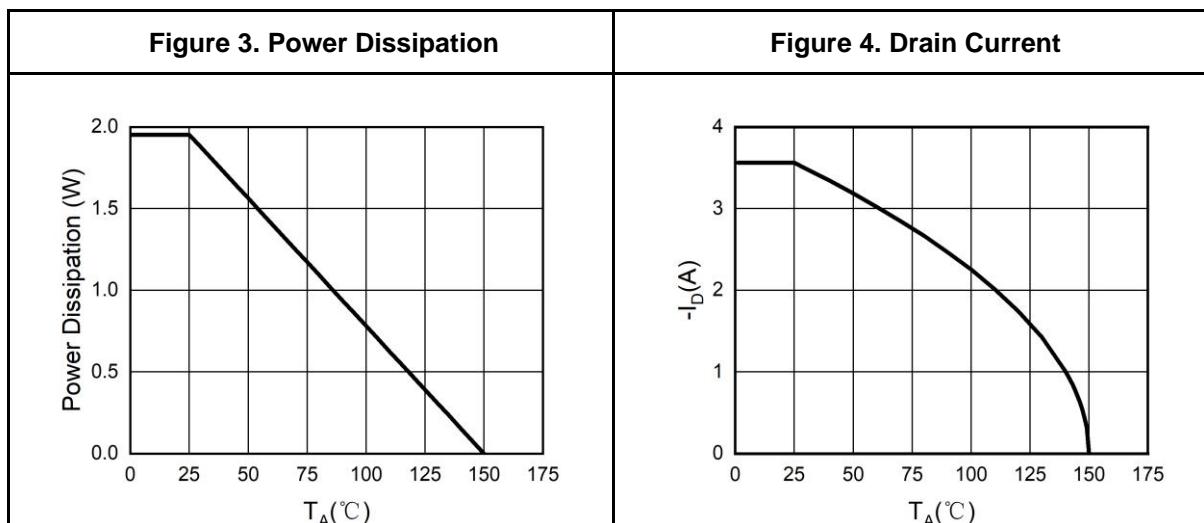
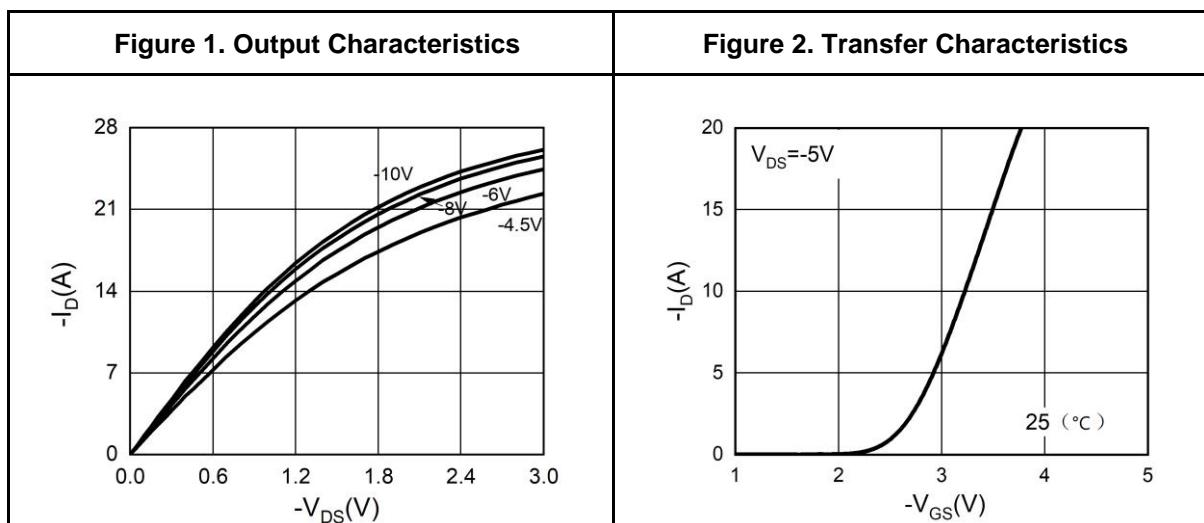
Notes 2.E_{AS} condition: $T_J=25^\circ\text{C}$, $V_{\text{DD}}=-40\text{V}$, $V_{\text{G}}=-10\text{V}$, $R_g=25\Omega$, $L=0.5\text{mH}$.

Notes 3.Repetitive Rating: Pulse width limited by maximum junction temperature.



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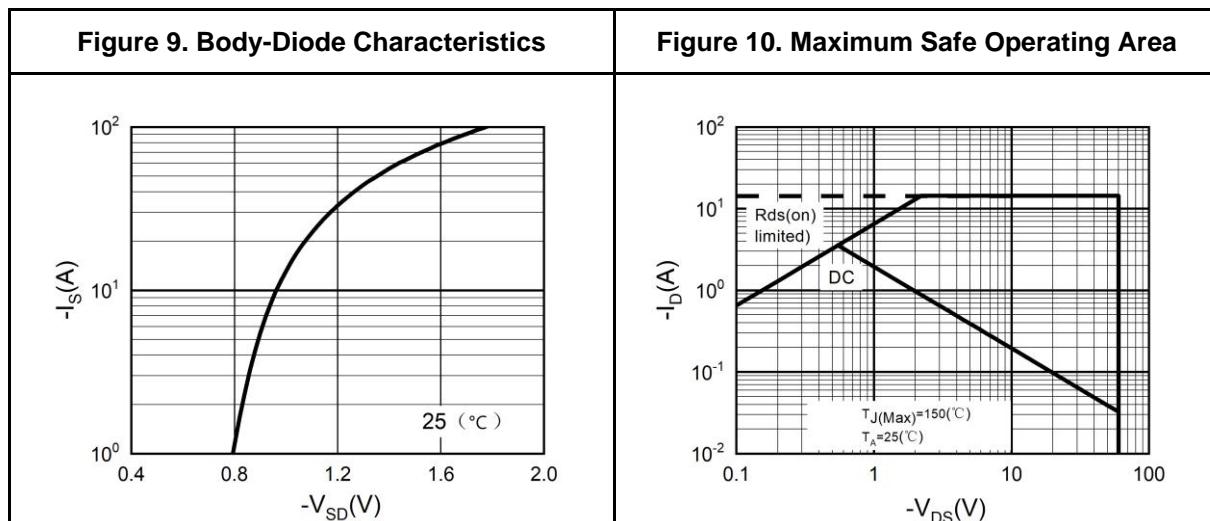
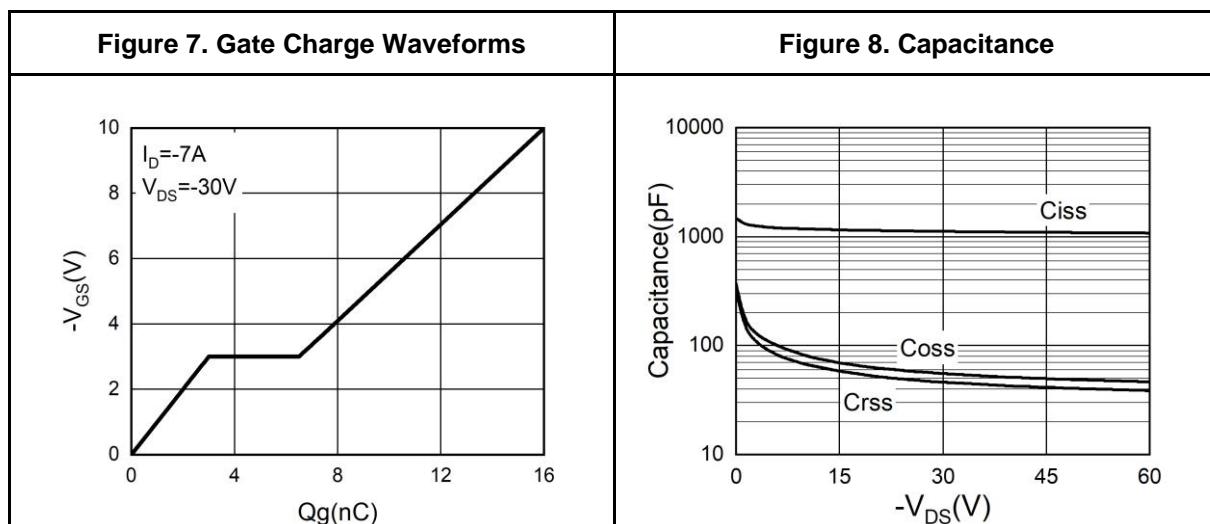
P-Channel Typical Electrical And Thermal Characteristics (Curves)





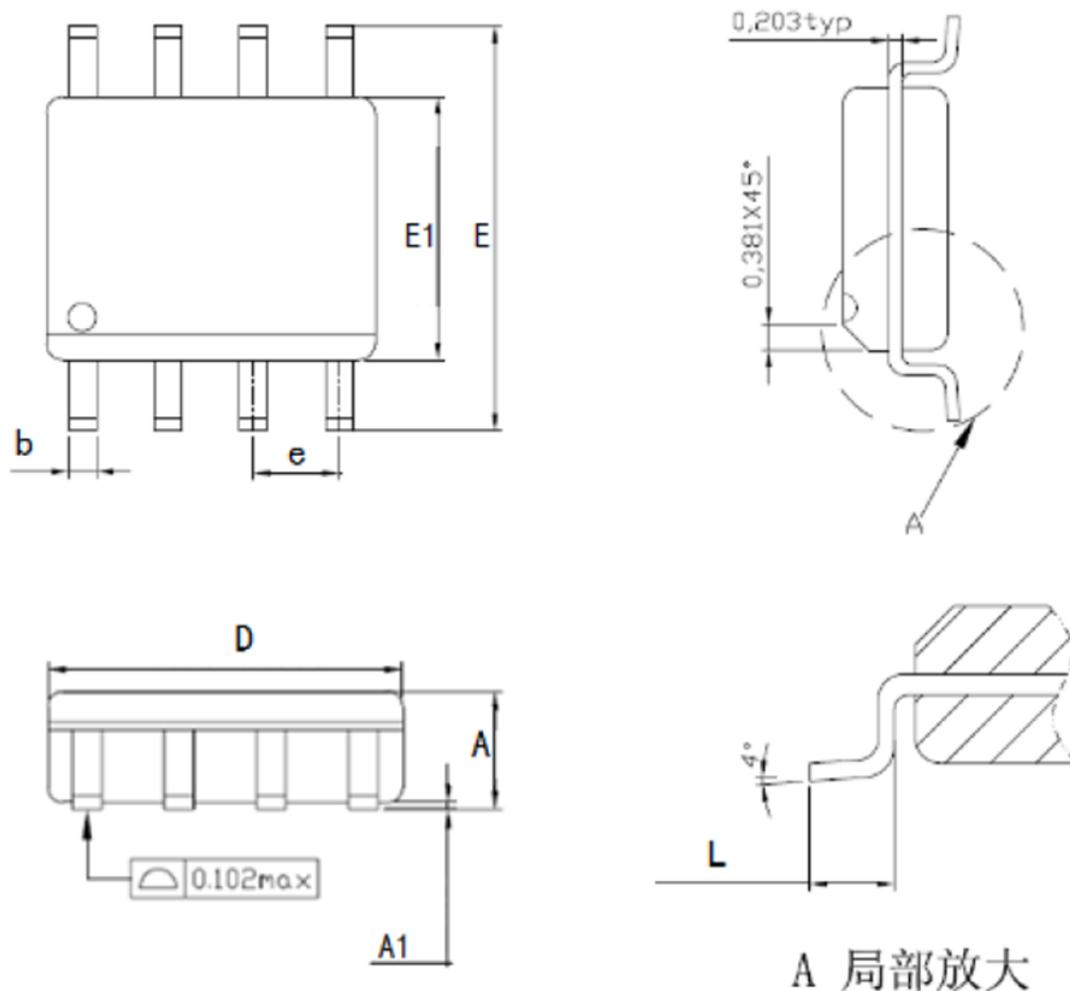
60V N&P-Channel Trench Power MOSFET

P-Channel Typical Electrical And Thermal Characteristics (Curves)





SOP-8 Package Information



Symbol	Dimensions In Millimeters		
	Min.	Nom.	Max
A	1.35	1.55	1.75
A1	0.1	0.15	0.2
b	0.346	0.406	0.466
D	4.8	4.89	4.98
E	5.75	6.00	6.25
E1	3.81	3.90	3.99
e	1.27TYP		
L	0.406	0.838	1.27



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