



## 40V P-Channel Trench Power MOSFET

### General Description

The SJP40P185 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as -4.5V. This device is suitable for use as a wide variety of applications.

### Features

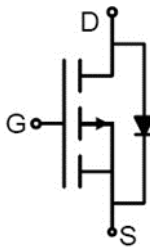
- Low Gate Charge
- 100% UIS Tested, 100% DVDS Tested
- High Power and current handling capability
- Lead free product is acquired

### Application

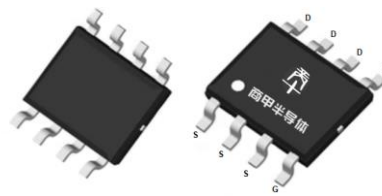
- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

### Key Performance Parametes

Parameter	Value	Unit
$V_{DS}$	-40	V
$R_{DS(ON\_TYP)}$	19.7	mΩ
$I_D$	-8.1	A
$Q_G$	25	nC



Schematic Diagram



SOP-8 top&bottom view



### Package Marking and Ordering Information

Device/Ordering Code	Marking	Package	Packing	Reel Size	Tape width	Quantity
SJP40P185	SJP40P185	SOP-8	Tape	\	\	4000 Pcs

**Table 1. Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$  unless otherwise noted)**

Symbol	Parameter	Limit	Unit
$V_{DS}$	Drain-Source Voltage ( $V_{GS}=0V$ )	-40	V
$V_{GS}$	Gate-Source Voltage ( $V_{DS}=0V$ )	$\pm 20$	V
$I_D$	Drain Current-Continuous( $T_A=25^\circ\text{C}$ )	-8.1	A
	Drain Current-Continuous( $T_A=100^\circ\text{C}$ )	-5.1	A
$I_{DM}$ (pluse)	Drain Current-Continuous@ Current-Pulsed (Note 1)	-32.4	A
$P_D$	Maximum Power Dissipation( $T_A=25^\circ\text{C}$ )	2.9	W
	Maximum Power Dissipation( $T_A=100^\circ\text{C}$ )	1.2	W
$E_{AS}$	Avalanche energy (Note 2)	100	mJ
$T_J, T_{STG}$	Operating Junction and Storage Temperature Range	-55 To 150	$^\circ\text{C}$

**Table 2. Thermal Characteristic**

Symbol	Parameter	Typ	Max	Unit
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient		43	$^\circ\text{C/W}$



## 40V P-Channel Trench Power MOSFET

**Table 3. Electrical Characteristics ( $T_J=25^{\circ}\text{C}$  unless otherwise noted)**

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA	-40			V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =-40V, V <sub>GS</sub> =0V T <sub>J</sub> =25℃			-1	μA
		V <sub>DS</sub> =-40V, V <sub>GS</sub> =0V T <sub>J</sub> =125℃			-100	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V			±100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-1		-2.5	V
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> =-5V, I <sub>D</sub> =-5A		33		S
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance	V <sub>GS</sub> =-10V, I <sub>D</sub> =-5A T <sub>J</sub> =25℃		19.7	25.6	mΩ
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-4A T <sub>J</sub> =25℃		24.6	32.7	mΩ
Dynamic Characteristics						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =-20V,V <sub>GS</sub> =0V, f=1.0MHz		1420		pF
C <sub>oss</sub>	Output Capacitance			129		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			87		pF
R <sub>g</sub>	Gate resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1.0MHz		10		Ω
Switching Parameters						
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>GS</sub> =-10V, V <sub>DS</sub> =-20V, R <sub>L</sub> =4Ω, R <sub>GEN</sub> =3Ω		7.5		nS
t <sub>r</sub>	Turn-on Rise Time			4		nS
t <sub>d(off)</sub>	Turn-Off Delay Time			30		nS
t <sub>f</sub>	Turn-Off Fall Time			6		nS
Q <sub>g</sub>	Total Gate Charge	V <sub>GS</sub> =-10V, V <sub>DS</sub> =-20V, I <sub>D</sub> =-5A		25		nC
Q <sub>gs</sub>	Gate-Source Charge			6.5		nC
Q <sub>gd</sub>	Gate-Drain Charge			3.5		nC
Source-Drain Diode Characteristics						
I <sub>SD</sub>	Source-Drain Current (Body Diode)				-8.1	A
V <sub>SD</sub>	Forward on Voltage (Note 3)	V <sub>GS</sub> =0V, I <sub>S</sub> =-5A			1.2	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>F</sub> =-5A, dI/dt=100A/μs		36		ns
Q <sub>rr</sub>	Reverse Recovery Charge	I <sub>F</sub> =-5A, dI/dt=100A/μs		40		nC

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.

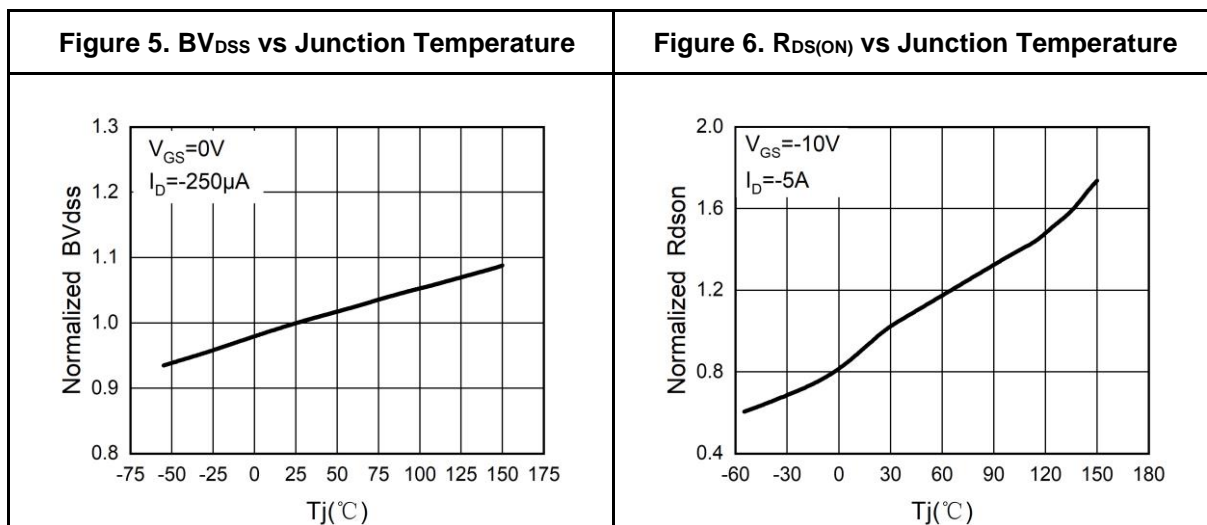
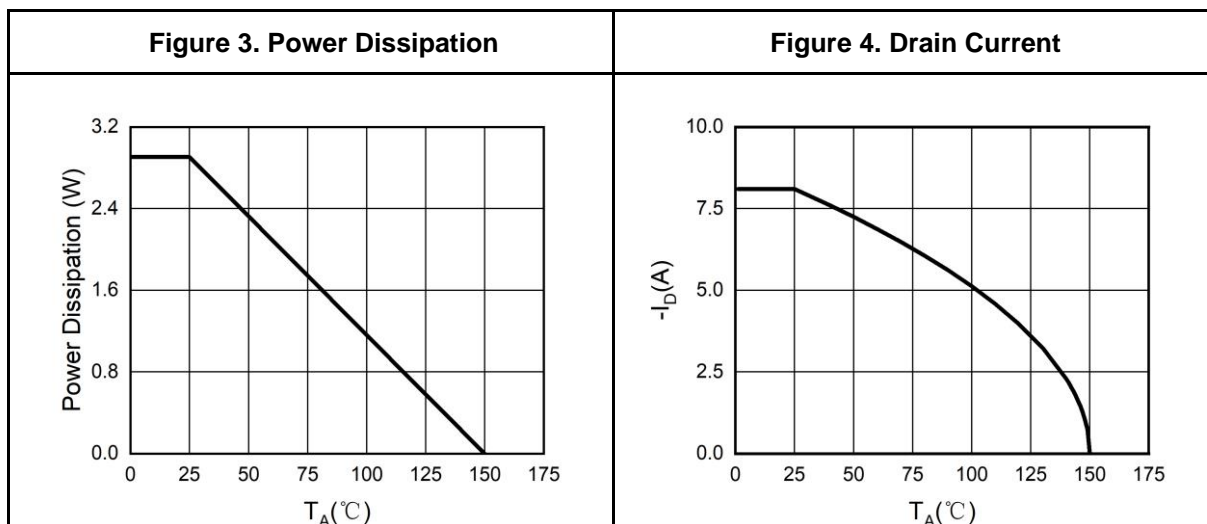
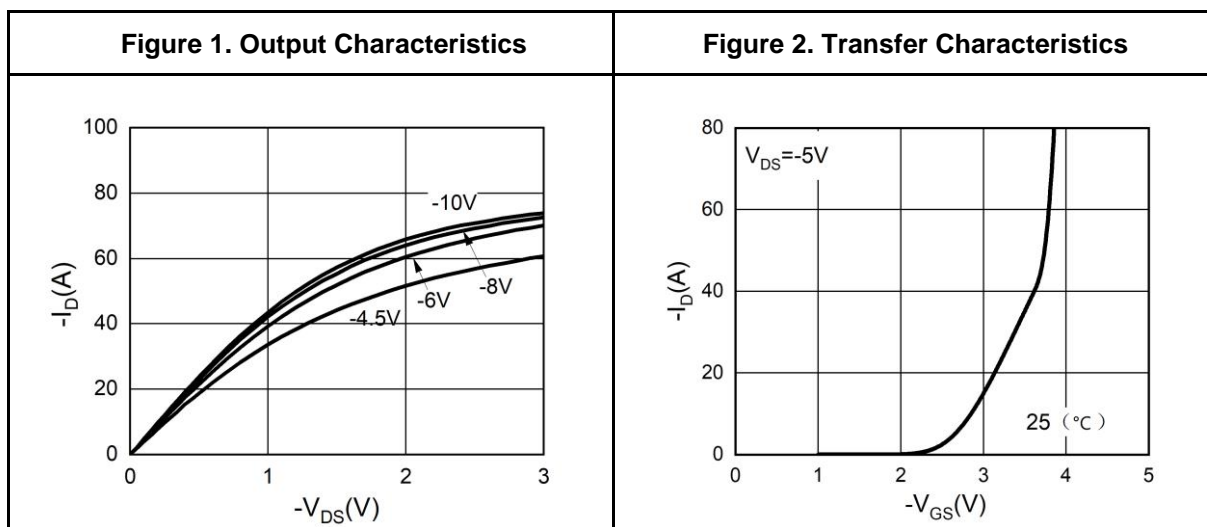
Notes 2.EAS condition:  $T_J=25^{\circ}\text{C}, V_{DD}=-40V, V_G=10V, R_g=25\Omega, L=0.5\text{mH}$ .

Notes 3.Repetitive Rating: Pulse width limited by maximum junction temperature.



## 40V P-Channel Trench Power MOSFET

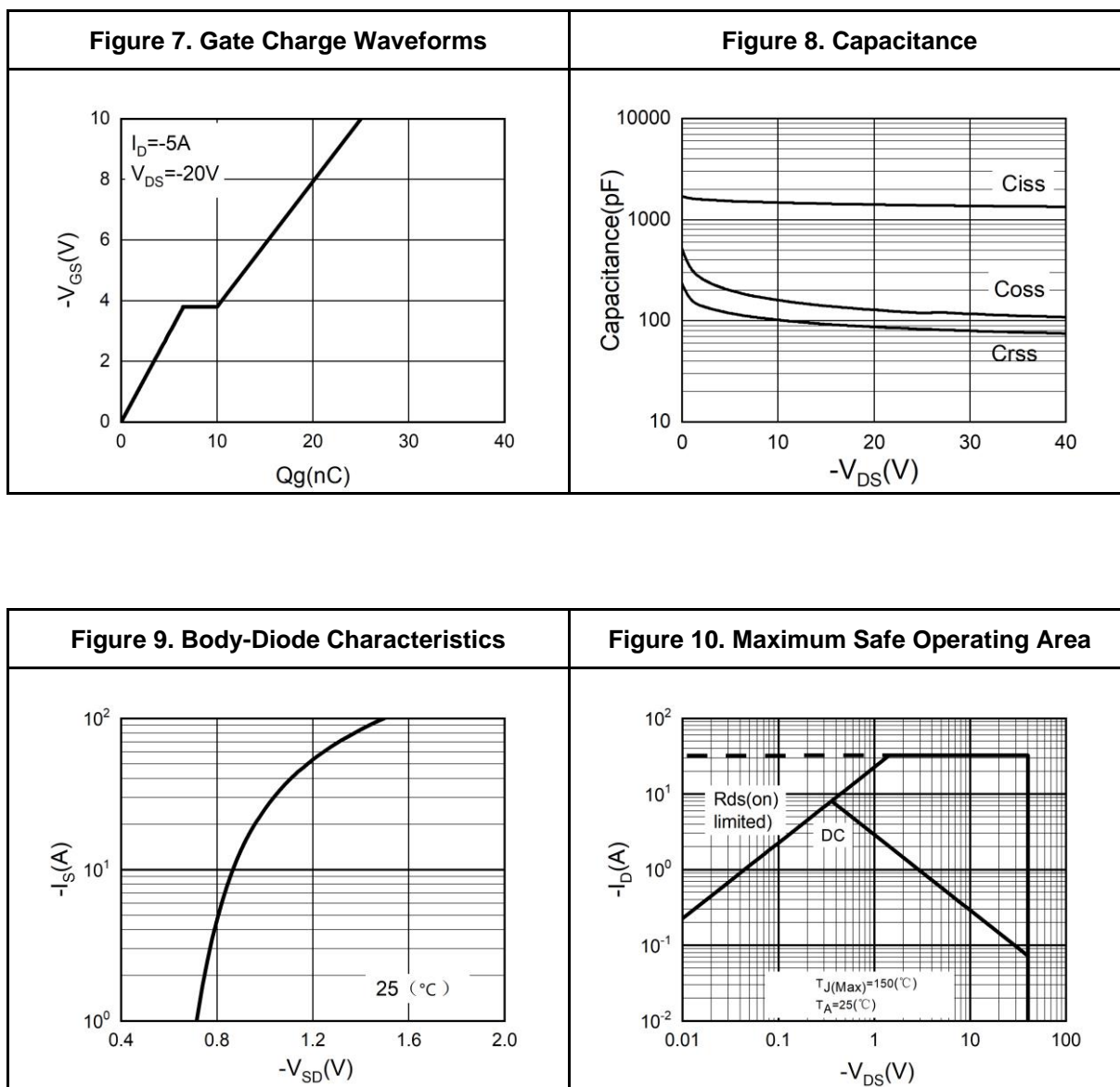
### Typical Electrical And Thermal Characteristics (Curves)





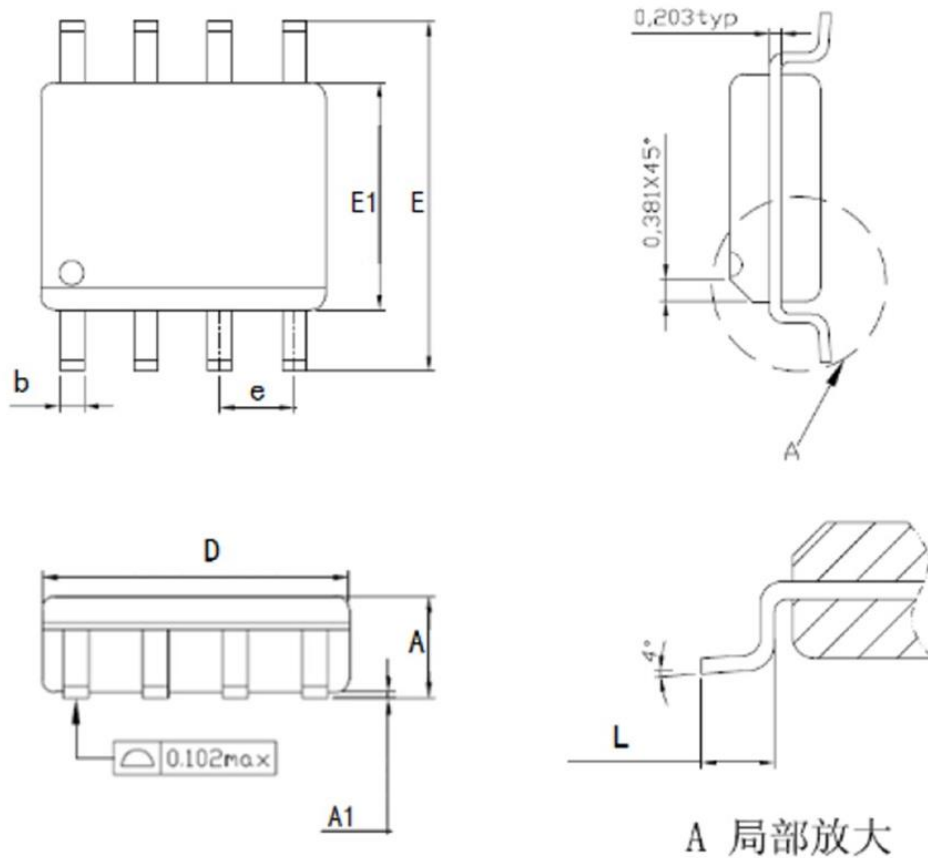
## 40V P-Channel Trench Power MOSFET

### Typical Electrical And Thermal Characteristics (Curves)





## SOP-8 Package Information



Symbol	Dimensions In Millimeters		
	Min.	Nom.	Max
A	1.35	1.55	1.75*
A1	0.1	0.15	0.2
b	0.346	0.406	0.466
D	4.8	4.89	4.98
E	5.75	6.00	6.25
E1	3.81	3.90	3.99
e	1.27TYP		
L	0.406	0.838	1.27



## 40V P-Channel Trench Power MOSFET

---

### Attention

This product described in this document can not be used in life support devices or systems, aircraft's control systems, and other applications whose failure can be reasonably expected to result in serious physical and/or material damage, apart from that when an application agreement is signed between customer and Wuxi Shangjia Semiconductor.

The performances and characteristics of this product in the independent testing state are displayed in this document. Wuxi Shangjia Semiconductor can't guarantee of the performances and characteristics of this described product that mounted in the customer's products or equipments as same as that in the independent testing state. So the customer should evaluate and test devices mounted in the customer's products or equipments.

Wuxi Shangjia Semiconductor reserves the right to improve the designs, functions and reliability of this product and modify any and all information described in this document without notice customer, apart from that when an notice agreement is signed between customer and Wuxi Shangjia Semiconductor.

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Wuxi Shangjia Semiconductor hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.