

General Description

The SJP40NP635 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as $\pm 4.5V$. This device is suitable for use as a wide variety of applications.

Features

- Low Gate Charge
- High Power and current handing capability
- Lead free product is acquired

Application

- Battery Protection
- Power Management
- Load Switch

Key Performance Parametes

Parameter	Value	Value	Unit
V _{DS}	40	-40	V
R _{DS(ON)_TYP}	21.6	46.6	mΩ
lo	6.3	-4.2	А
Q _G	10	11	nC



Schematic Diagram

SOP-8 top&bottom view

Package Marking and Ordering Information

Device/Ordering Code	Marking	Package	Packing	Reel Size	Tape width	Quantity
SJP40NP635	P40NP635	SOP-8	Tape	\	١	4000 Pcs

Table 1. Absolute Maximum Ratings (T_A=25℃ unless otherwise noted)

Symbol	Parameter	N Limit	P Limit	Unit
V _{DS}	Drain-Source Voltage (V _{GS} =0V)	40	-40	V
V _{GS}	Gate-Source Voltage (V _{DS} =0V)	±20	±20	V
	Drain Current-Continuous(T _A =25°C)	6.3	-4.2	А
lo	Drain Current-Continuous(T _A =100 °C)	4	-2.7	А
DM (pluse)	Drain Current-Continuous@ Current-Pulsed (Note 1)	25.2	-16.8	А
P	Maximum Power Dissipation(T _A =25 °C)	1.8	1.8	W
PD	Maximum Power Dissipation(T _A =100 °C)	0.7	0.7	W
T _J , T _{STG}	Operating Junction and Storage Temperature Range	-55 T	o 150	ĉ

Table 2. Thermal Characteristic

Symbol	Parameter		P Max	Unit
R _{0JA}	Thermal Resistance, Junction-to- Ambient		68	°C/W



Table 3. N-Channel Electrical Characteristics (T_J=25 $^{\circ}$ C unless otherwise noted)

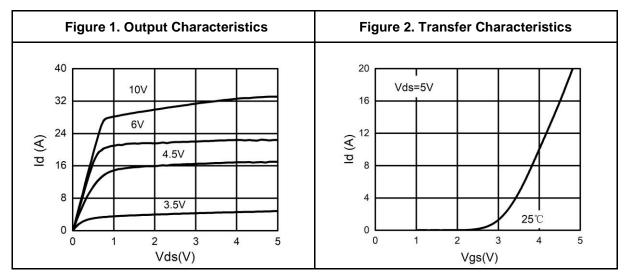
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
On/Off States						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V I _D =250µA	40			V
		V _{DS} =40V, V _{GS} =0V TJ=25℃			1	μA
IDSS	Zero Gate Voltage Drain Current	Voltage Drain Current V _{DS} =40V, V _{GS} =0V T _J =125°C			100	μA
Igss	Gate-Body Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$			±100	nA
$V_{GS(th)}$	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250µA	1.0		2.5	V
gfs	Forward Transconductance	V _{DS} =5V, I _D =5A		6		S
R _{DS(ON)}	Drain-Source On-State Resistance	V _{GS} =10V, I _D =5A T _J =25℃		21.6	27.5	mΩ
RDS(ON)	Drain-Source On-State Resistance	V _{GS} =4.5V, I _D =4A TJ=25℃		23.6	30.7	mΩ
Dynamic Chara	cteristics			•		
Ciss	Input Capacitance	V _{DS} =20V,V _{GS} =0V, f=1.0MHz		777		pF
Coss	Output Capacitance			55		pF
Crss	Reverse Transfer Capacitance			34		pF
Switching Para	meters					
t _{d(on)}	Turn-on Delay Time			5		nS
tr	Turn-on Rise Time	V _{GS} =10V, V _{DS} =20V,		2.5		nS
t _{d(off)}	Turn-Off Delay Time	$R_L=3.3\Omega, R_{GEN}=3\Omega$		18		nS
t _f	Turn-Off Fall Time			2.6		nS
Qg	Total Gate Charge			10		nC
Q _{gs}	Gate-Source Charge	V _{GS} =10V, V _{DS} =20V, I _D =5A		2.7		nC
Q_gd	Gate-Drain Charge			2.6		nC
Source-Drain D	iode Characteristics					
I _{SD}	Source-Drain Current (Body Diode)				6.3	Α
V _{SD}	Forward on Voltage (Note 3)	V _{GS} =0V, I _S =5A			1.2	V

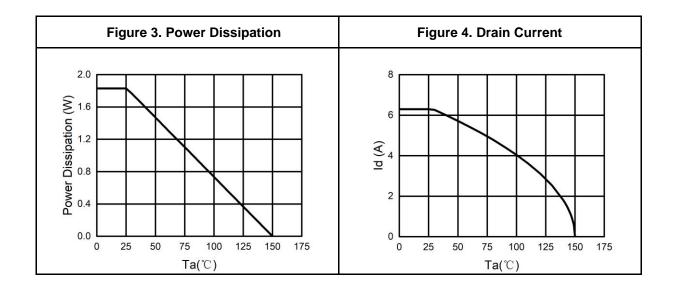
Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature. Notes 2.E_{AS} condition: T_J =25°C, V_{DD} =40V, V_G =10V, Rg=25 Ω , L=0.5mH.

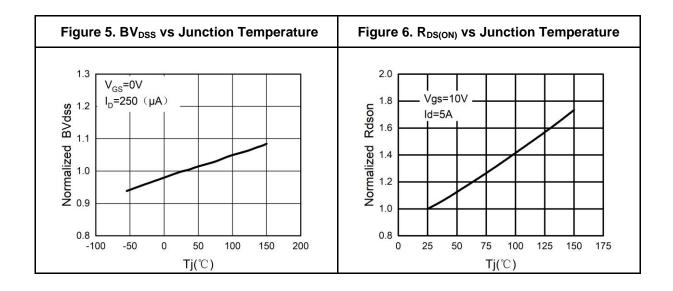
Notes 3.Repetitive Rating: Pulse width limited by maximum junction temperature.



N-Channel Typical Electrical And Thermal Characteristics (Curves)





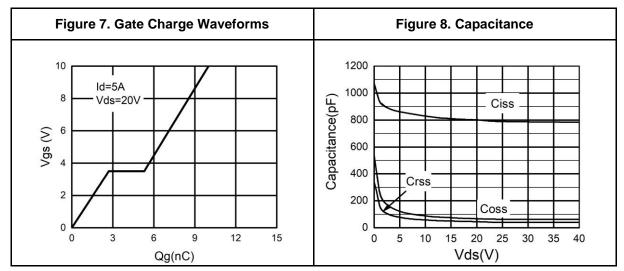




SJP40NP635

40V N&P-Channel Trench Power MOSFET

N-Channel Typical Electrical And Thermal Characteristics (Curves)



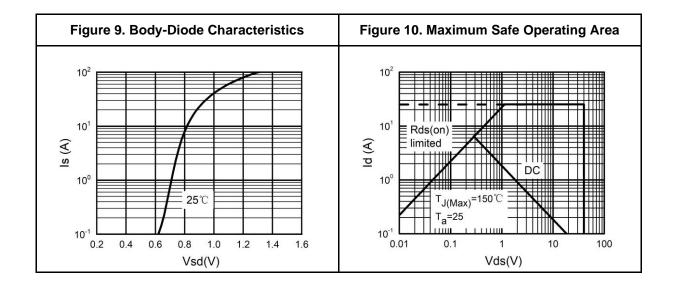




Table 4. P-Channel Electrical Characteristics (TJ=25℃ unless otherwise noted)

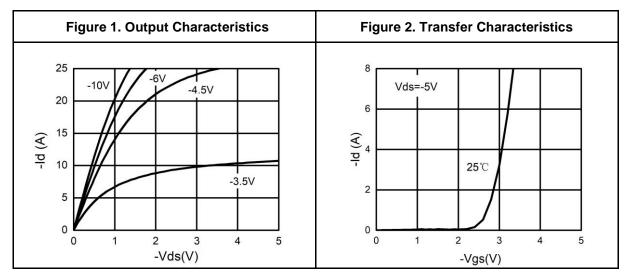
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
On/Off States						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V I _D =-250µA	-40			V
		V _{DS} =-40V, V _{GS} =0V T _J =25℃			-1	μA
IDSS	Zero Gate Voltage Drain Current	V _{DS} =-40V, V _{GS} =0V T _J =125℃			-100	μA
Igss	Gate-Body Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$			±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250µA	-1		-2.5	V
gfs	Forward Transconductance	V _{DS} =-5V, I _D =-4A		8		S
RDS(ON)	Drain-Source On-State Resistance	V _{GS} =-10V, I _D =-4A T _J =25℃		46.6	60	mΩ
RDS(ON)	Drain-Source On-State Resistance	V _{GS} =-4.5V, I _D =-3A T _J =25℃		59	76.7	mΩ
Dynamic Chara	cteristics					
Ciss	Input Capacitance	V _{DS} =-20V,V _{GS} =0V, f=1.0MHz		900		pF
Coss	Output Capacitance			61		pF
Crss	Reverse Transfer Capacitance			45		pF
Switching Para	meters					
t _{d(on)}	Turn-on Delay Time			7.5		nS
tr	Turn-on Rise Time	V _{GS} =-10V, V _{DS} =-20V,		3.5		nS
t _{d(off)}	Turn-Off Delay Time	$R_L=5\Omega, R_{GEN}=3\Omega$		18		nS
t _f	Turn-Off Fall Time			4.5		nS
Qg	Total Gate Charge			11		nC
Q_{gs}	Gate-Source Charge	V _{GS} =-10V, V _{DS} =-20V, I _D =-4A		3.3		nC
Q_gd	Gate-Drain Charge			2.7		nC
Source-Drain D	iode Characteristics	•			-	
I _{SD}	Source-Drain Current (Body Diode)				-4.2	Α
V _{SD}	Forward on Voltage (Note 3)	V _{GS} =0V, I _S =-4A			-1.2	V

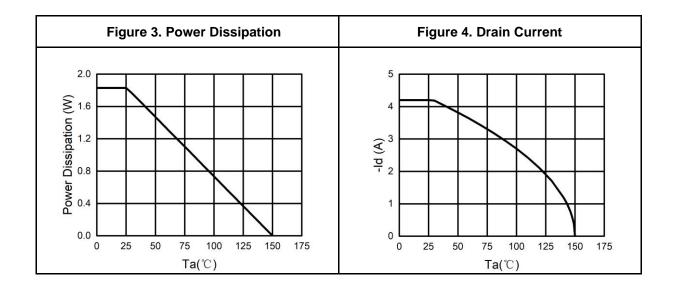
Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature. Notes 2.E_{AS} condition: T_J=25°C,V_{DD}=-40V,V_G=-10V, Rg=25\Omega, L=0.5mH.

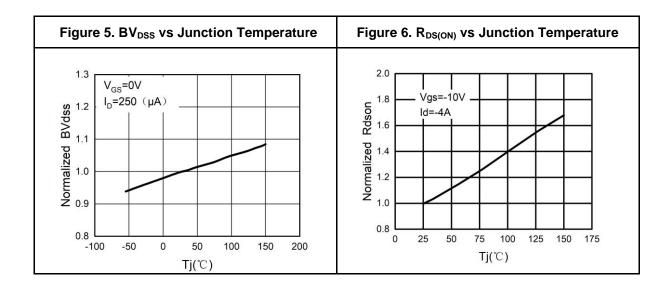
Notes 3.Repetitive Rating: Pulse width limited by maximum junction temperature.



P-Channel Typical Electrical And Thermal Characteristics (Curves)





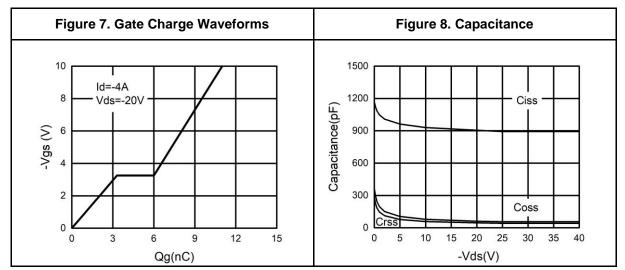


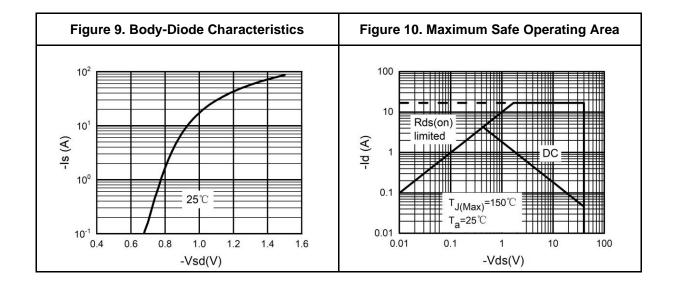


SJP40NP635

40V N&P-Channel Trench Power MOSFET

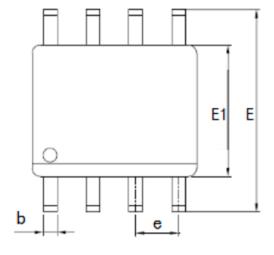
P-Channel Typical Electrical And Thermal Characteristics (Curves)

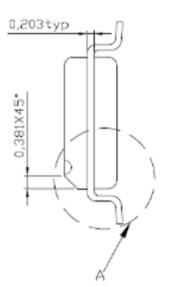


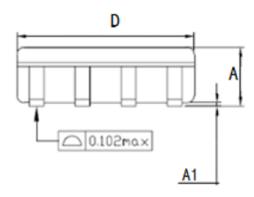


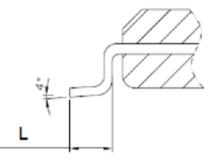


SOP-8 Package Information











Symbol	Dimer		
Symbol	Min.	Nom.	Max
А	1.35	1.55	1.75
A1	0.1	0.15	0.2
b	0.346	0.406	0.466
D	4.8	4.89	4.98
E	5.75	6.00	6.25
E1	3.81	3.90	3.99
е	1.27TYP		
L	0.406	0.838	1.27



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