



40V N-Channel Trench Power MOSFET

General Description

The SJP40ND045 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a wide variety of applications.

Features

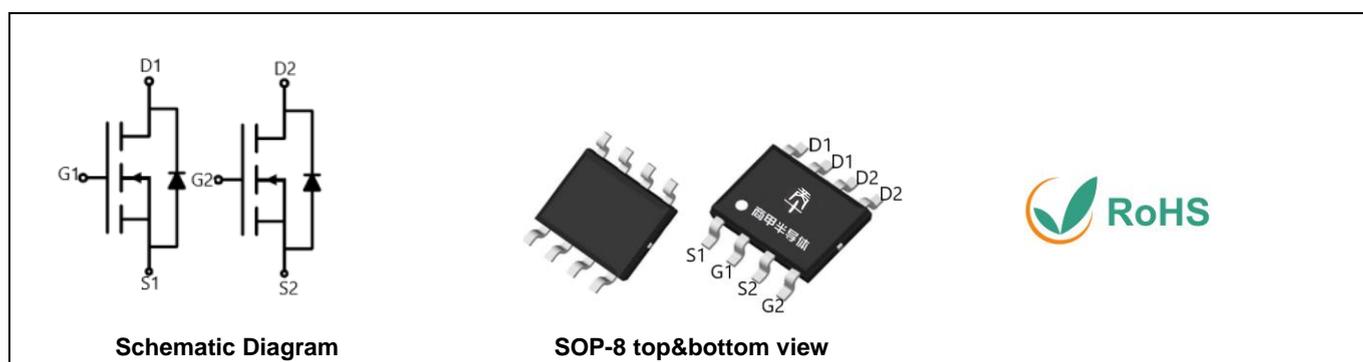
- Low Gate Charge
- 100% UIS Tested, 100% DVDS Tested
- High Power and current handling capability
- Lead free product is acquired

Application

- Load switch
- PMW

Key Performance Parametes

Parameter	Value	Unit
V_{DS}	40	V
$R_{DS(ON_TYP)}$	7.2	m Ω
I_D	16	A
Q_G	55	nC



Package Marking and Ordering Information

Device/Ordering Code	Marking	Package	Packing	Reel Size	Tape width	Quantity
SJP40ND045	SJP40ND045	SOP-8	Tape	\	\	4000 Pcs

Table 1. Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Limit	Unit
V_{DS}	Drain-Source Voltage ($V_{GS}=0V$)	40	V
V_{GS}	Gate-Source Voltage ($V_{DS}=0V$)	± 20	V
I_D	Drain Current-Continuous($T_A=25^\circ\text{C}$)	16	A
	Drain Current-Continuous($T_A=100^\circ\text{C}$)	10	A
I_{DM} (pluse)	Drain Current-Continuous@ Current-Pulsed (Note 1)	64	A
P_D	Maximum Power Dissipation($T_A=25^\circ\text{C}$)	4	W
	Maximum Power Dissipation($T_A=100^\circ\text{C}$)	1.6	W
E_{AS}	Avalanche energy (Note 2)	256	mJ
T_J, T_{STG}	Operating Junction and Storage Temperature Range	-55 To 150	$^\circ\text{C}$

Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient		31	$^\circ\text{C/W}$



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Table 3. Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	40			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=40V, V_{GS}=0V, T_J=25^\circ\text{C}$			1	μA
		$V_{DS}=40V, V_{GS}=0V, T_J=125^\circ\text{C}$			100	μA
I_{GSS}	Gate-Body Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$			± 100	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1.0		2.5	V
g_{FS}	Forward Transconductance	$V_{DS}=5V, I_D=5A$		38		S
$R_{DS(on)}$	Drain-Source On-State Resistance	$V_{GS}=10V, I_D=5A, T_J=25^\circ\text{C}$		7.3	9.5	m Ω
		$V_{GS}=4.5V, I_D=4A, T_J=25^\circ\text{C}$		10.8	14.4	m Ω
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS}=20V, V_{GS}=0V, f=1.0\text{MHz}$		3000		pF
C_{oss}	Output Capacitance			250		pF
C_{rss}	Reverse Transfer Capacitance			170		pF
R_g	Gate resistance	$V_{GS}=0V, V_{DS}=0V, f=1.0\text{MHz}$		0.67		Ω
Switching Parameters						
$t_{d(on)}$	Turn-on Delay Time	$V_{GS}=10V, V_{DS}=20V, R_L=4\Omega, R_{GEN}=3\Omega$		14		nS
t_r	Turn-on Rise Time			8		nS
$t_{d(off)}$	Turn-Off Delay Time			44		nS
t_f	Turn-Off Fall Time			15		nS
Q_g	Total Gate Charge	$V_{GS}=10V, V_{DS}=20V, I_D=5A$		55		nC
Q_{gs}	Gate-Source Charge			8.7		nC
Q_{gd}	Gate-Drain Charge			13.5		nC
Source-Drain Diode Characteristics						
I_{SD}	Source-Drain Current (Body Diode)				16	A
V_{SD}	Forward on Voltage (Note 3)	$V_{GS}=0V, I_S=5A$			1.2	V
t_{rr}	Reverse Recovery Time	$I_F=5A, dI/dt=500A/\mu s$		44		ns
Q_{rr}	Reverse Recovery Charge	$I_F=5A, dI/dt=500A/\mu s$		49		nC

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.

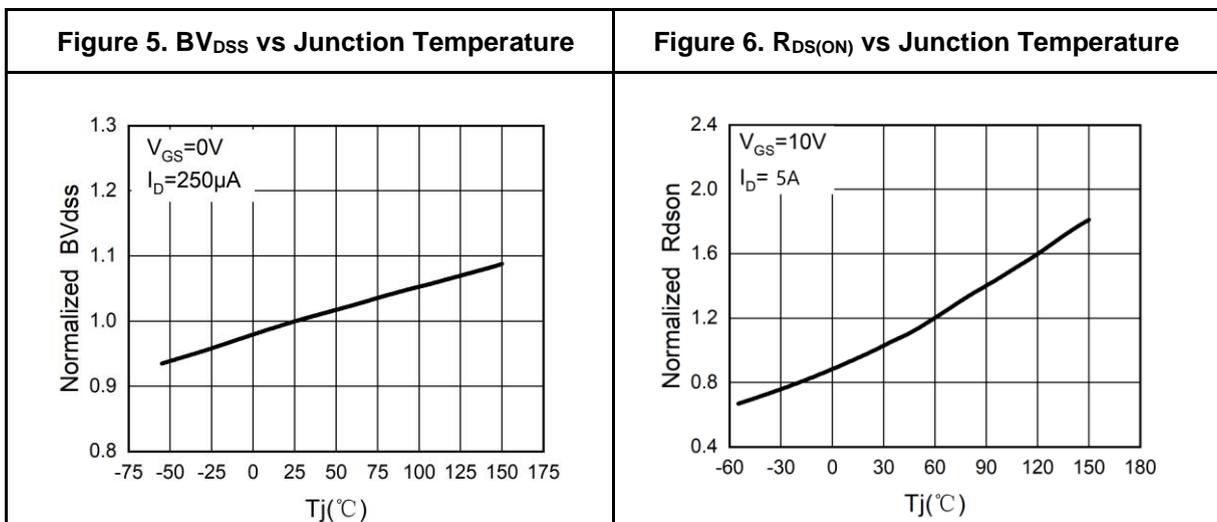
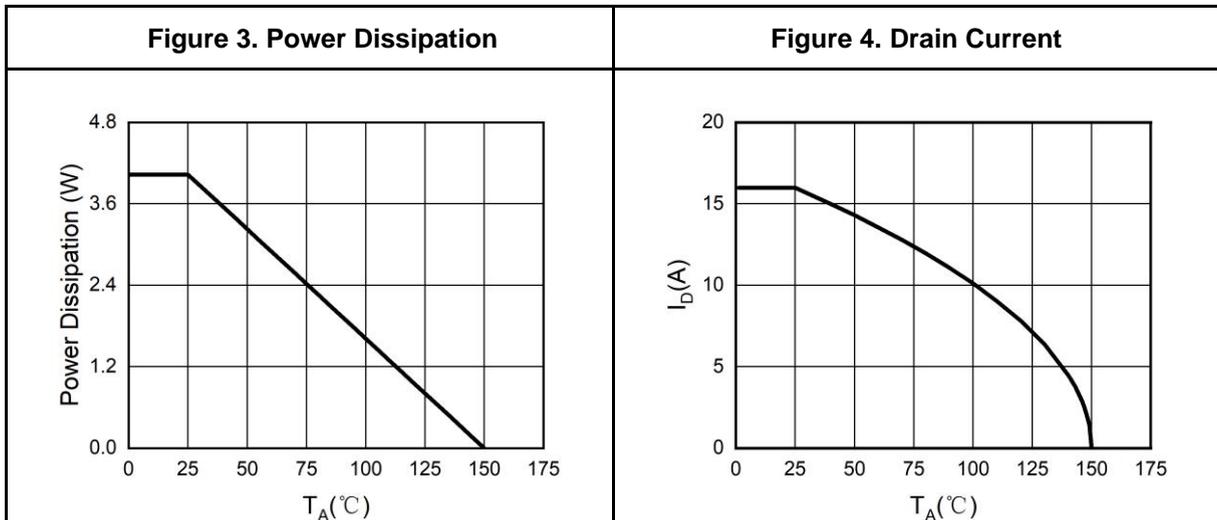
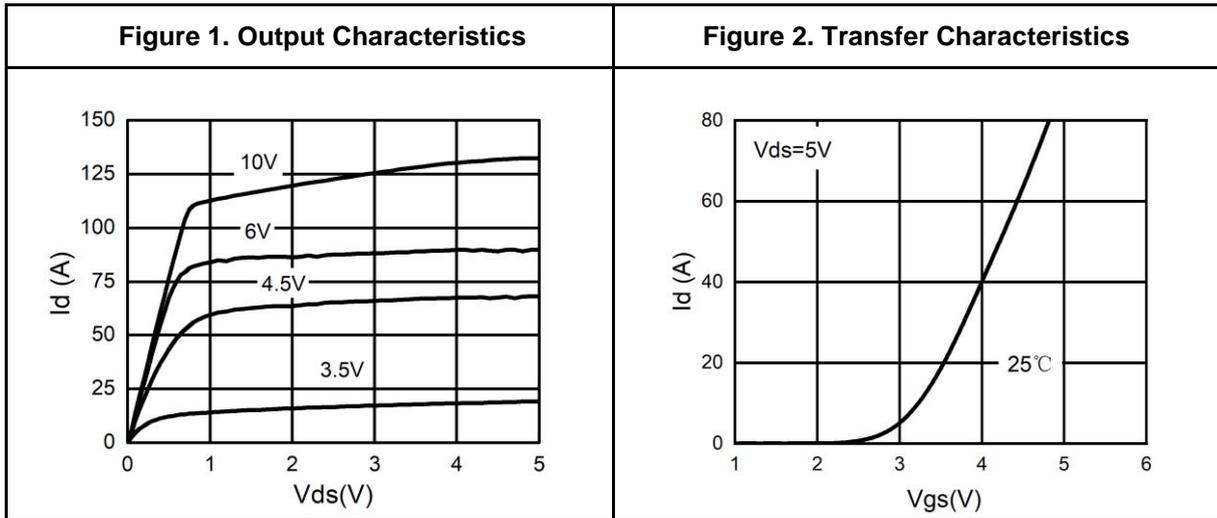
Notes 2. E_{AS} condition: $T_J=25^\circ\text{C}, V_{DD}=40V, V_G=10V, R_g=25\Omega, L=0.5\text{mH}$.

Notes 3.Repetitive Rating: Pulse width limited by maximum junction temperature.



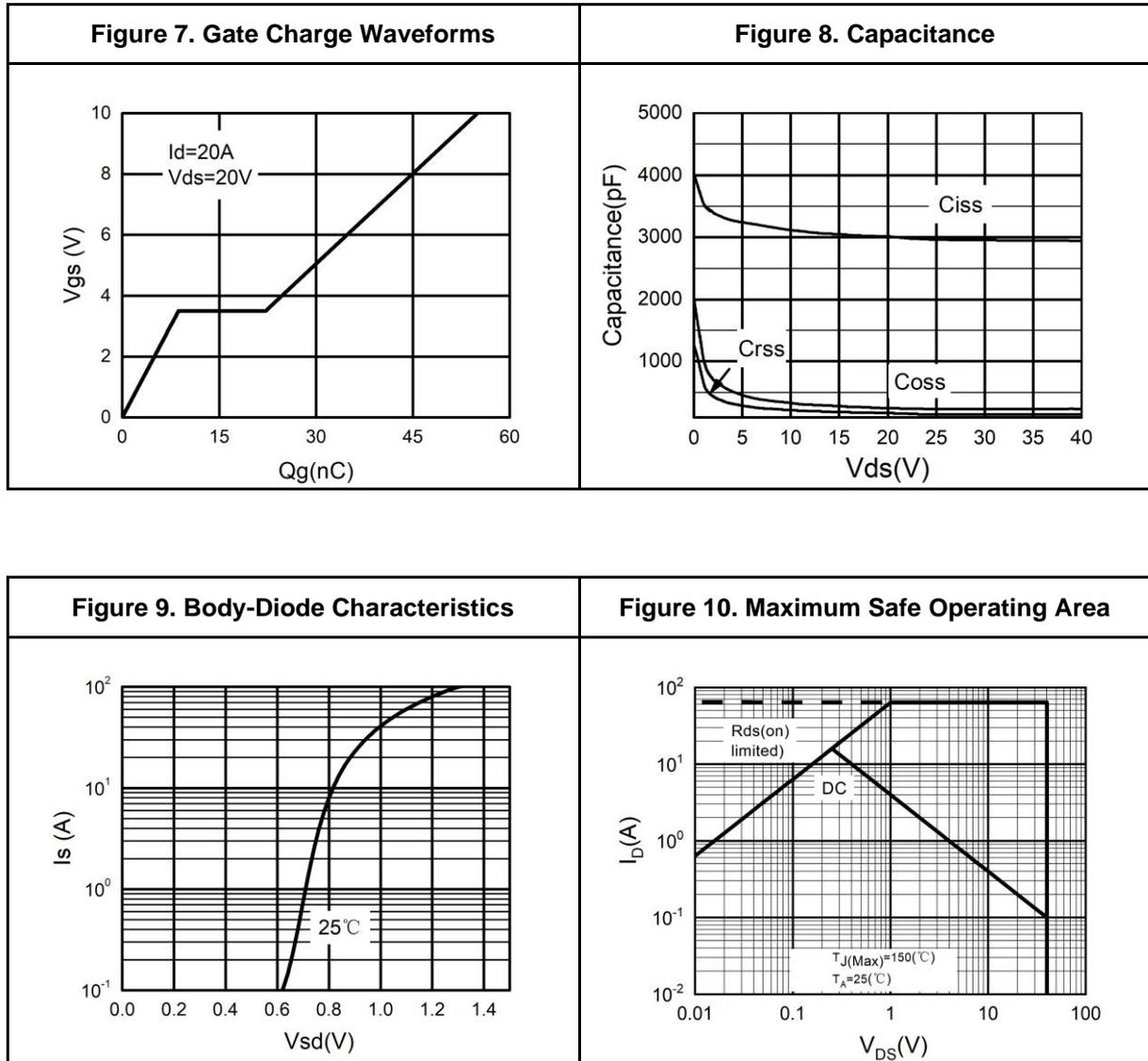
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Typical Electrical And Thermal Characteristics (Curves)



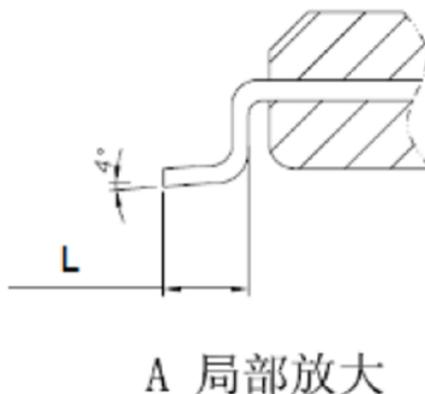
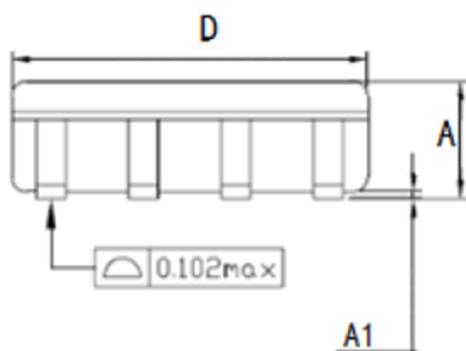
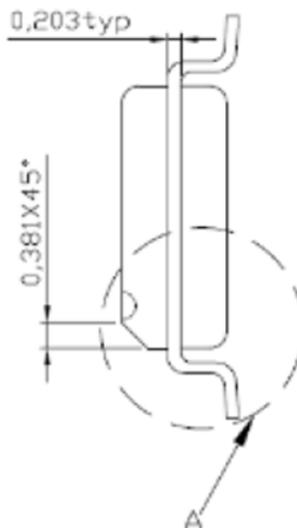
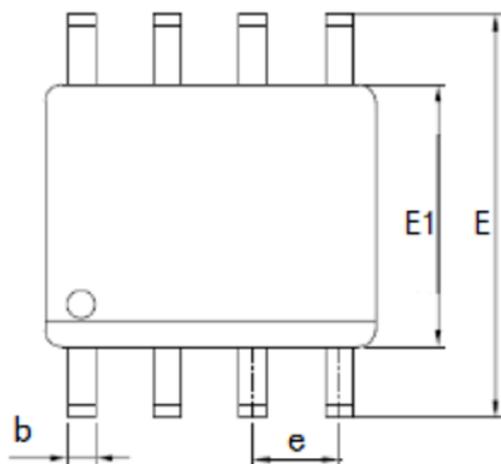


Typical Electrical And Thermal Characteristics (Curves)





SOP-8 Package Information



A 局部放大

Symbol	Dimensions In Millimeters		
	Min.	Nom.	Max
A	1.35	1.55	1.75
A1	0.1	0.15	0.2
b	0.346	0.406	0.466
D	4.8	4.89	4.98
E	5.75	6.00	6.25
E1	3.81	3.90	3.99
e	1.27TYP		
L	0.406	0.838	1.27



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