General Description

The SJP20ND160 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a wide variety of applications.

Features

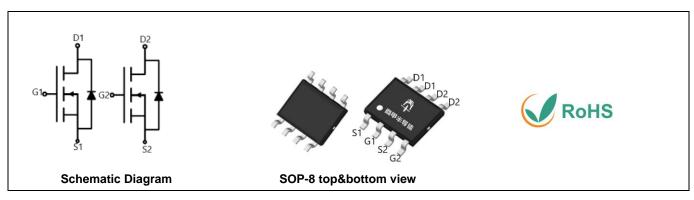
- Low Gate Charge
- 100% UIS Tested, 100% DVDS Tested
- High Power and current handing capability
- Lead free product is acquired

Application

- Load switch
- PMW

Key Performance Parametes

Parameter	Value	Unit
V _{DS}	20	٧
R _{DS(ON)_TYP}	13.2	mΩ
I _D	6	A
Q _G	55	nC



Package Marking and Ordering Information

Device/Ordering Code	Marking	Package	Packing	Reel Size	Tape width	Quantity
SJP20ND160	SJP20ND160	SOP-8	Tape	\	/	4000 Pcs

Table 1. Absolute Maximum Ratings (T_A=25℃ unless otherwise noted)

Symbol	Parameter	Limit	Unit
V _{DS}	Drain-Source Voltage (V _{GS} =0V)	20	V
V _G s	Gate-Source Voltage (V _{DS} =0V)	±12	V
1-	Drain Current-Continuous(T _A =25°C)	6	А
I _D	Drain Current-Continuous(T _A =100℃)	3.8	А
I _{DM} (pluse)	Drain Current-Continuous@ Current-Pulsed (Note 1)	24	А
D	Maximum Power Dissipation(T _A =25°C)	0.9	W
P _D	Maximum Power Dissipation(T _A =100℃)	0.3	W
Eas	Avalanche energy (Note 2)	256	mJ
TJ, TSTG	Operating Junction and Storage Temperature Range	-55 To 150	C

Table 2. Thermal Characteristic

Symbol	Parameter	Тур	Max	Unit
$R_{ heta JA}$	R _{0JA} Thermal Resistance, Junction-to-Ambient		144	°C/W



Table 3. Electrical Characteristics (T_J=25℃ unless otherwise noted)

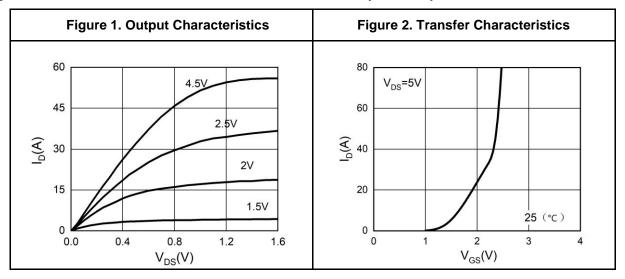
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
On/Off States						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V I _D =250μA	20			V
	Zero Gate Voltage Drain Current	V _{DS} =20V, V _{GS} =0V T _J =25℃			1	μΑ
IDSS		V _{DS} =20V, V _{GS} =0V T _J =125°C			100	μΑ
Igss	Gate-Body Leakage Current	V _{GS} =±12V, V _{DS} =0V			±100	nA
$V_{GS(th)}$	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	0.5		1	V
g FS	Forward Transconductance	V _{DS} =5V, I _D =1.5A		6.5		S
Г.	D : 0 0 0 1 D : 1	V _{GS} =1.5V, I _D =1.5A T _J =25℃		13.2	16.5	mΩ
RDS(ON)	Drain-Source On-State Resistance	V _{GS} =2.5V, I _D =1A T _J =25℃		16.7	22.2	mΩ
Dynamic Chara	cteristics			•		
Ciss	Input Capacitance	V _{DS} =10V,V _{GS} =0V, f=1.0MHz		568		pF
Coss	Output Capacitance			68		pF
Crss	Reverse Transfer Capacitance			60		pF
Rg	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1.0MHz		9		Ω
Switching Parar	meters			•		
t _{d(on)}	Turn-on Delay Time			14		nS
tr	Turn-on Rise Time	V _{GS} =4.5V, V _{DS} =10V,		8		nS
t _{d(off)}	Turn-Off Delay Time	$R_L=6.7\Omega$, $R_{GEN}=3\Omega$		44		nS
t f	Turn-Off Fall Time			15		nS
Qg	Total Gate Charge			55		nC
Q _{gs}	Gate-Source Charge	V _{GS} =4.5V, V _{DS} =10V, I _D =1.5A		8.7		nC
Q_{gd}	Gate-Drain Charge			13.5		nC
Source-Drain Di	iode Characteristics					
I _{SD}	Source-Drain Current (Body Diode)				6	Α
V _{SD}	Forward on Voltage (Note 3)	V _{GS} =0V, I _S =1.5A			1.2	V
t _{rr}	Reverse Recovery Time	I _F =1.5A, dI/dt=500A/μs		44		ns
Q_{rr}	Reverse Recovery Charge	I _F =1.5A, dI/dt=500A/μs		49		nC

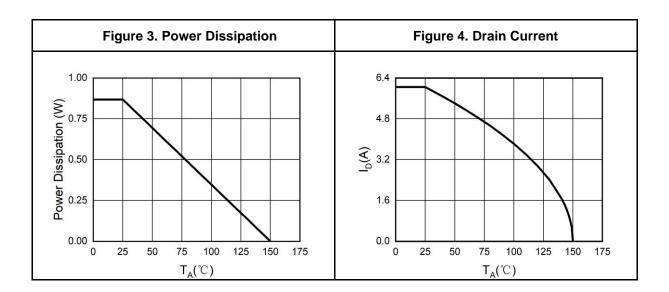
Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.

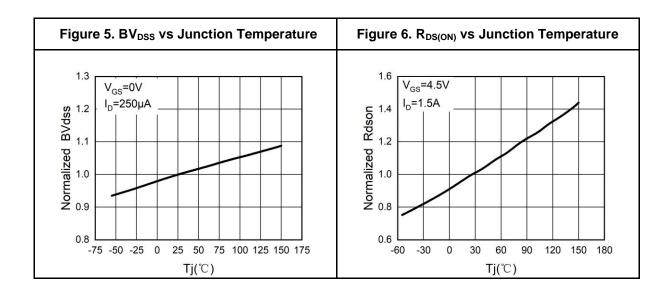
Notes 2.E_{AS} condition: T_J =25 °C, V_{DD} =20V, V_G =10V, Rg=25 Ω , L=0.5mH.

Notes 3.Repetitive Rating: Pulse width limited by maximum junction temperature.

Typical Electrical And Thermal Characteristics (Curves)

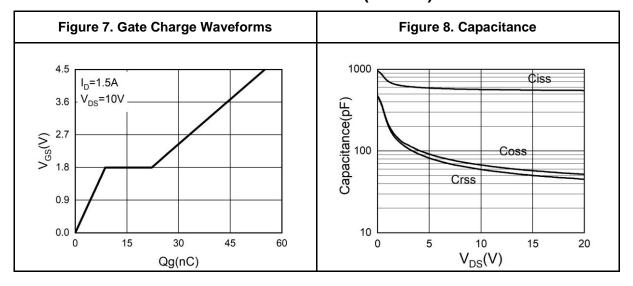


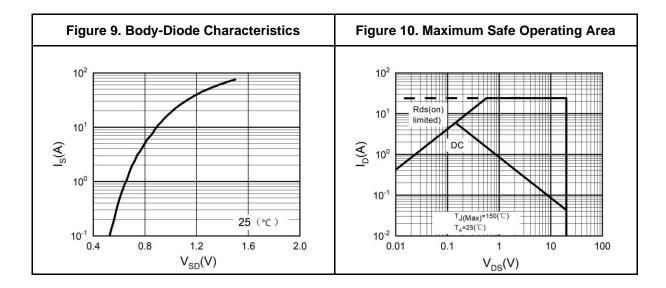






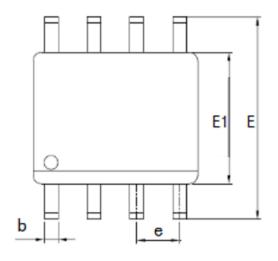
Typical Electrical And Thermal Characteristics (Curves)

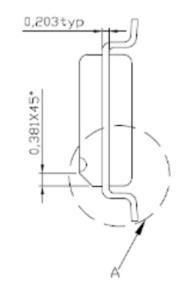


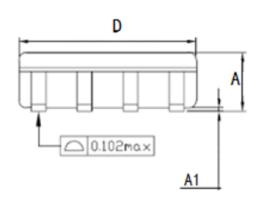




SOP-8 Package Information









Symbol	Dimer			
Symbol	Min.	Nom.	Max	
Α	1.35	1.55	1.75	
A1	0.1	0.15	0.2	
b	0.346	0.406	0.466	
D	4.8	4.89	4.98	
Е	5.75	6.00	6.25	
E1	3.81	3.90	3.99	
е	1.27TYP			
L	0.406	0.838	1.27	



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