



60V N-Channel Trench Power MOSFET

General Description

The SJM60N075 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a wide variety of applications.

Features

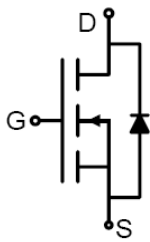
- Low Gate Charge
- High Power and current handing capability
- Lead free product is acquired

Application

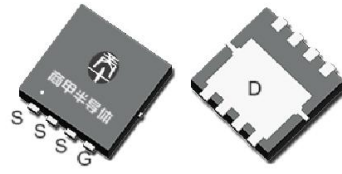
- 48V E-bike controller
- Uninterruptible power supply
- Hard switched and high frequency circuits

Key Performance Parametes

Parameter	Value	Unit
V_{DS}	60	V
$R_{DS(ON_TYP)}$	7.6	mΩ
I_D	47	A
Q_G	18	nC



Schematic Diagram



PDFN3X3-8L top&bottom view



Package Marking and Ordering Information

Device/Ordering Code	Marking	Package	Packing	Reel Size	Tape width	Quantity
SJM60N075	SJM60N075	PDFN3X3-8L	Tape	\	\	5000 Pcs

Table 1. Absolute Maximum Ratings ($T_C=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Limit	Unit
V_{DS}	Drain-Source Voltage ($V_{GS}=0V$)	60	V
V_{GS}	Gate-Source Voltage ($V_{DS}=0V$)	± 20	V
I_D	Drain Current-Continuous($T_C=25^\circ\text{C}$)	47	A
	Drain Current-Continuous($T_C=100^\circ\text{C}$)	30	A
I_{DM} (pluse)	Drain Current-Continuous@ Current-Pulsed (Note 1)	188	A
P_D	Maximum Power Dissipation($T_C=25^\circ\text{C}$)	47	W
	Maximum Power Dissipation($T_C=100^\circ\text{C}$)	19	W
E_{AS}	Avalanche energy (Note 2)	256	mJ
T_J, T_{STG}	Operating Junction and Storage Temperature Range	-55 To 150	$^\circ\text{C}$

Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case		2.65	$^\circ\text{C/W}$



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Table 3. Electrical Characteristics ($T_J=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V I _D =250μA	60			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =60V, V _{GS} =0V T _J =25℃			1	μA
		V _{DS} =60V, V _{GS} =0V T _J =125℃			100	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V			±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	1		2.5	V
g _{FS}	Forward Transconductance	V _{DS} =5V, I _D =20A		45.9		S
R _{DS(ON)}	Drain-Source On-State Resistance	V _{GS} =10V, I _D =20A T _J =25℃		7.6	9.8	mΩ
R _{DS(ON)}	Drain-Source On-State Resistance	V _{GS} =4.5V, I _D =20A T _J =25℃		8.9	11.8	mΩ
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =30V,V _{GS} =0V, f=1.0MHz		2065		pF
C _{oss}	Output Capacitance			255		pF
C _{rss}	Reverse Transfer Capacitance			149		pF
R _g	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1.0MHz		1.6		Ω
Switching Parameters						
t _{d(on)}	Turn-on Delay Time	V _{GS} =10V, V _{DS} =30V, R _L =1.5Ω, R _{GEN} =6Ω		5		nS
t _r	Turn-on Rise Time			7		nS
t _{d(off)}	Turn-Off Delay Time			24		nS
t _f	Turn-Off Fall Time			9		nS
Q _g	Total Gate Charge	V _{GS} =10V, V _{DS} =30V, I _D =20A		18		nC
Q _{gs}	Gate-Source Charge			3		nC
Q _{gd}	Gate-Drain Charge			3		nC
Source-Drain Diode Characteristics						
I _{SD}	Source-Drain Current (Body Diode)				47	A
V _{SD}	Forward on Voltage (Note 3)	V _{GS} =0V, I _S =20A			1.2	V
t _{rr}	Reverse Recovery Time	I _F =20A, dI/dt=100A/μs		26		ns
Q _{rr}	Reverse Recovery Charge	I _F =20A, dI/dt=100A/μs		14		nC

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.

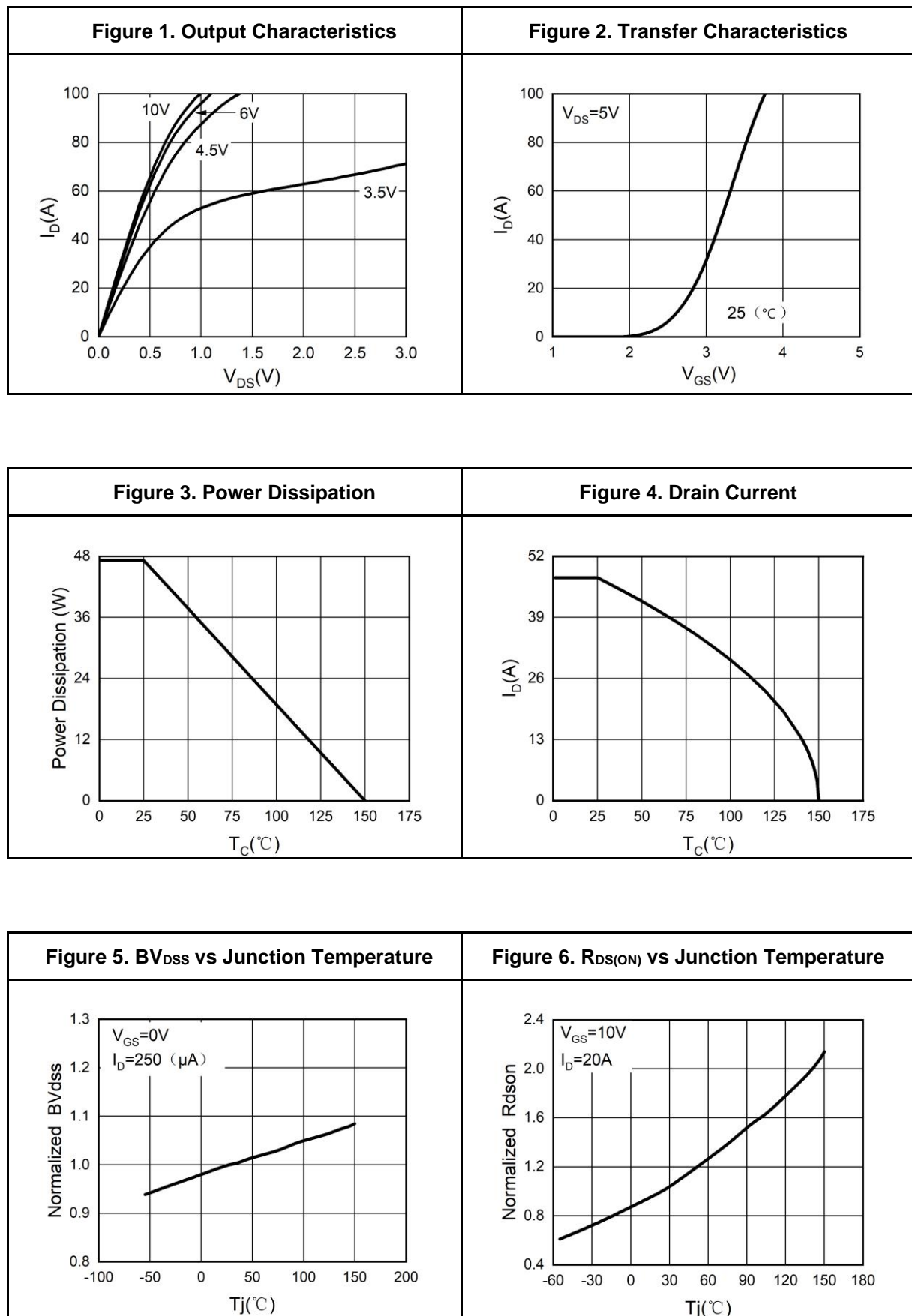
Notes 2.EAS condition: $T_J=25^{\circ}\text{C}, V_{DD}=40V, V_G=10V, R_g=25\Omega, L=0.5\text{mH}$.

Notes 3.Repetitive Rating: Pulse width limited by maximum junction temperature.



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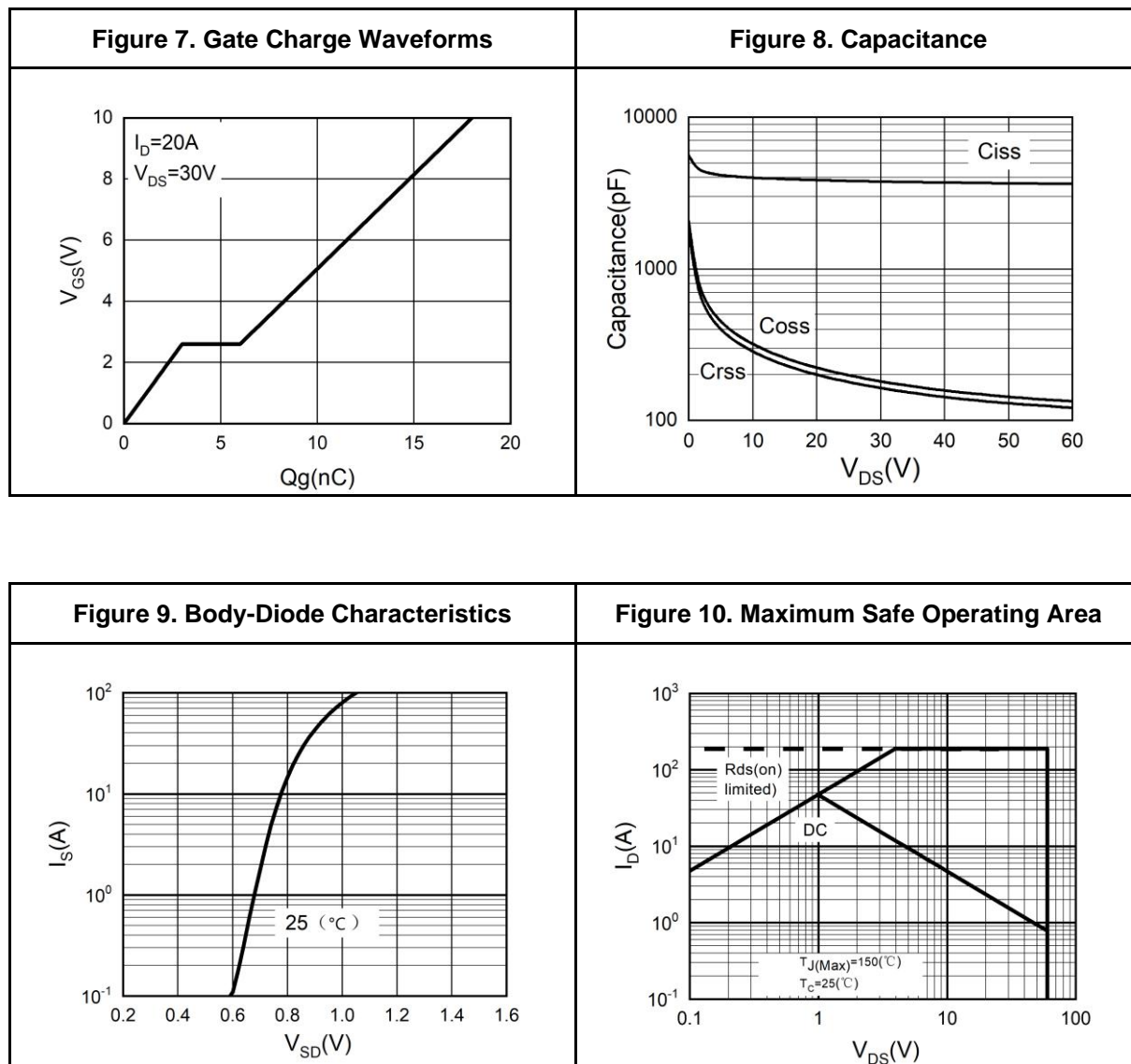
Typical Electrical And Thermal Characteristics (Curves)





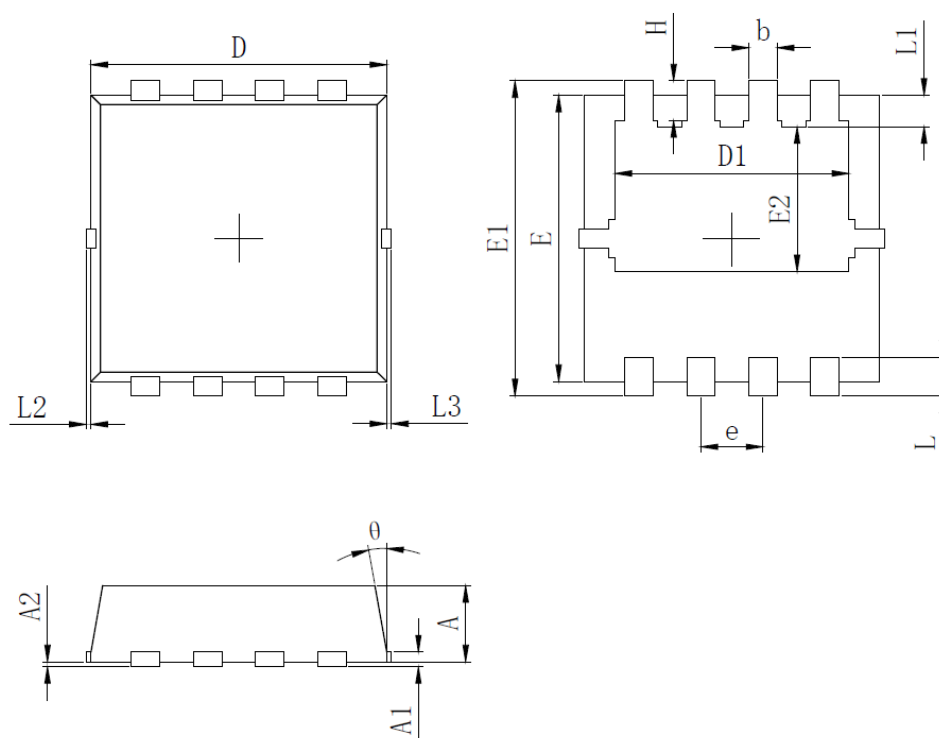
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Typical Electrical And Thermal Characteristics (Curves)





PDFN3X3-8L Package Information



SYMBOL	MILLIMETER		
	MIN	Typ.	MAX
A	0.700	0.800	0.900
A1	0.152 REF.		
A2	0~0.05		
D	3.000	3.100	3.200
D1	2.300	2.450	2.600
E	2.900	3.000	3.100
E1	3.150	3.300	3.450
E2	1.320	1.520	1.720
b	0.200	0.300	0.400
e	0.550	0.650	0.750
L	0.300	0.400	0.500
L1	0.180	0.330	0.480
L2	0~0.100		
L3	0~0.100		
H	0.315	0.415	0.515
θ	8°	10°	12°



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