40V P-Channel Trench Power MOSFET

General Description

The SJM40P085 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as -4.5V. This device is suitable for use as a wide variety of applications.

Features

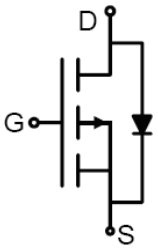
- Low Gate Charge
- High Power and current handing capability
- Lead free product is acquired

Application

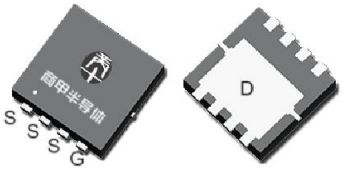
- PWM Applications
- Load Switch
- Power Management

Key Performance Parametes

Parameter	Value	Unit
V_{DS}	-40	V
$R_{DS(ON)\_TYP}$	10.7	mΩ
I_D	-42	A
Q_G	60	nC



Schematic Diagram



PDFN3X3-8L top&bottom view

RoHS

Package Marking and Ordering Information

Device/Ordering Code	Marking	Package	Packing	Reel Size	Tape width	Quantity
SJM40P085	40P085	PDFN3X3-8L	Tape	\	\	5000 Pcs

Table 1. Absolute Maximum Ratings (T_C=25℃ unless otherwise noted)

Symbol	Parameter	Limit	Unit
V_{DS}	Drain-Source Voltage (V_{GS}=0V)	-40	V
V_{GS}	Gate-Source Voltage (V_{DS}=0V)	±20	V
I_D	Drain Current-Continuous(T_C=25℃)	-42	A
	Drain Current-Continuous(T_C=100℃)	-26	A
$I_{DM\ (pluse)}$	Drain Current-Continuous@ Current-Pulsed (Note 1)	-168	A
P_D	Maximum Power Dissipation(T_C=25℃)	42	W
	Maximum Power Dissipation(T_C=100℃)	17	W
E_{AS}	Avalanche energy (Note 2)	272	mJ
T_J, T_{STG}	Operating Junction and Storage Temperature Range	-55 To 150	℃

Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case		3	℃/W

Table 3. Electrical Characteristics (T_J=25℃ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V I_D=-250μA	-40			V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=-40V, V_{GS}=0V T_J=25℃			-1	μA
		V_{DS}=-40V, V_{GS}=0V T_J=125℃			-100	μA
I_{GSS}	Gate-Body Leakage Current	V_{GS}=±20V, V_{DS}=0V			±100	nA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_D=-250μA	-1		-2.5	V
g_{FS}	Forward Transconductance	V_{DS}=-5V, I_D=-10A		31		S
$R_{DS(ON)}$	Drain-Source On-State Resistance	V_{GS}=-10V, I_D=-20A T_J=25℃		10.7	13.9	mΩ
$R_{DS(ON)}$	Drain-Source On-State Resistance	V_{GS}=-4.5V, I_D=-20A T_J=25℃		14	18.5	mΩ
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{DS}=-20V,V_{GS}=0V, f=1.0MHz		3241		pF
C_{oss}	Output Capacitance			228		pF
C_{rss}	Reverse Transfer Capacitance			205		pF
R_g	Gate resistance	V_{GS}=0V, V_{DS}=0V, f=1.0MHz		4.5		Ω
Switching Parameters						
$t_{d(on)}$	Turn-on Delay Time	V_{GS}=-10V, V_{DS}=-20V, R_L=2Ω, R_{GEN}=3Ω		18		nS
t_r	Turn-on Rise Time			4.8		nS
$t_{d(off)}$	Turn-Off Delay Time			88.8		nS
t_f	Turn-Off Fall Time			26.4		nS
Q_g	Total Gate Charge	V_{GS}=-10V, V_{DS}=-20V, I_D=-10A		60		nC
Q_{gs}	Gate-Source Charge			8.6		nC
Q_{gd}	Gate-Drain Charge			13.9		nC
Source-Drain Diode Characteristics						
I_{SD}	Source-Drain Current (Body Diode)				-42	A
V_{SD}	Forward on Voltage (Note 3)	V_{GS}=0V, I_S=-10A			-1.2	V
t_{rr}	Reverse Recovery Time	I_F=-10A, dI/dt=-100A/μs		17.3		ns
Q_{rr}	Reverse Recovery Charge	I_F=-10A, dI/dt=-100A/μs		9.5		nC

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.
Notes 2.E_{AS} condition: T_J=25℃,V_{DD}=-40V,V_G=-10V, Rg=25Ω, L=0.5mH.
Notes 3.Repetitive Rating: Pulse width limited by maximum junction temperature.

Typical Electrical And Thermal Characteristics (Curves)

Figure 1. Output Characteristics

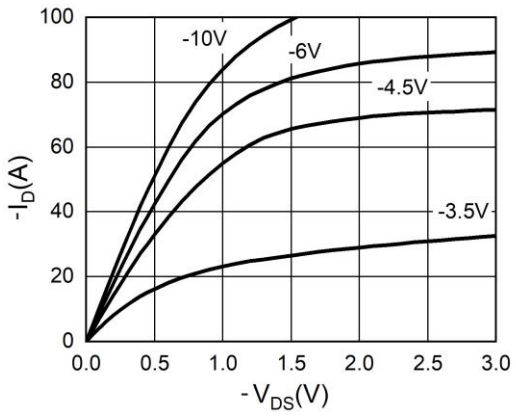


Figure 2. Transfer Characteristics

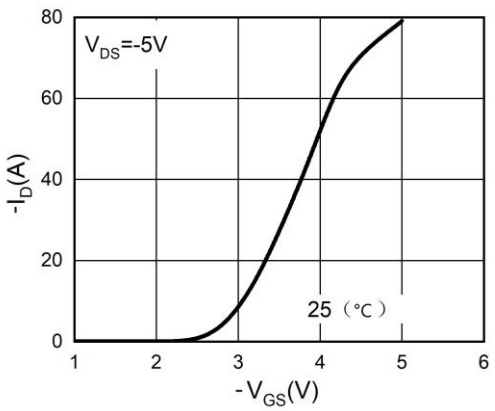


Figure 3. Power Dissipation

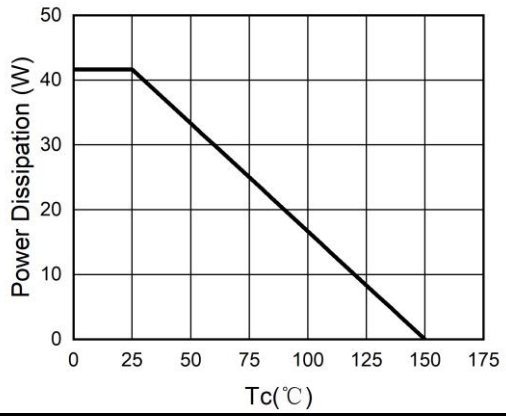


Figure 4. Drain Current

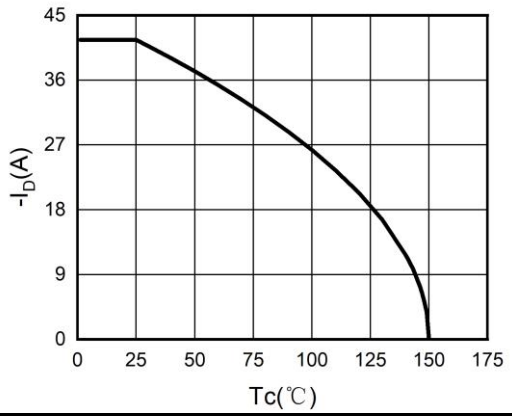


Figure 5. BV_{DSS} vs Junction Temperature

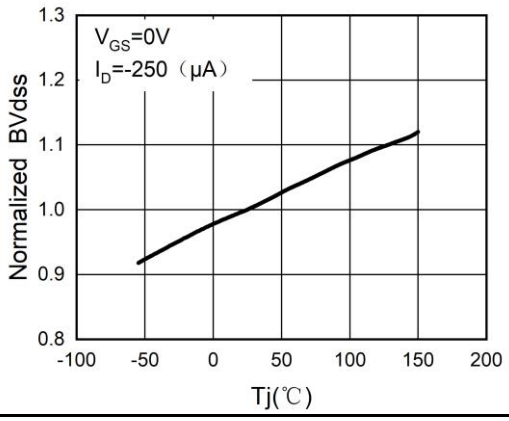
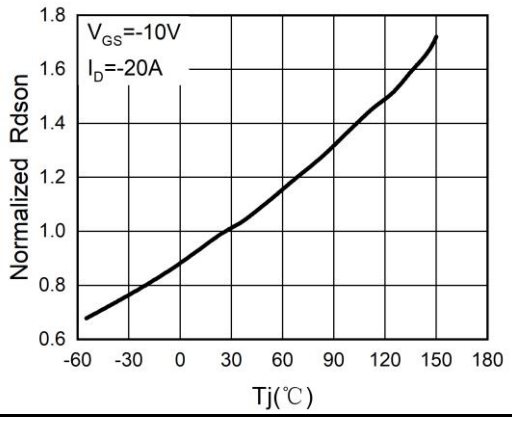


Figure 6. $R_{DS(ON)}$ vs Junction Temperature



Typical Electrical And Thermal Characteristics (Curves)

Figure 7. Gate Charge Waveforms

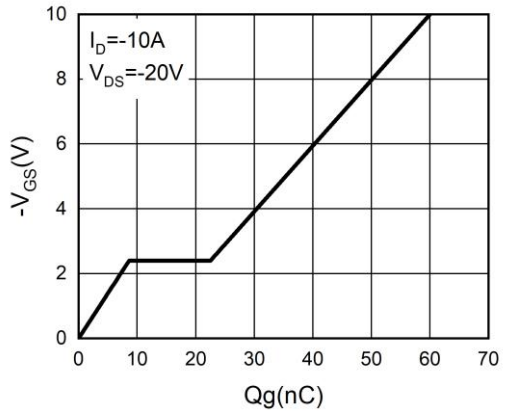


Figure 8. Capacitance

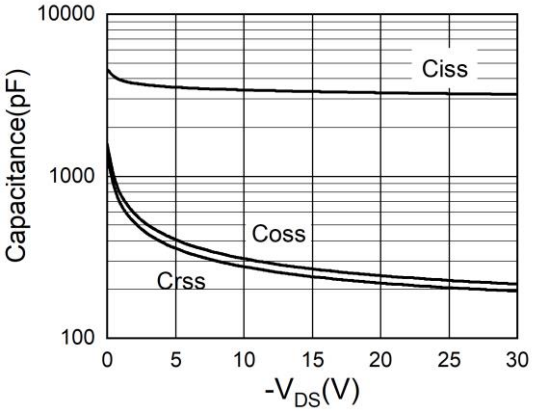


Figure 9. Body-Diode Characteristics

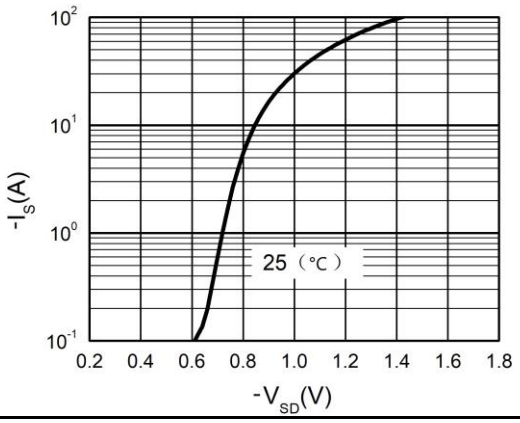
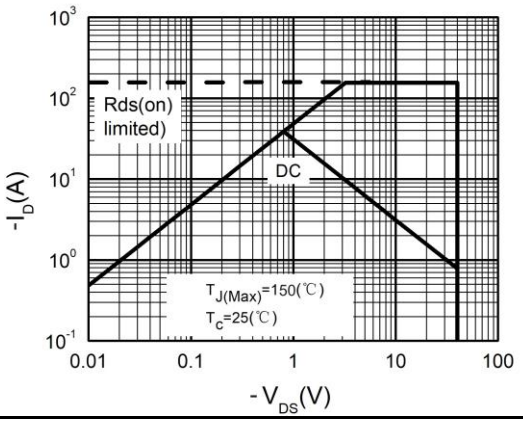
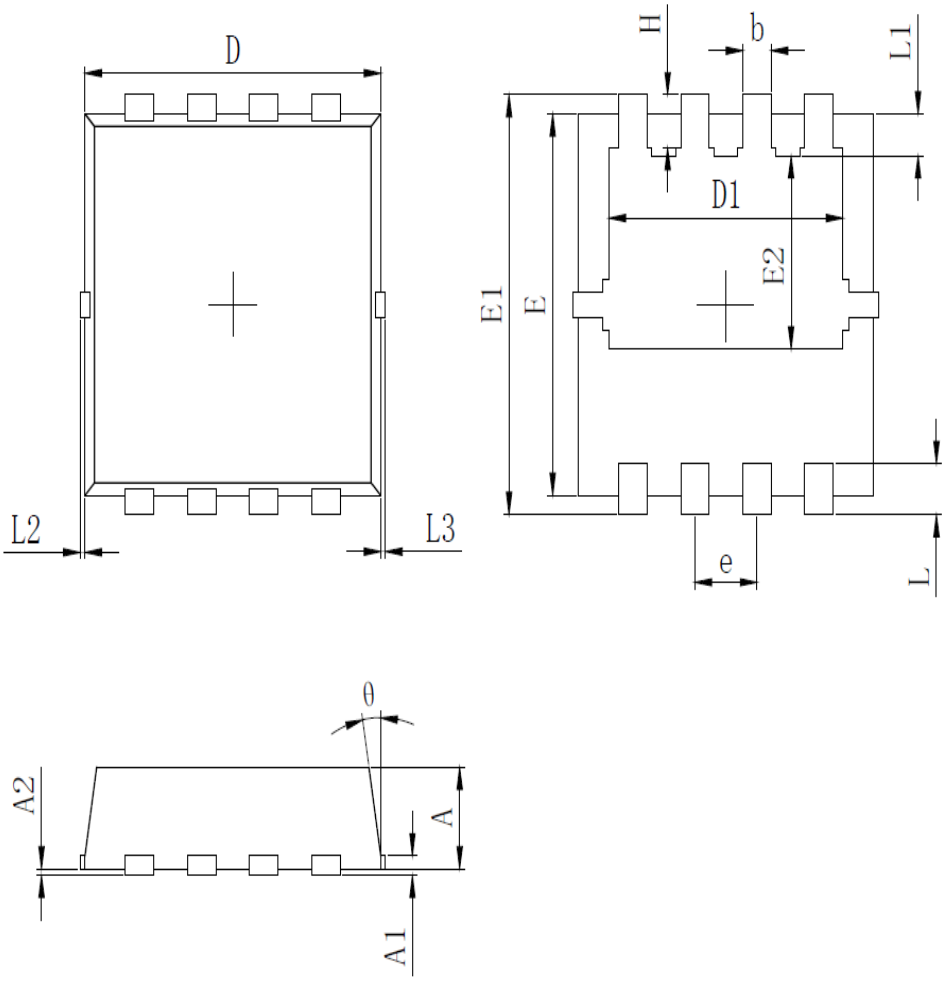


Figure 10. Maximum Safe Operating Area



PDFN3X3 Package Information



SYMBOL	MILLIMETER		
	MIN	Typ.	MAX
A	0.700	0.800	0.900
A1	0.152 REF.		
A2	0~0.05		
D	3.000	3.100	3.200
D1	2.300	2.450	2.600
E	2.900	3.000	3.100
E1	3.150	3.300	3.450
E2	1.320	1.520	1.720
b	0.200	0.300	0.400
e	0.550	0.650	0.750
L	0.300	0.400	0.500
L1	0.180	0.330	0.480
L2	0~0.100		
L3	0~0.100		
H	0.315	0.415	0.515
θ	8°	10°	12°

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