40V N-Channel Trench Power MOSFET

General Description

The SJM40N045 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a wide variety of applications.

Features

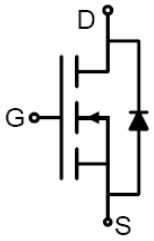
- Low Gate Charge
- 100% UIS Tested, 100% DVDS Tested
- High Power and current handing capability
- Lead free product is acquired

Application

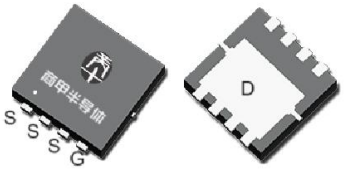
- Load switch
- PMW

Key Performance Parametes

Parameter	Value	Unit
V_{DS}	40	V
$R_{DS(ON)\_TYP}$	3.9	mΩ
I_D	70	A
Q_G	55	nC



Schematic Diagram



PDFN3X3-8L top&bottom view

RoHS

Package Marking and Ordering Information

Device/Ordering Code	Marking	Package	Packing	Reel Size	Tape width	Quantity
SJM40N045	SJM40N045	PDFN3X3-8L	Tape	\	\	5000 Pcs

Table 1. Absolute Maximum Ratings (T_c=25℃ unless otherwise noted)

Symbol	Parameter	Limit	Unit
V_{DS}	Drain-Source Voltage (V_{GS}=0V)	40	V
V_{GS}	Gate-Source Voltage (V_{DS}=0V)	±20	V
I_D	Drain Current-Continuous(T_C=25℃)	70	A
	Drain Current-Continuous(T_C=100℃)	44	A
$I_{DM\ (pluse)}$	Drain Current-Continuous@ Current-Pulsed (Note 1)	280	A
P_D	Maximum Power Dissipation(T_C=25℃)	42	W
	Maximum Power Dissipation(T_C=100℃)	17	W
E_{AS}	Avalanche energy (Note 2)	256	mJ
T_J, T_{STG}	Operating Junction and Storage Temperature Range	-55 To 150	℃

Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case		3	℃/W

Table 3. Electrical Characteristics (T_J=25℃ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V I_D=250μA	40			V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=40V, V_{GS}=0V T_J=25℃			1	μA
		V_{DS}=40V, V_{GS}=0V T_J=125℃			100	μA
I_{GSS}	Gate-Body Leakage Current	V_{GS}=±20V, V_{DS}=0V			±100	nA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_D=250μA	1.0		2.5	V
g_{FS}	Forward Transconductance	V_{DS}=5V, I_D=20A		38		S
$R_{DS(ON)}$	Drain-Source On-State Resistance	V_{GS}=10V, I_D=20A T_J=25℃		3.9	5.1	mΩ
		V_{GS}=4.5V, I_D=20A T_J=25℃		7	9.3	mΩ
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{DS}=20V,V_{GS}=0V, f=1.0MHz		3000		pF
C_{oss}	Output Capacitance			250		pF
C_{rss}	Reverse Transfer Capacitance			170		pF
Rg	Gate resistance	V_{GS}=0V, V_{DS}=0V, f=1.0MHz		0.67		Ω
Switching Parameters						
$t_{d(on)}$	Turn-on Delay Time	V_{GS}=10V, V_{DS}=20V, R_L=1Ω, R_{GEN}=3Ω		14		nS
t_r	Turn-on Rise Time			8		nS
$t_{d(off)}$	Turn-Off Delay Time			44		nS
t_f	Turn-Off Fall Time			15		nS
Q_g	Total Gate Charge	V_{GS}=10V, V_{DS}=20V, I_D=20A		55		nC
Q_{gs}	Gate-Source Charge			8.7		nC
Q_{gd}	Gate-Drain Charge			13.5		nC
Source-Drain Diode Characteristics						
I_{SD}	Source-Drain Current (Body Diode)				70	A
V_{SD}	Forward on Voltage (Note 3)	V_{GS}=0V, I_S=20A			1.2	V
t_{rr}	Reverse Recovery Time	I_F=20A, dI/dt=500A/μs		44		ns
Q_{rr}	Reverse Recovery Charge	I_F=20A, dI/dt=500A/μs		49		nC

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.
Notes 2.E_{AS} condition: T_J=25℃,V_{DD}=40V,V_G=10V, Rg=25Ω, L=0.5mH.
Notes 3.Repetitive Rating: Pulse width limited by maximum junction temperature.

Typical Electrical And Thermal Characteristics (Curves)

Figure 1. Output Characteristics

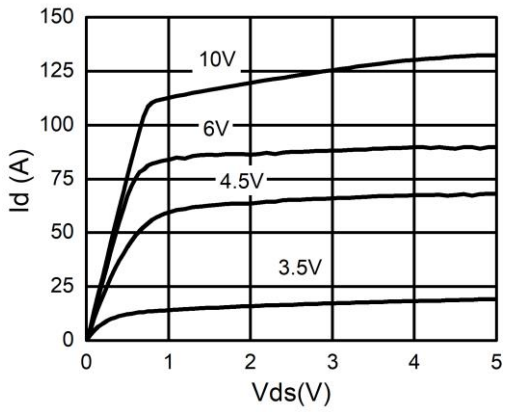


Figure 2. Transfer Characteristics

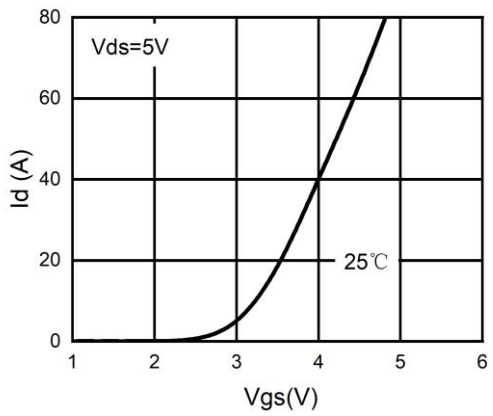


Figure 3. Power Dissipation

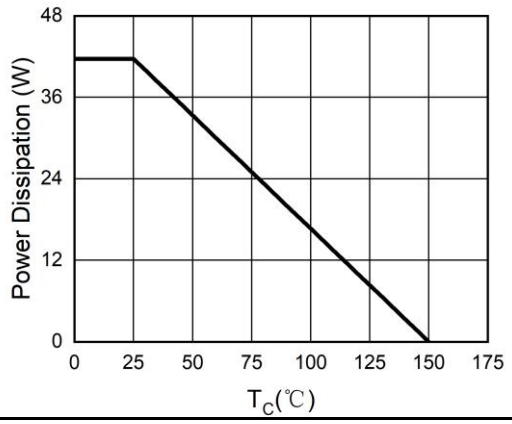


Figure 4. Drain Current

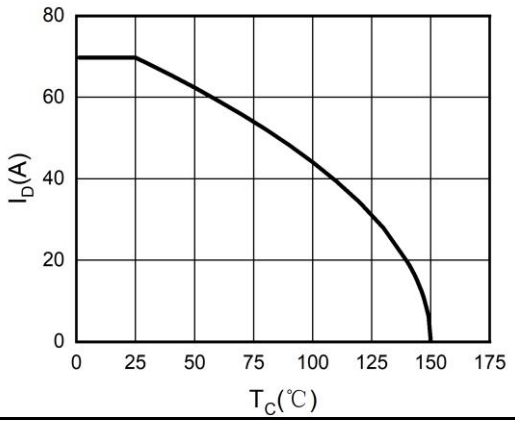


Figure 5. BV_{DSS} vs Junction Temperature

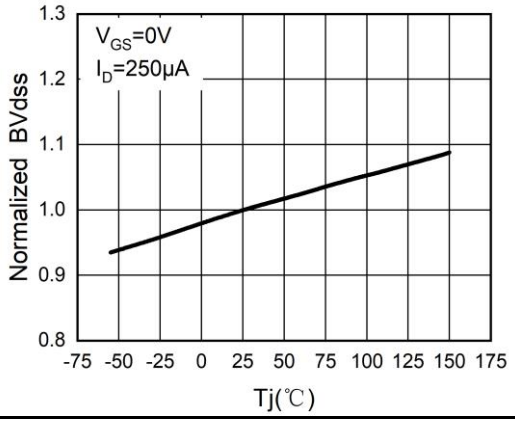
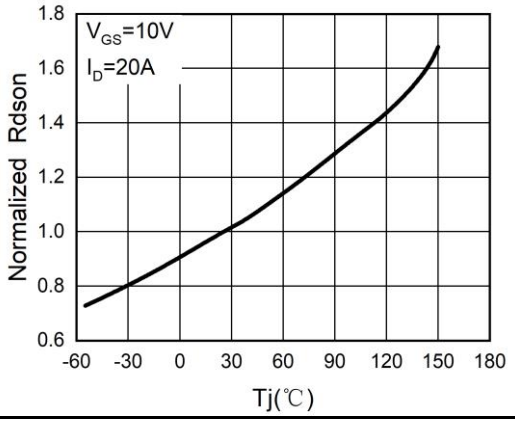


Figure 6. $R_{DS(ON)}$ vs Junction Temperature



Typical Electrical And Thermal Characteristics (Curves)

Figure 7. Gate Charge Waveforms

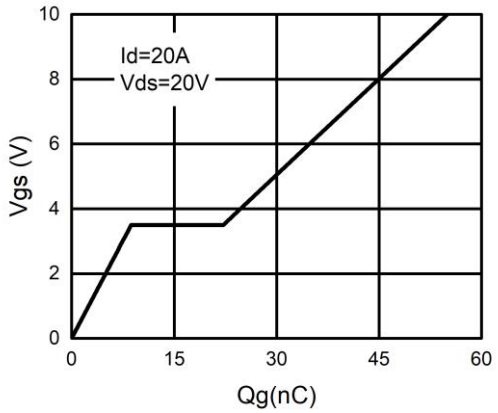


Figure 8. Capacitance

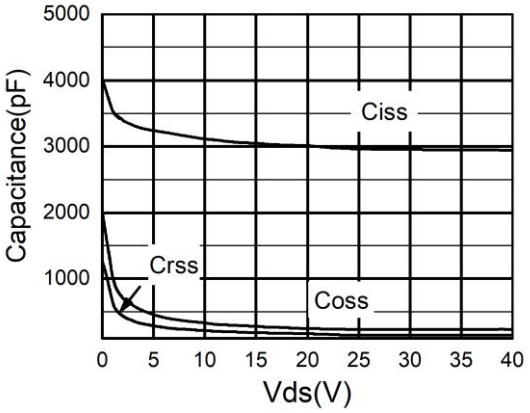


Figure 9. Body-Diode Characteristics

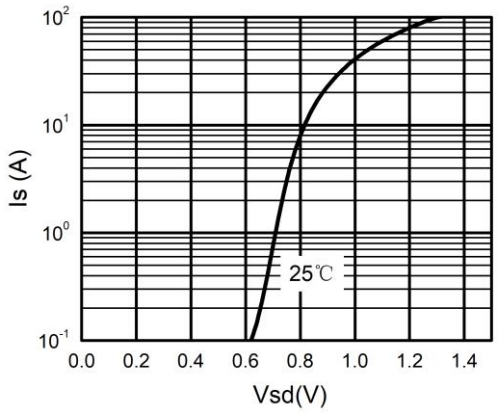
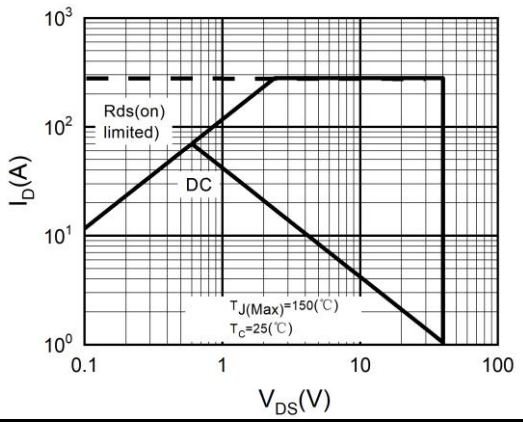
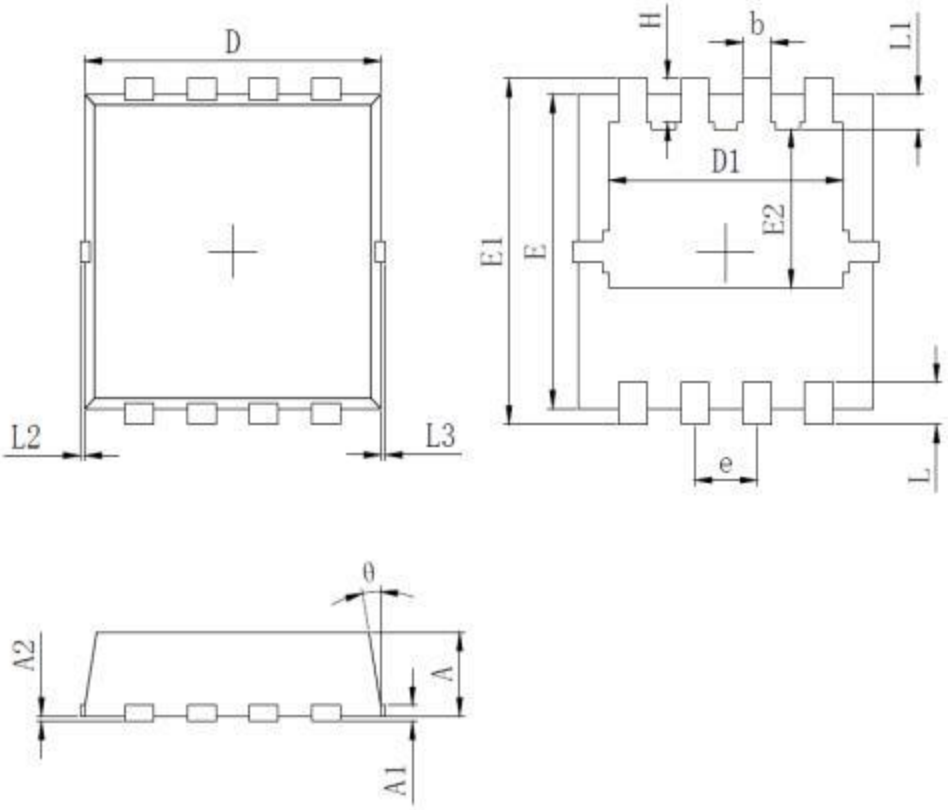


Figure 10. Maximum Safe Operating Area



PDFN3X3-8L Package Information



SYMBOL	MILLIMETER		
	MIN	Typ.	MAX
A	0.700	0.800	0.900
A1	0.152 REF.		
A2	0~0.05		
D	3.000	3.100	3.200
D1	2.300	2.450	2.600
E	2.900	3.000	3.100
E1	3.150	3.300	3.450
E2	1.320	1.520	1.720
b	0.200	0.300	0.400
e	0.550	0.650	0.750
L	0.300	0.400	0.500
L1	0.180	0.330	0.480
L2	0~0.100		
L3	0~0.100		
H	0.315	0.415	0.515
θ	8°	10°	12°

Attention

This product described in this document can not be used in life support devices or systems, aircraft's control systems, and other applications whose failure can be reasonably expected to result in serious physical and/or material damage，apart from that when an application agreement is signed between customer and Linde Semiconductor

The performances and characteristics of this product in the independent testing state are displayed in this document. Linde Semiconductor can't guarantee of the performances and characteristics of this described product that mounted in the customer's products or equipments as same as that in the independent testing state. So the customer should evaluate and test devices mounted in the customer's products or equipments.

Linde Semiconductor reserves the right to improve the designs, functions and reliability of this product and modify any and all information described in this document without notice customer, apart from that when an notice agreement is signed between customer and Linde Semiconductor.

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Linde Semiconductor hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.