



30V NP-Channel Trench Power MOSFET

General Description

The SJM30NP200 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a wide variety of applications.

Features

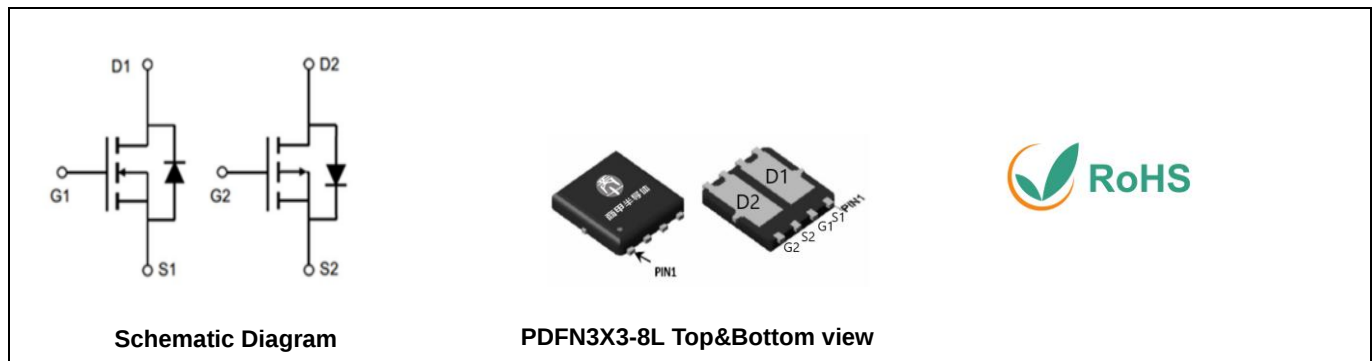
- Low Gate Charge
- High Power and current handing capability
- Lead free product is acquired

Application

- PWM Applications
- Load Switch
- Power Management

Key Performance Parametes

Parameter	Value	Value	Unit
V_{DS}	30	-30	V
$R_{DS(ON_TYP)}$	7.6	12.3	mΩ
I_D	45	-37	A
Q_G	21.2	38	nC



Package Marking and Ordering Information

Device/Ordering Code	Marking	Package	Packing	Reel Size	Tape width	Quantity
SJM30NP200	SJM30NP200	PDFN3X3-8L	Tape	\	\	5000 Pcs

Table 1. Absolute Maximum Ratings ($T_c=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	N Limit	P Limit	Unit
V_{DS}	Drain-Source Voltage ($V_{GS}=0V$)	30	-30	V
V_{GS}	Gate-Source Voltage ($V_{DS}=0V$)	± 20	± 20	V
I_D	Drain Current-Continuous($T_c=25^\circ\text{C}$)	45	-37	A
	Drain Current-Continuous($T_c=100^\circ\text{C}$)	28	-23	A
I_{DM} (pluse)	Drain Current-Continuous@ Current-Pulsed (Note 1)	180	-148	A
P_D	Maximum Power Dissipation($T_c=25^\circ\text{C}$)	30	34	W
	Maximum Power Dissipation($T_c=100^\circ\text{C}$)	12	13.5	W
E_{AS}	Avalanche energy (Note 2)	72	100	mJ
T_J, T_{STG}	Operating Junction and Storage Temperature Range	-55 To 150		$^\circ\text{C}$

Table 2. Thermal Characteristic

Symbol	Parameter	N Limit	P Limit	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to- Case	4.19	3.7	$^\circ\text{C/W}$



30V NP-Channel Trench Power MOSFET

Table 3. N-Channel Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V I _D =-250μA	30			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =30V, V _{GS} =0V T _J =25℃			1	μA
		V _{DS} =30V, V _{GS} =0V T _J =125℃			100	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V			±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	1		2.5	V
g _{FS}	Forward Transconductance	V _{DS} =5V, I _D =4A		15.7		S
R _{DS(ON)}	Drain-Source On-State Resistance	V _{GS} =10V, I _D =4A T _J =25℃		7.6	9.5	mΩ
R _{DS(ON)}	Drain-Source On-State Resistance	V _{GS} =4.5V, I _D =3A T _J =25℃		12.3	15.4	mΩ
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =15V, V _{GS} =0V, f=1.0MHz		1062		pF
C _{oss}	Output Capacitance			130		pF
C _{rss}	Reverse Transfer Capacitance			114		pF
R _g	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1.0MHz		1.53		Ω
Switching Parameters						
t _{d(on)}	Turn-on Delay Time	V _{GS} =4.5V, V _{DS} =15V, R _L =5Ω, R _{GEN} =3Ω		6.4		nS
t _r	Turn-on Rise Time			346		nS
t _{d(off)}	Turn-Off Delay Time			19.4		nS
t _f	Turn-Off Fall Time			9.8		nS
Q _g	Total Gate Charge	V _{GS} =10V, V _{DS} =15V, I _D =4A		21.2		nC
Q _{gs}	Gate-Source Charge			2.32		nC
Q _{gd}	Gate-Drain Charge			4.24		nC
Source-Drain Diode Characteristics						
I _{SD}	Source-Drain Current (Body Diode)				45	A
V _{SD}	Forward on Voltage ^(Note 3)	V _{GS} =0V, I _S =4A			1.2	V

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.

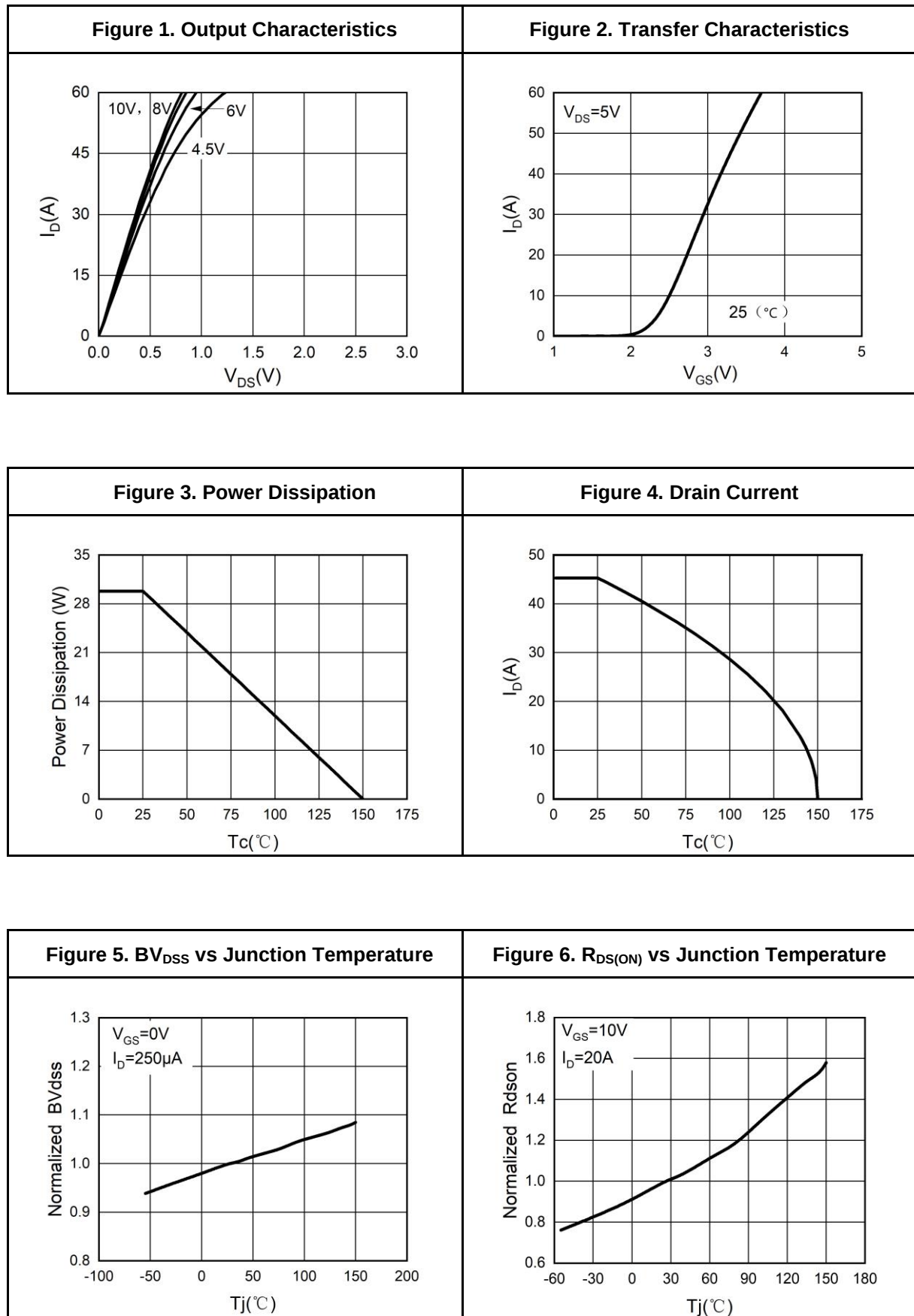
Notes 2.EAS condition: $T_J=25^\circ\text{C}, V_{DD}=30V, V_G=10V, R_g=25\Omega, L=0.5\text{mH}$.

Notes 3.Repetitive Rating: Pulse width limited by maximum junction temperature.



30V NP-Channel Trench Power MOSFET

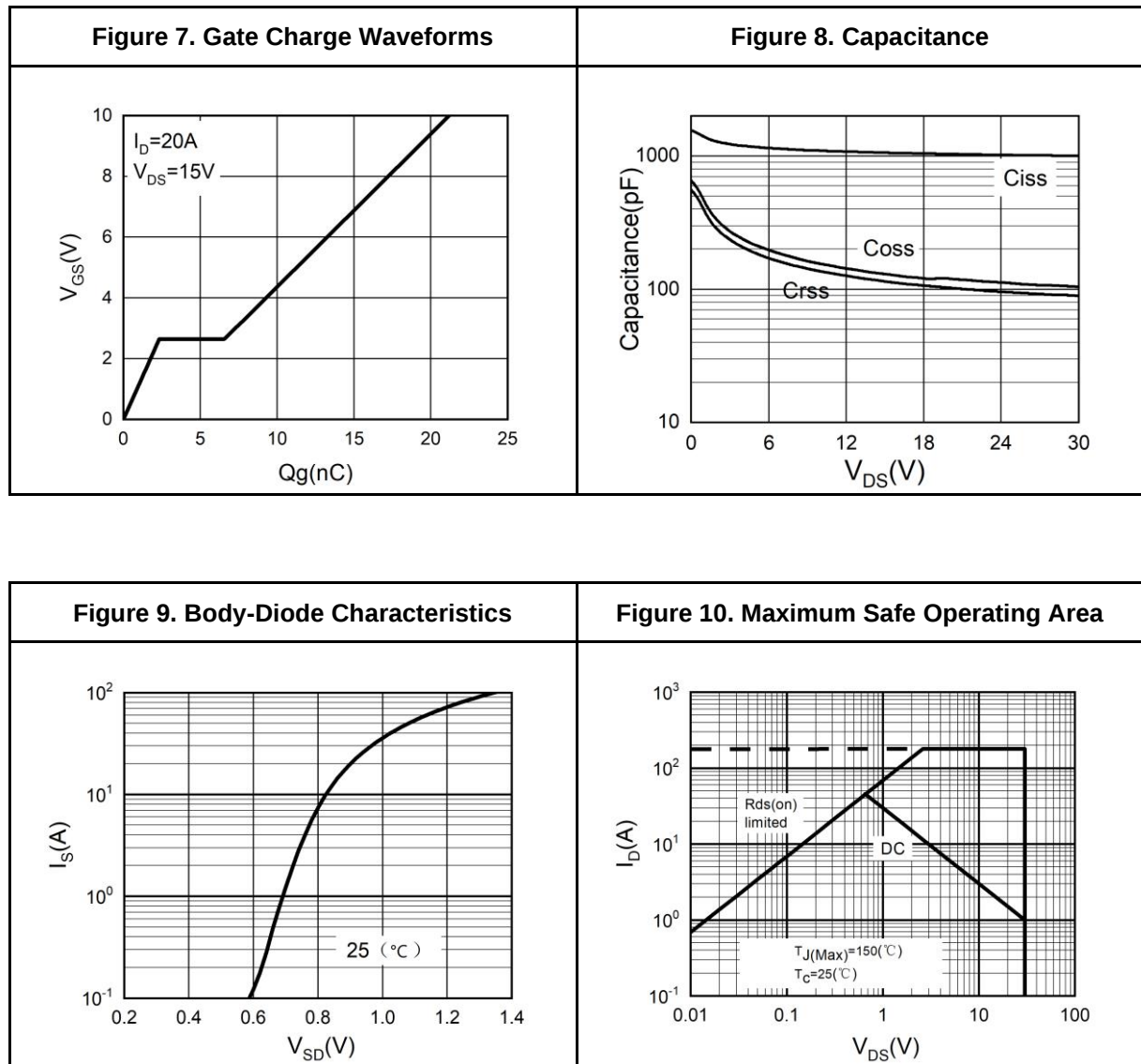
N-Channel Typical Electrical And Thermal Characteristics (Curves)





30V NP-Channel Trench Power MOSFET

N-Channel Typical Electrical And Thermal Characteristics (Curves)





30V NP-Channel Trench Power MOSFET

Table 4. P-Channel Electrical Characteristics (T_J=25℃ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V I _D =-250μA	-30			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =-30V, V _{GS} =0V T _J =25℃			-1	μA
		V _{DS} =-30V, V _{GS} =0V T _J =125℃			-100	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V			±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250μA	-1		-2.5	V
g _{FS}	Forward Transconductance	V _{DS} =-5V, I _D =-5A		8.1		S
R _{DS(ON)}	Drain-Source On-State Resistance	V _{GS} =-10V, I _D =-5A T _J =25℃		11	14.3	mΩ
R _{DS(ON)}	Drain-Source On-State Resistance	V _{GS} =-4.5V, I _D =-4A T _J =25℃		16.6	21.6	mΩ
Dynamic Characteristics2						
C _{iss}	Input Capacitance	V _{DS} =-15V,V _{GS} =0V, f=1.0MHz		1470		pF
C _{oss}	Output Capacitance			165		pF
C _{rss}	Reverse Transfer Capacitance			131		pF
				13		
Switching Parameters						
t _{d(on)}	Turn-on Delay Time	V _{GS} =-10V, V _{DS} =-15V, R _L =3Ω, R _{GEN} =3Ω		14.6		nS
t _r	Turn-on Rise Time			3		nS
t _{d(off)}	Turn-Off Delay Time			91.2		nS
t _f	Turn-Off Fall Time			35.6		nS
Q _g	Total Gate Charge	V _{GS} =-10V, V _{DS} =-15V, I _D =-5A		38		nC
Q _{gs}	Gate-Source Charge			8		nC
Q _{gd}	Gate-Drain Charge			9		nC
Source-Drain Diode Characteristics						
I _{SD}	Source-Drain Current (Body Diode)				-37	A
V _{SD}	Forward on Voltage ^(Note 3)	V _{GS} =0V, I _S =-5A			-1.2	V

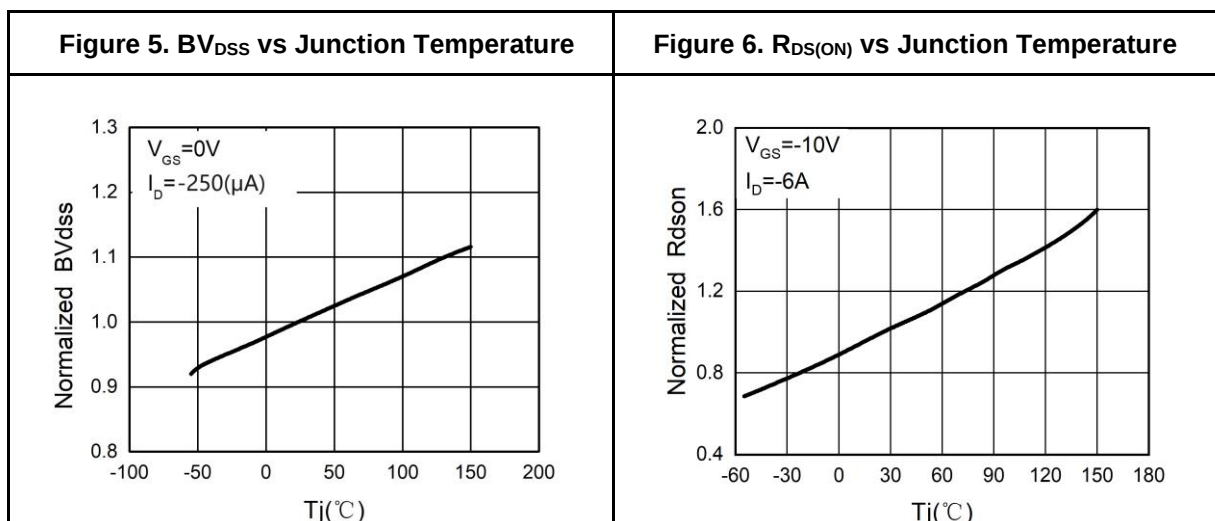
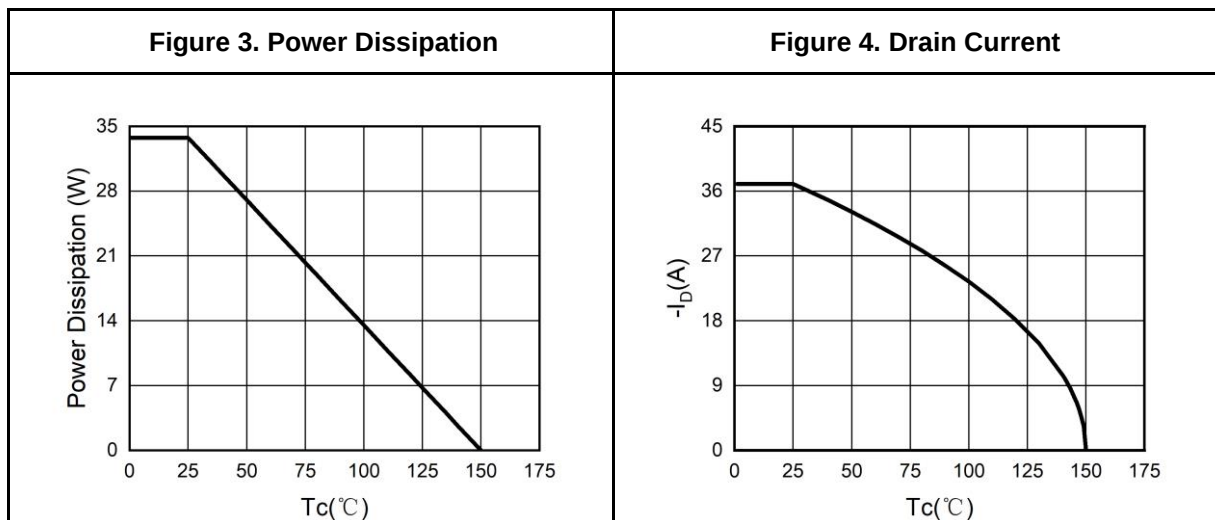
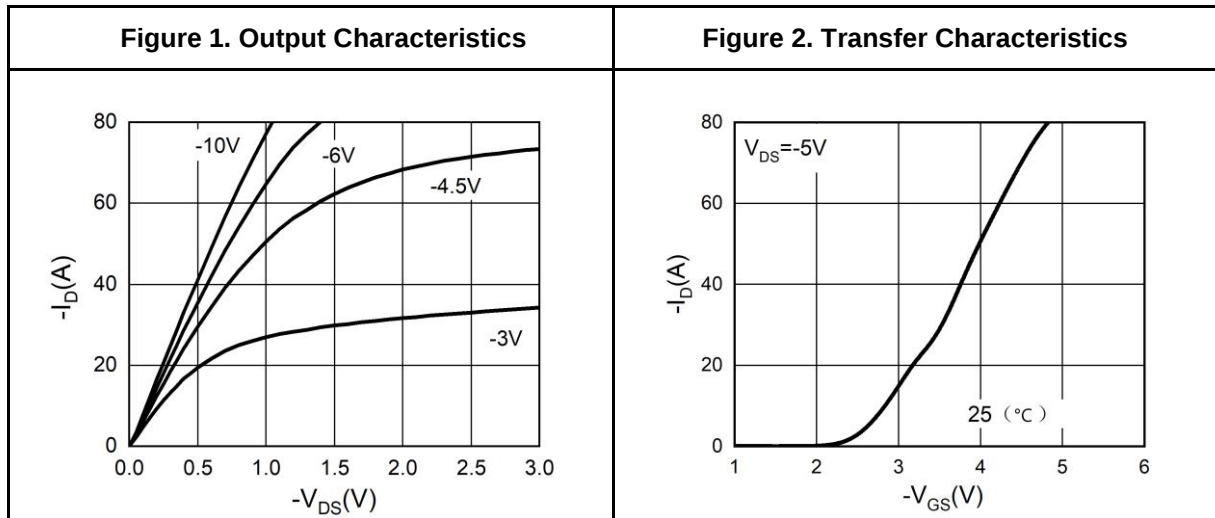
Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.

Notes 2.EAS condition: T_J=25℃, V_{DS}=-30V, V_G=-10V, R_g=25Ω, L=0.5mH.

Notes 3.Repetitive Rating: Pulse width limited by maximum junction temperature.

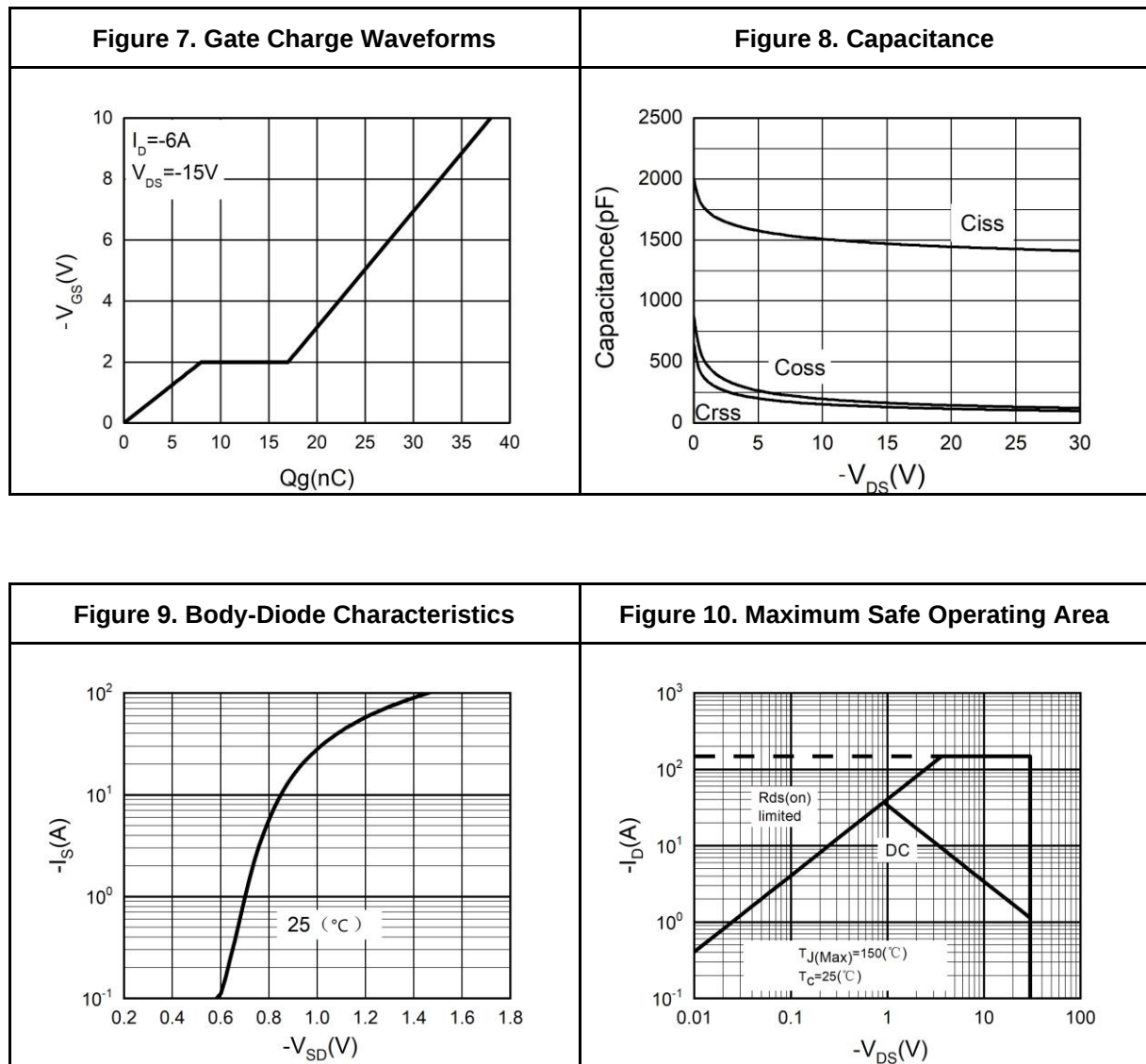


P-Channel Typical Electrical And Thermal Characteristics (Curves)



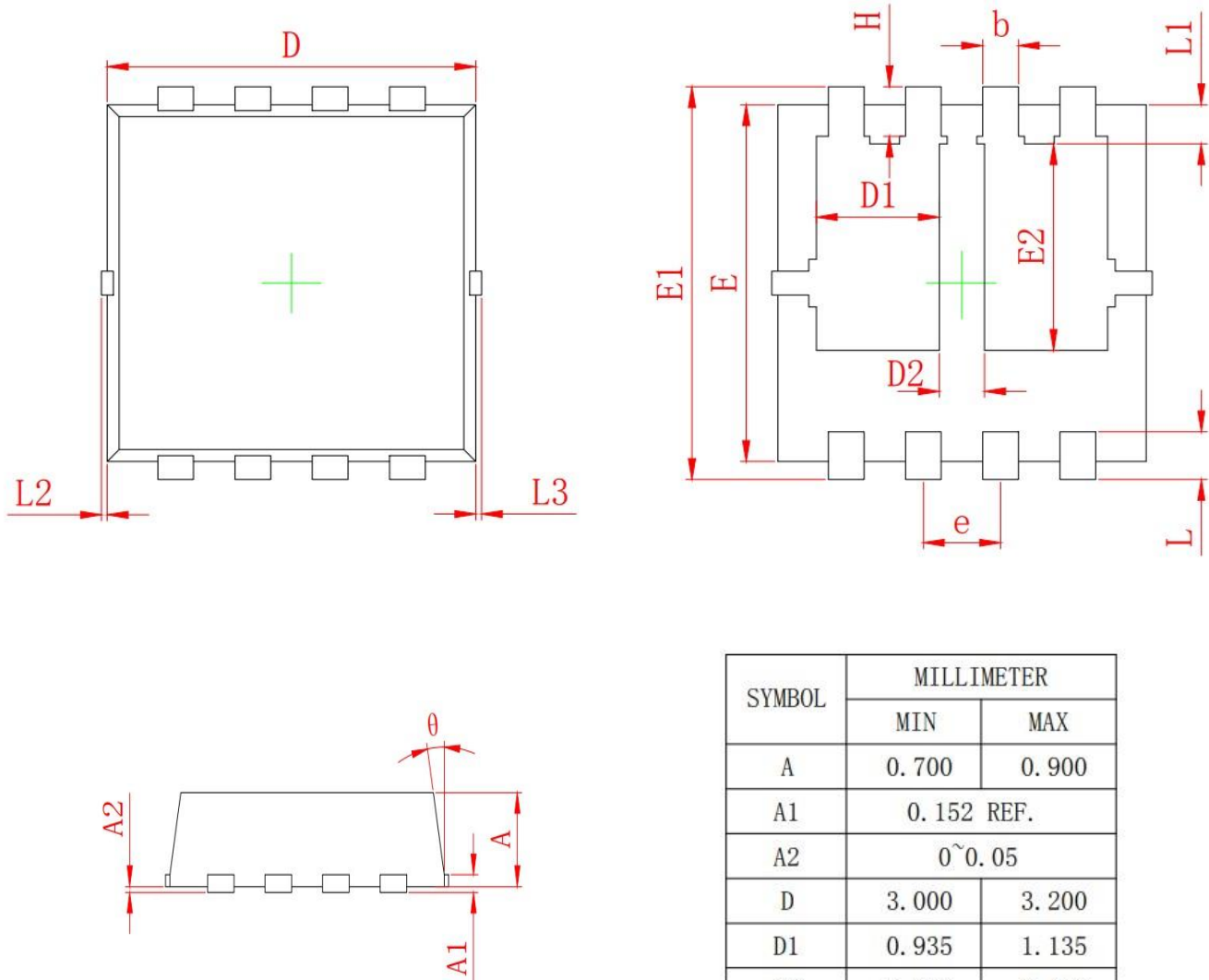


P-Channel Typical Electrical And Thermal Characteristics (Curves)





PDFN3X3-8L Package Information



SYMBOL	MILLIMETER	
	MIN	MAX
A	0.700	0.900
A1	0.152 REF.	
A2	0~0.05	
D	3.000	3.200
D1	0.935	1.135
D2	0.280	0.480
E	2.900	3.100
E1	3.150	3.450
E2	1.535	1.935
b	0.200	0.400
e	0.550	0.750
L	0.300	0.500
L1	0.180	0.480
L2	0~0.100	
L3	0~0.100	
H	0.315	0.515
θ	8°	12°



Attention

This product described in this document can not be used in life support devices or systems, aircraft's control systems, and other applications whose failure can be reasonably expected to result in serious physical and/or material damage, apart from that when an application agreement is signed between customer and Wuxi Shangjia Semiconductor.

The performances and characteristics of this product in the independent testing state are displayed in this document. Wuxi Shangjia Semiconductor can't guarantee of the performances and characteristics of this described product that mounted in the customer's products or equipments as same as that in the independent testing state. So the customer should evaluate and test devices mounted in the customer's products or equipments.

Wuxi Shangjia Semiconductor reserves the right to improve the designs, functions and reliability of this product and modify any and all information described in this document without notice customer, apart from that when an notice agreement is signed between customer and Wuxi Shangjia Semiconductor.

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Wuxi Shangjia Semiconductor hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.