



# 100V N&P-Channel Trench Power MOSFET

## General Description

The SJM01NP235 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a wide variety of applications.

### Features

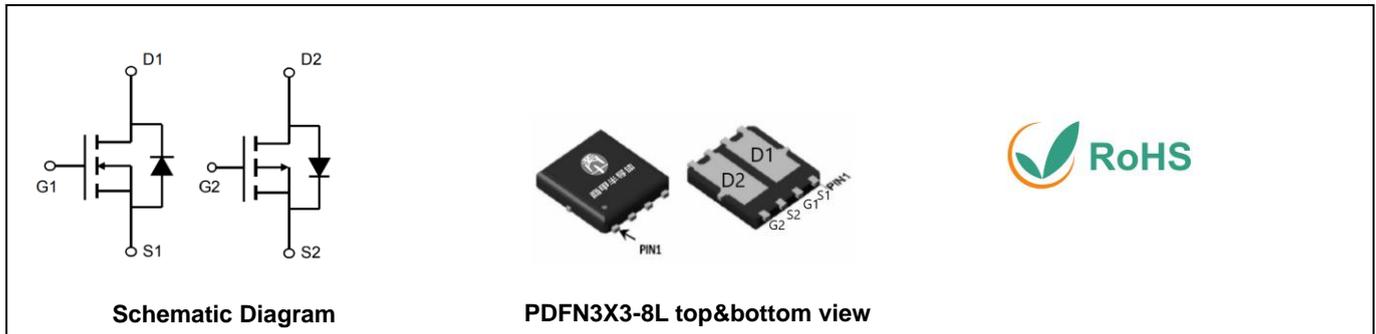
- Low Gate Charge
- High Power and current handing capability
- Lead free product is acquired

### Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

### Key Performance Parametes

Parameter	Value	Value	Unit
$V_{DS}$	100	-100	V
$R_{DS(ON\_TYP)}$	92	162	mΩ
$I_D$	10.3	-9	A
$Q_G$	21	25	nC



## Package Marking and Ordering Information

Device/Ordering Code	Marking	Package	Packing	Reel Size	Tape width	Quantity
SJM01NP235	SJM01NP235	PDFN3X3-8L	Tape	\	\	5000 Pcs

**Table 1. Absolute Maximum Ratings ( $T_C=25^\circ\text{C}$  unless otherwise noted)**

Symbol	Parameter	N Limit	P Limit	Unit
$V_{DS}$	Drain-Source Voltage ( $V_{GS}=0V$ )	100	-100	V
$V_{GS}$	Gate-Source Voltage ( $V_{DS}=0V$ )	$\pm 20$	$\pm 20$	V
$I_D$	Drain Current-Continuous( $T_C=25^\circ\text{C}$ )	10.3	-9	A
	Drain Current-Continuous( $T_C=100^\circ\text{C}$ )	6.5	-5.6	A
$I_{DM}$ (pluse)	Drain Current-Continuous@ Current-Pulsed (Note 1)	41.2	-36	A
$P_D$	Maximum Power Dissipation( $T_C=25^\circ\text{C}$ )	23	34	W
	Maximum Power Dissipation( $T_C=100^\circ\text{C}$ )	9.1	13.7	W
$E_{AS}$	Avalanche energy (Note 2)	25	64	mJ
$T_J, T_{STG}$	Operating Junction and Storage Temperature Range	-55 To 150		$^\circ\text{C}$

**Table 2. Thermal Characteristic**

Symbol	Parameter	N Limit	P Limit	Unit
$R_{\theta JA}$	Thermal Resistance, Junction-to- Ambient	5.5	3.64	$^\circ\text{C}/\text{W}$



# 100V N&P-Channel Trench Power MOSFET

**Table 3. N-Channel Electrical Characteristics ( $T_J=25^\circ\text{C}$  unless otherwise noted)**

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>On/Off States</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	100			V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=100V, V_{GS}=0V, T_J=25^\circ\text{C}$			1	$\mu A$
		$V_{DS}=100V, V_{GS}=0V, T_J=125^\circ\text{C}$			100	$\mu A$
$I_{GSS}$	Gate-Body Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$			$\pm 100$	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1		2.5	V
$g_{FS}$	Forward Transconductance	$V_{DS}=5V, I_D=6A$		14		S
$R_{DS(ON)}$	Drain-Source On-State Resistance	$V_{GS}=10V, I_D=6A, T_J=25^\circ\text{C}$		92	115	m $\Omega$
$R_{DS(ON)}$	Drain-Source On-State Resistance	$V_{GS}=4.5V, I_D=4A, T_J=25^\circ\text{C}$		95	123.5	m $\Omega$
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS}=50V, V_{GS}=0V, f=1.0\text{MHz}$		996		pF
$C_{oss}$	Output Capacitance			31		pF
$C_{rss}$	Reverse Transfer Capacitance			28		pF
$R_g$	Gate resistance	$V_{GS}=0V, V_{DS}=0V, f=1.0\text{MHz}$		1.1		$\Omega$
<b>Switching Parameters</b>						
$t_{d(on)}$	Turn-on Delay Time	$V_{GS}=10V, V_{DS}=50V, R_L=8.3\Omega, R_{GEN}=3\Omega$		11		nS
$t_r$	Turn-on Rise Time			7.4		nS
$t_{d(off)}$	Turn-Off Delay Time			35		nS
$t_f$	Turn-Off Fall Time			9		nS
$Q_g$	Total Gate Charge	$V_{GS}=10V, V_{DS}=50V, I_D=6A$		21		nC
$Q_{gs}$	Gate-Source Charge			3		nC
$Q_{gd}$	Gate-Drain Charge			6		nC
<b>Source-Drain Diode Characteristics</b>						
$I_{SD}$	Source-Drain Current (Body Diode)				10.3	A
$V_{SD}$	Forward on Voltage <sup>(Note 3)</sup>	$V_{GS}=0V, I_S=6A$			1.2	V
$t_{rr}$	Reverse Recovery Time	$I_F=6A, dI/dt=100A/\mu s$		26		ns
$Q_{rr}$	Reverse Recovery Charge	$I_F=6A, dI/dt=100A/\mu s$		27		nC

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.

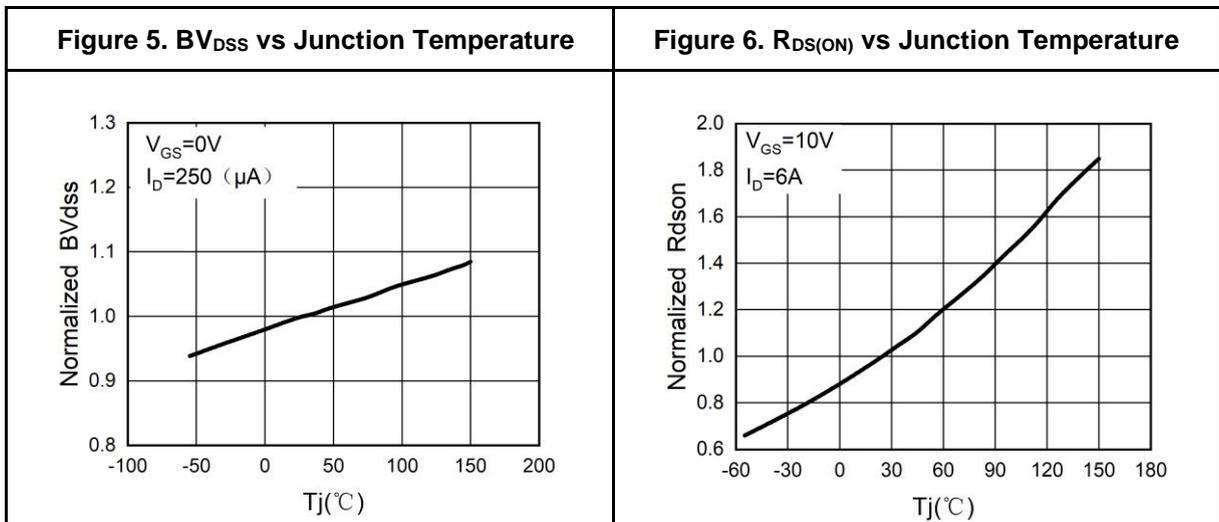
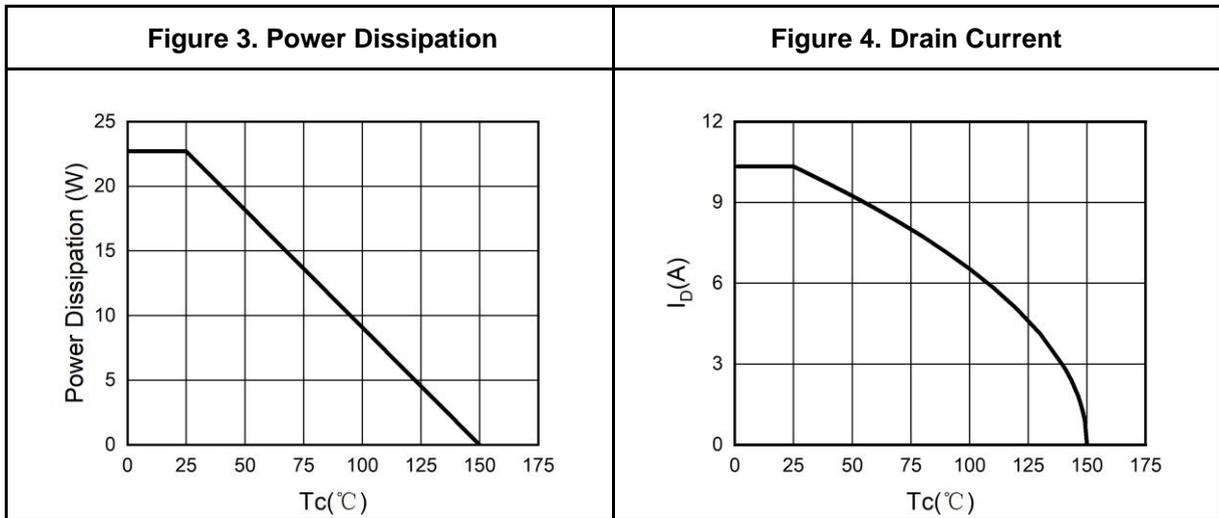
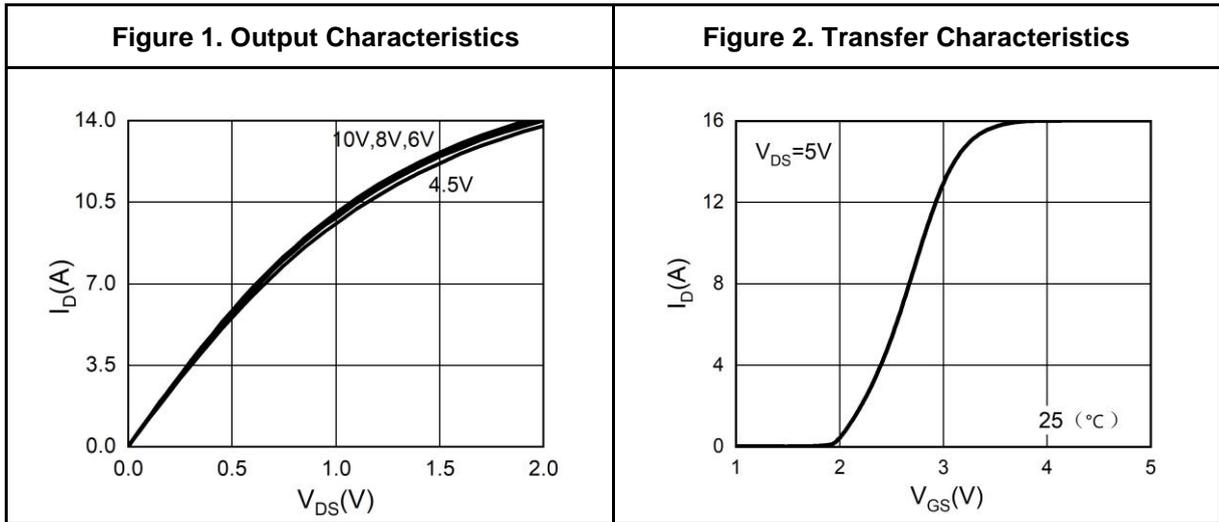
Notes 2.EAS condition:  $T_J=25^\circ\text{C}, V_{DD}=30V, V_G=10V, R_g=25\Omega, L=0.5\text{mH}$ .

Notes 3.Repetitive Rating: Pulse width limited by maximum junction temperature.



# 100V N&P-Channel Trench Power MOSFET

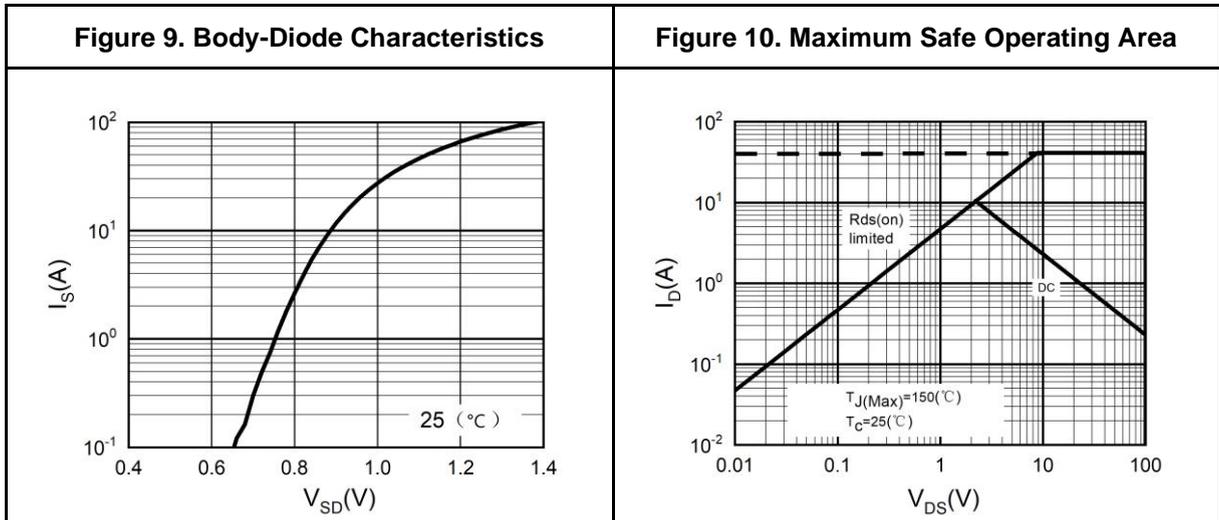
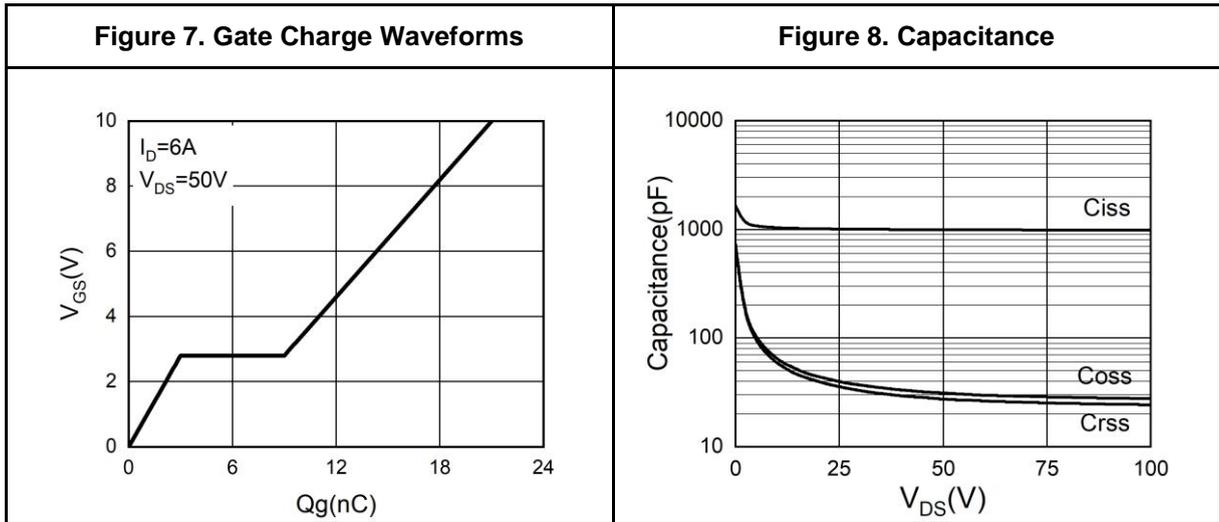
## N-Channel Typical Electrical And Thermal Characteristics (Curves)





# 100V N&P-Channel Trench Power MOSFET

## N-Channel Typical Electrical And Thermal Characteristics (Curves)





# 100V N&P-Channel Trench Power MOSFET

**Table 4. P-Channel Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise noted)**

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>On/Off States</b>						
B <sub>V</sub> DSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA	-100			V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =-100V, V <sub>GS</sub> =0V T <sub>J</sub> =25°C			-1	μA
		V <sub>DS</sub> =-100V, V <sub>GS</sub> =0V T <sub>J</sub> =125°C			-100	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V			±100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-1		-2.5	V
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> =-5V, I <sub>D</sub> =-5A		13		S
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance	V <sub>GS</sub> =-10V, I <sub>D</sub> =-5A T <sub>J</sub> =25°C		162	202.5	mΩ
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-4A T <sub>J</sub> =25°C		171	222.3	mΩ
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =-50V, V <sub>GS</sub> =0V, f=1.0MHz		1980		pF
C <sub>oss</sub>	Output Capacitance			47		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			39		pF
R <sub>g</sub>	Gate resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1.0MHz		5		Ω
<b>Switching Parameters</b>						
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>GS</sub> =-10V, V <sub>DS</sub> =-50V, R <sub>L</sub> =10Ω, R <sub>GEN</sub> =3Ω		14		nS
t <sub>r</sub>	Turn-on Rise Time			18		nS
t <sub>d(off)</sub>	Turn-Off Delay Time			50		nS
t <sub>f</sub>	Turn-Off Fall Time			18		nS
Q <sub>g</sub>	Total Gate Charge	V <sub>GS</sub> =-10V, V <sub>DS</sub> =-50V, I <sub>D</sub> =-5A		25		nC
Q <sub>gs</sub>	Gate-Source Charge			5		nC
Q <sub>gd</sub>	Gate-Drain Charge			7		nC
<b>Source-Drain Diode Characteristics</b>						
I <sub>SD</sub>	Source-Drain Current (Body Diode)				-9	A
V <sub>SD</sub>	Forward on Voltage <sup>(Note 3)</sup>	V <sub>GS</sub> =0V, I <sub>S</sub> =-5A			-1.2	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>F</sub> =-5A, dI/dt=-100A/μs		35		ns
Q <sub>rr</sub>	Reverse Recovery Charge	I <sub>F</sub> =-5A, dI/dt=-100A/μs		46		nC

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.

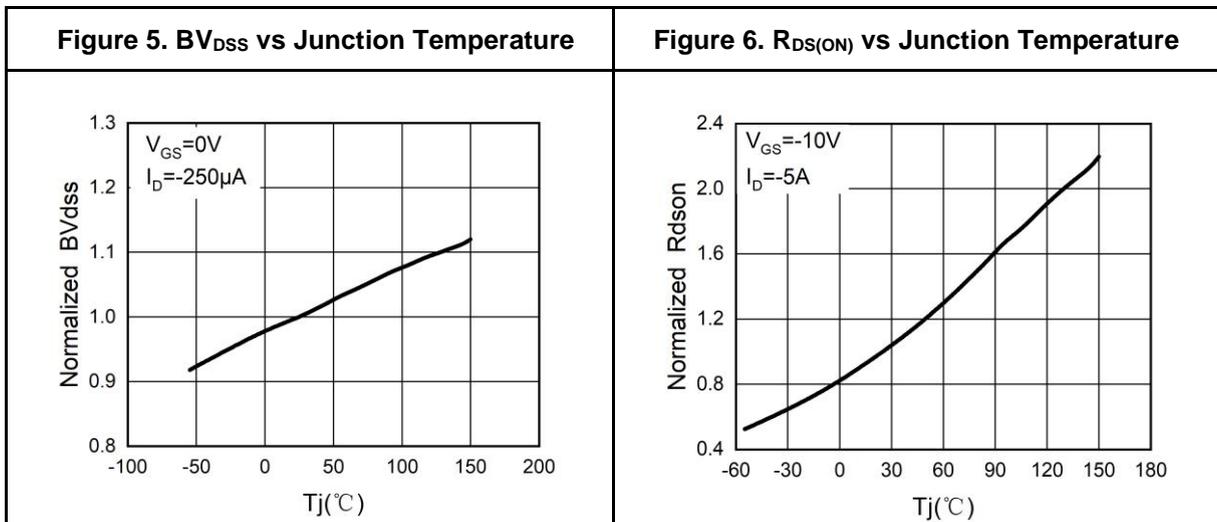
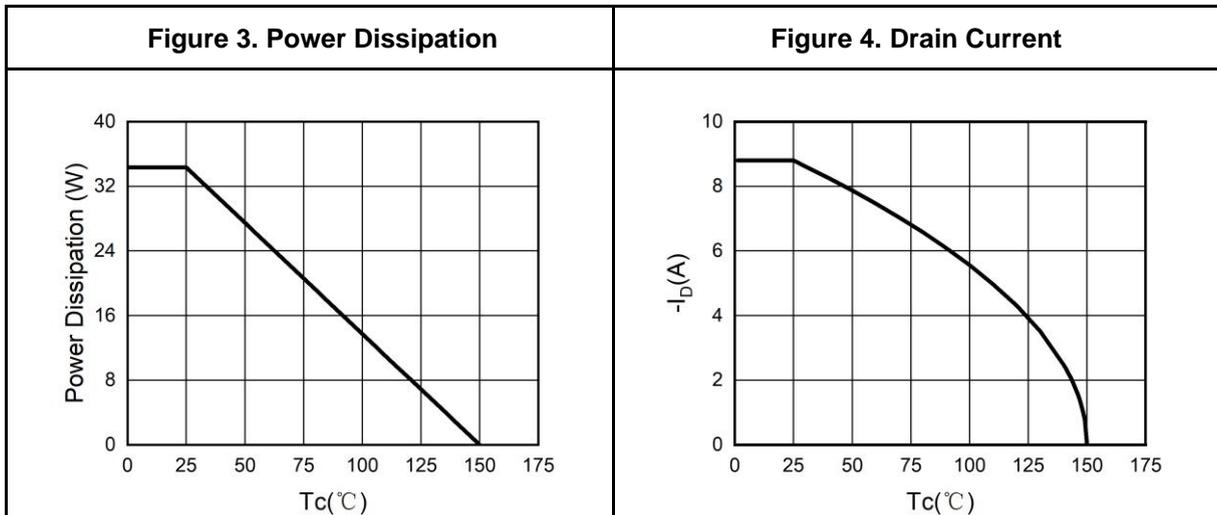
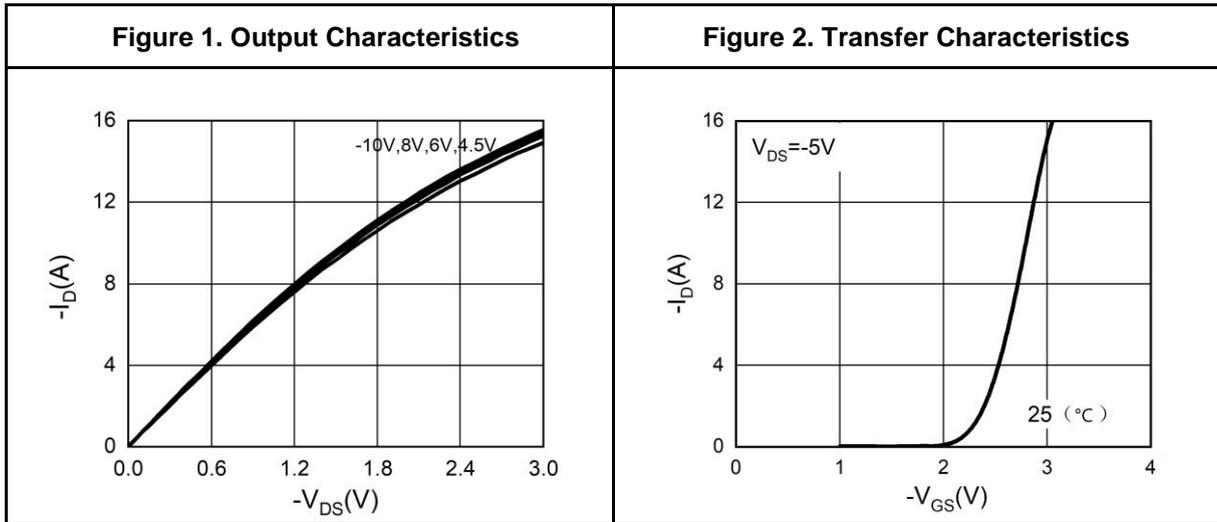
Notes 2.EAS condition: T<sub>J</sub>=25°C, V<sub>DD</sub>=-40V, V<sub>G</sub>=-10V, R<sub>g</sub>=25Ω, L=0.5mH.

Notes 3.Repetitive Rating: Pulse width limited by maximum junction temperature.



# 100V N&P-Channel Trench Power MOSFET

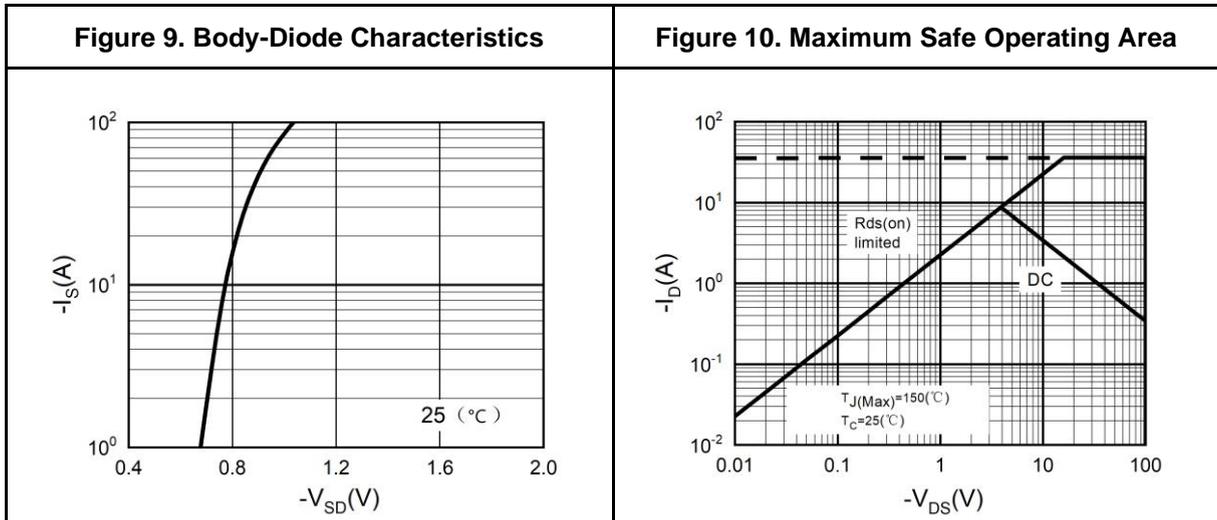
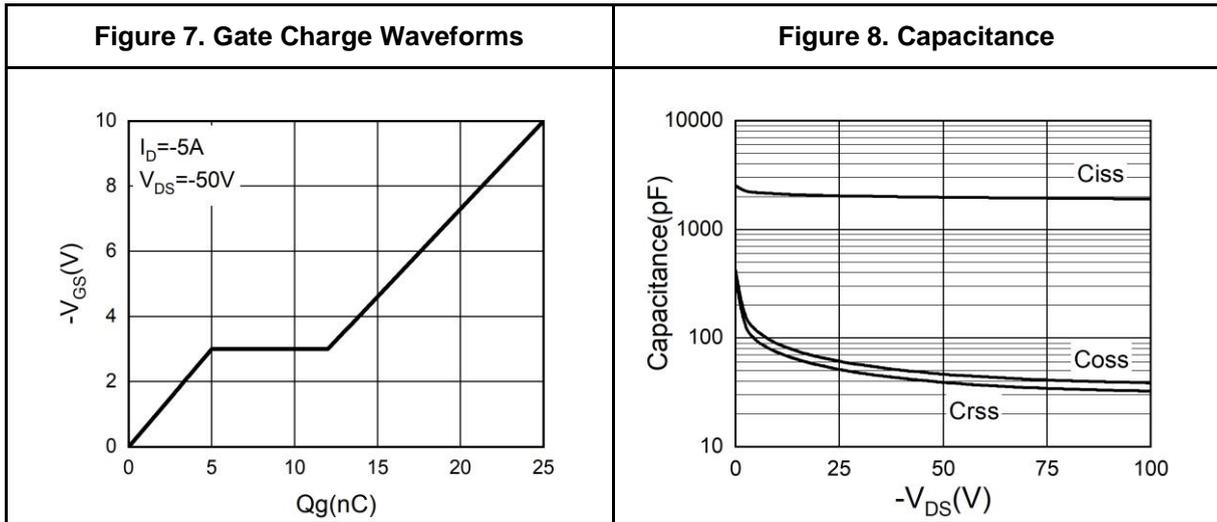
## P-Channel Typical Electrical And Thermal Characteristics (Curves)





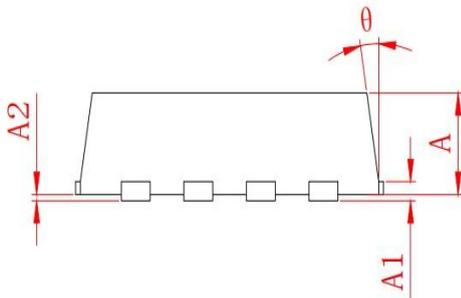
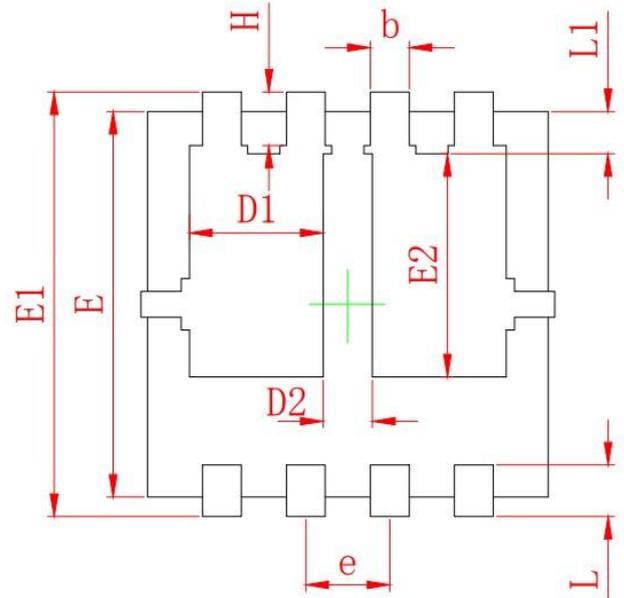
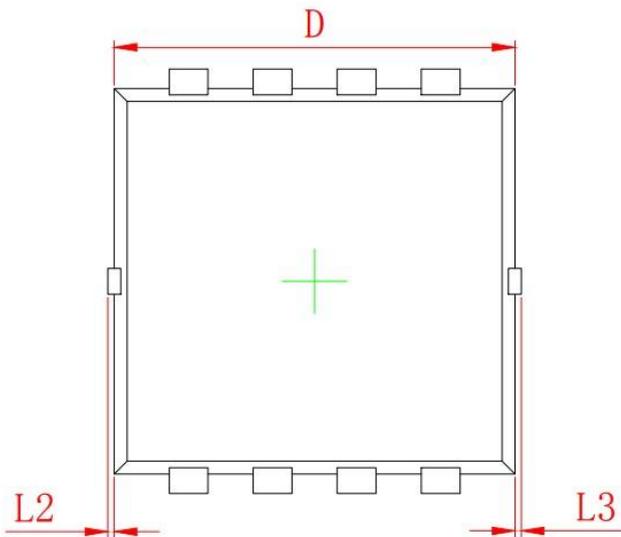
# 100V N&P-Channel Trench Power MOSFET

## P-Channel Typical Electrical And Thermal Characteristics (Curves)





PDFN3X3-8L Package Information



SYMBOL	MILLIMETER	
	MIN	MAX
A	0.700	0.900
A1	0.152 REF.	
A2	0~0.05	
D	3.000	3.200
D1	0.935	1.135
D2	0.280	0.480
E	2.900	3.100
E1	3.150	3.450
E2	1.535	1.935
b	0.200	0.400
e	0.550	0.750
L	0.300	0.500
L1	0.180	0.480
L2	0~0.100	
L3	0~0.100	
H	0.315	0.515
θ	8°	12°



## 100V N&P-Channel Trench Power MOSFET

---

### Attention

This product described in this document can not be used in life support devices or systems, aircraft's control systems, and other applications whose failure can be reasonably expected to result in serious physical and/or material damage, apart from that when an application agreement is signed between customer and Wuxi Shangjia Semiconductor.

The performances and characteristics of this product in the independent testing state are displayed in this document. Wuxi Shangjia Semiconductor can't guarantee of the performances and characteristics of this described product that mounted in the customer's products or equipments as same as that in the independent testing state. So the customer should evaluate and test devices mounted in the customer's products or equipments.

Wuxi Shangjia Semiconductor reserves the right to improve the designs, functions and reliability of this product and modify any and all information described in this document without notice customer, apart from that when a notice agreement is signed between customer and Wuxi Shangjia Semiconductor.

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Wuxi Shangjia Semiconductor hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.