



40V N&P-Channel Trench Power MOSFET

General Description

The SJH40NP195 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a wide variety of applications.

Features

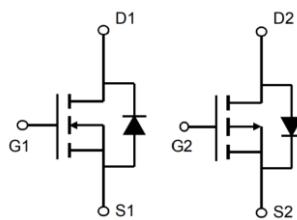
- Low Gate Charge
- 100% UIS Tested, 100% DVDS Tested
- High Power and current handing capability
- Lead free product is acquired

Application

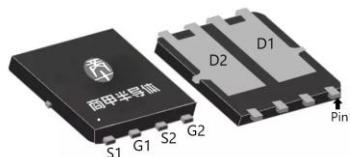
- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

Key Performance Parametes

Parameter	Value	Value	Unit
V_{DS}	40	-40	V
$R_{DS(ON)}_{TYP}$	7.8	16.4	mΩ
I_D	52	-34	A
Q_G	24.5	60	nC



Schematic Diagram



PDFN5X6 top&bottom view

Package Marking and Ordering Information

Device/Ordering Code	Marking	Package	Packing	Reel Size	Tape width	Quantity
SJH40NP195	SJH40NP195	PDFN5X6	Tape	\	\	2500 Pcs

Table 1. Absolute Maximum Ratings ($T_c=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	N Limit	P Limit	Unit
V_{DS}	Drain-Source Voltage ($V_{GS}=0\text{V}$)	40	-40	V
V_{GS}	Gate-Source Voltage ($V_{DS}=0\text{V}$)	± 20	± 20	V
I_D	Drain Current-Continuous($T_c=25^\circ\text{C}$)	52	-34	A
	Drain Current-Continuous($T_c=100^\circ\text{C}$)	33	-21	A
$I_{DM\ (pulse)}$	Drain Current-Continuous@ Current-Pulsed (Note 1)	208	-136	A
P_D	Maximum Power Dissipation($T_c=25^\circ\text{C}$)	50	39	W
	Maximum Power Dissipation($T_c=100^\circ\text{C}$)	20	16	W
E_{AS}	Avalanche energy (Note 2)	156	156	mJ
T_J, T_{STG}	Operating Junction and Storage Temperature Range	-55 To 150		°C

Table 2. Thermal Characteristic

Symbol	Parameter	N Limit	P Limit	Unit
$R_{\theta JA}$	Thermal Resistance, Junction-to- Case	2.48	3.2	°C/W



40V N&P-Channel Trench Power MOSFET

Table 3. N-Channel Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ $I_{\text{D}}=250\mu\text{A}$	40			V
$I_{\text{DS}}^{\text{SS}}$	Zero Gate Voltage Drain Current	$V_{\text{DS}}=40\text{V}$, $V_{\text{GS}}=0\text{V}$ $T_J=25^\circ\text{C}$			1	μA
		$V_{\text{DS}}=40\text{V}$, $V_{\text{GS}}=0\text{V}$ $T_J=125^\circ\text{C}$			100	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$			± 100	nA
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_{\text{D}}=250\mu\text{A}$	1		2	V
g_{FS}	Forward Transconductance	$V_{\text{DS}}=5\text{V}$, $I_{\text{D}}=20\text{A}$		38		S
$R_{\text{DS(ON)}}$	Drain-Source On-State Resistance	$V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=20\text{A}$ $T_J=25^\circ\text{C}$		7.8	9.8	$\text{m}\Omega$
$R_{\text{DS(ON)}}$	Drain-Source On-State Resistance	$V_{\text{GS}}=4.5\text{V}$, $I_{\text{D}}=15\text{A}$ $T_J=25^\circ\text{C}$		8.8	11.7	$\text{m}\Omega$
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{\text{DS}}=20\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1.0\text{MHz}$		2150		pF
C_{oss}	Output Capacitance			164		pF
C_{rss}	Reverse Transfer Capacitance			134		pF
R_g	Gate resistance	$V_{\text{GS}}=0\text{V}$, $V_{\text{DS}}=0\text{V}$, $f=1.0\text{MHz}$		2		Ω
Switching Parameters						
$t_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{GS}}=10\text{V}$, $V_{\text{DS}}=20\text{V}$, $R_{\text{L}}=1\Omega$, $R_{\text{GEN}}=3\Omega$		4.6		nS
t_r	Turn-on Rise Time			12		nS
$t_{\text{d(off)}}$	Turn-Off Delay Time			18.8		nS
t_f	Turn-Off Fall Time			6		nS
Q_g	Total Gate Charge	$V_{\text{GS}}=10\text{V}$, $V_{\text{DS}}=20\text{V}$, $I_{\text{D}}=20\text{A}$		24.5		nC
Q_{gs}	Gate-Source Charge			3.7		nC
Q_{gd}	Gate-Drain Charge			6.3		nC
Source-Drain Diode Characteristics						
I_{SD}	Source-Drain Current (Body Diode)				52	A
V_{SD}	Forward on Voltage (Note 3)	$V_{\text{GS}}=0\text{V}$, $I_{\text{S}}=20\text{A}$			1.2	V
t_{rr}	Reverse Recovery Time	$I_{\text{F}}=20\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$		17.5		ns
Q_{rr}	Reverse Recovery Charge	$I_{\text{F}}=20\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$		10.9		nC

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.

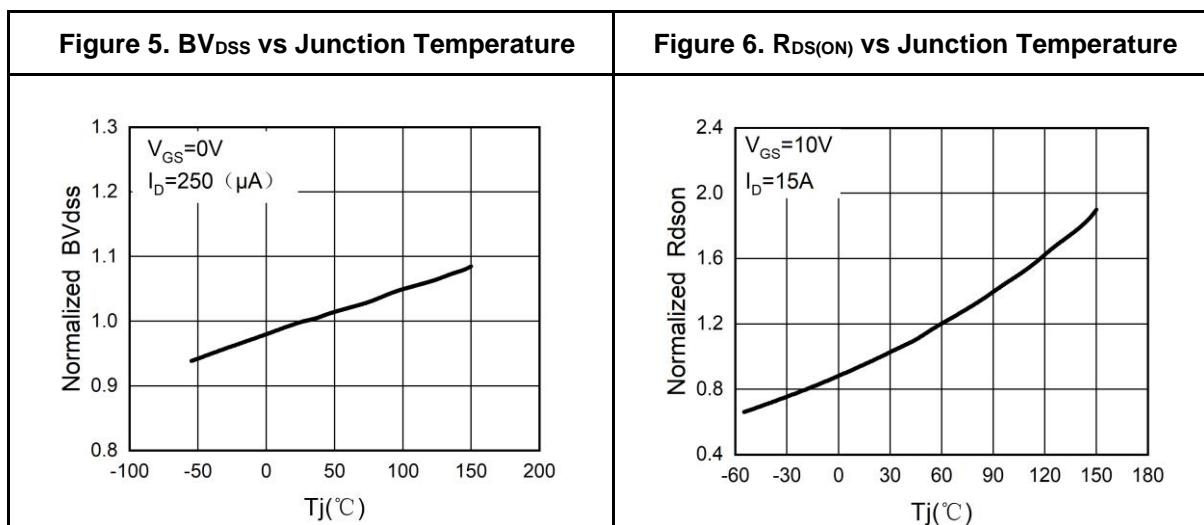
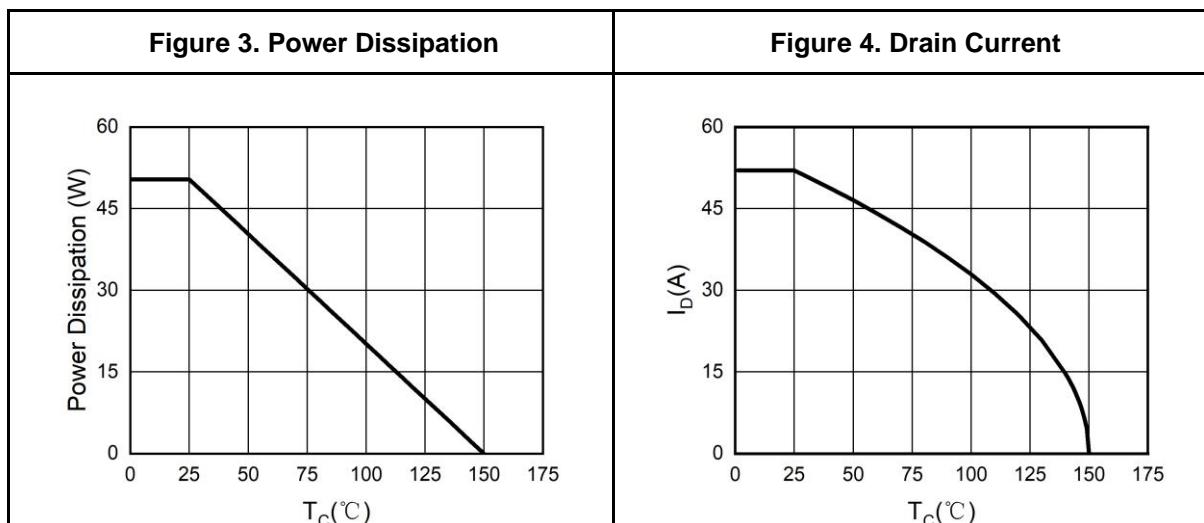
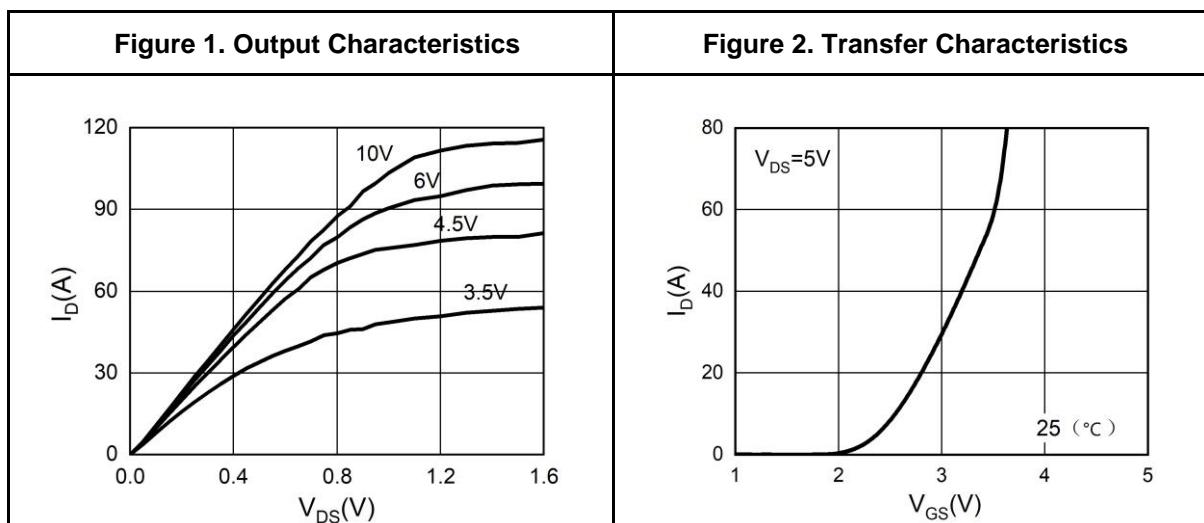
Notes 2.E_{AS} condition: $T_J=25^\circ\text{C}$, $V_{\text{DD}}=30\text{V}$, $V_G=10\text{V}$, $R_g=25\Omega$, $L=0.5\text{mH}$.

Notes 3.Repetitive Rating: Pulse width limited by maximum junction temperature.



40V N&P-Channel Trench Power MOSFET

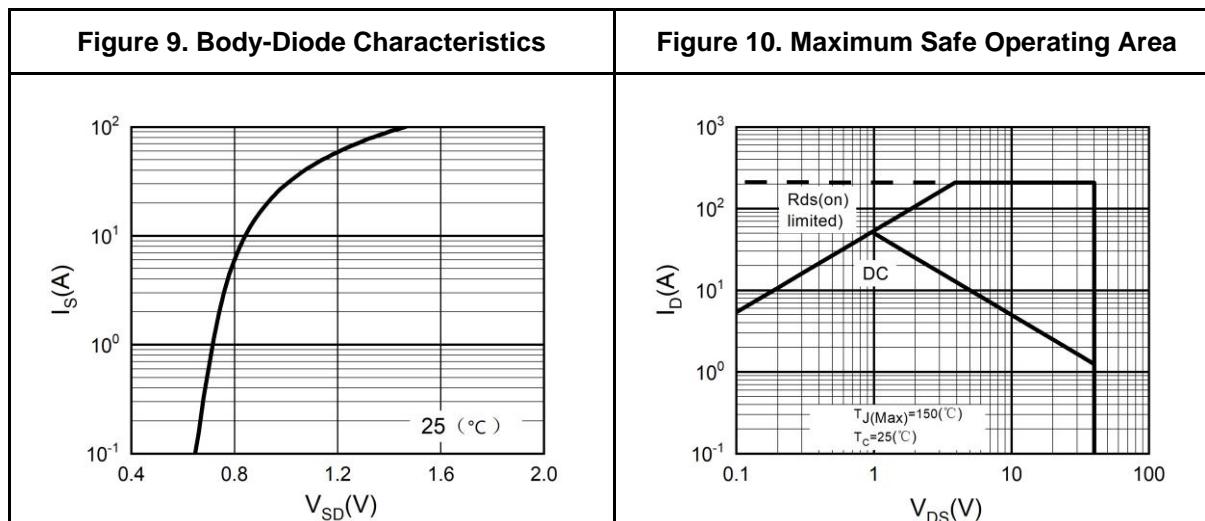
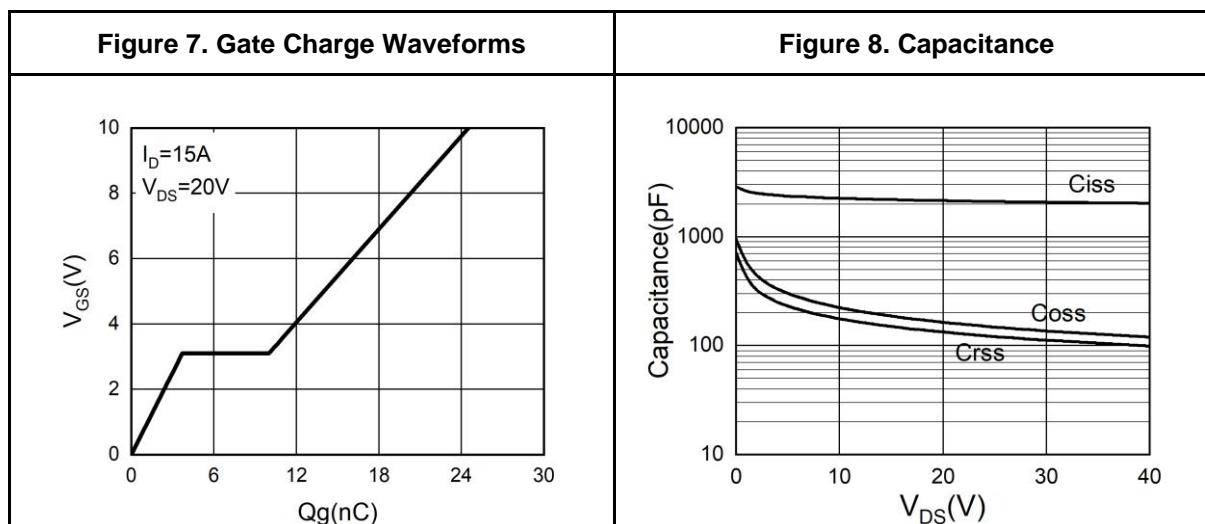
N-Channel Typical Electrical And Thermal Characteristics (Curves)





40V N&P-Channel Trench Power MOSFET

N-Channel Typical Electrical And Thermal Characteristics (Curves)





40V N&P-Channel Trench Power MOSFET

Table 4. P-Channel Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ $I_{\text{D}}=-250\mu\text{A}$	-40			V
$I_{\text{DS}(\text{SS})}$	Zero Gate Voltage Drain Current	$V_{\text{DS}}=-40\text{V}$, $V_{\text{GS}}=0\text{V}$ $T_J=25^\circ\text{C}$			-1	μA
		$V_{\text{DS}}=-40\text{V}$, $V_{\text{GS}}=0\text{V}$ $T_J=125^\circ\text{C}$			-100	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$			± 100	nA
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_{\text{D}}=-250\mu\text{A}$	-1		-2.5	V
g_{FS}	Forward Transconductance	$V_{\text{DS}}=-5\text{V}$, $I_{\text{D}}=-20\text{A}$		-39		S
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance	$V_{\text{GS}}=-10\text{V}$, $I_{\text{D}}=-20\text{A}$ $T_J=25^\circ\text{C}$		16.4	20.5	$\text{m}\Omega$
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance	$V_{\text{GS}}=-4.5\text{V}$, $I_{\text{D}}=-15\text{A}$ $T_J=25^\circ\text{C}$		19.8	26.3	$\text{m}\Omega$
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{\text{DS}}=-20\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1.0\text{MHz}$		2100		pF
C_{oss}	Output Capacitance			168		pF
C_{rss}	Reverse Transfer Capacitance			136		pF
R_g	Gate resistance	$V_{\text{GS}}=0\text{V}$, $V_{\text{DS}}=0\text{V}$, $f=1.0\text{MHz}$		9		Ω
Switching Parameters						
$t_{\text{d}(\text{on})}$	Turn-on Delay Time	$V_{\text{GS}}=-10\text{V}$, $V_{\text{DS}}=-20\text{V}$, $R_{\text{L}}=1\Omega$, $R_{\text{GEN}}=3\Omega$		10		nS
t_r	Turn-on Rise Time			15		nS
$t_{\text{d}(\text{off})}$	Turn-Off Delay Time			38		nS
t_f	Turn-Off Fall Time			16.4		nS
Q_g	Total Gate Charge	$V_{\text{GS}}=-10\text{V}$, $V_{\text{DS}}=-20\text{V}$, $I_{\text{D}}=-20\text{A}$		60		nC
Q_{gs}	Gate-Source Charge			8.5		nC
Q_{gd}	Gate-Drain Charge			13		nC
Source-Drain Diode Characteristics						
I_{SD}	Source-Drain Current (Body Diode)				-34	A
V_{SD}	Forward on Voltage (Note 3)	$V_{\text{GS}}=0\text{V}$, $I_{\text{S}}=-20\text{A}$			-1.2	V
t_{rr}	Reverse Recovery Time	$I_{\text{F}}=-20\text{A}$, $dI/dt=-100\text{A}/\mu\text{s}$		17.3		ns
Q_{rr}	Reverse Recovery Charge	$I_{\text{F}}=-20\text{A}$, $dI/dt=-100\text{A}/\mu\text{s}$		9.5		nC

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.

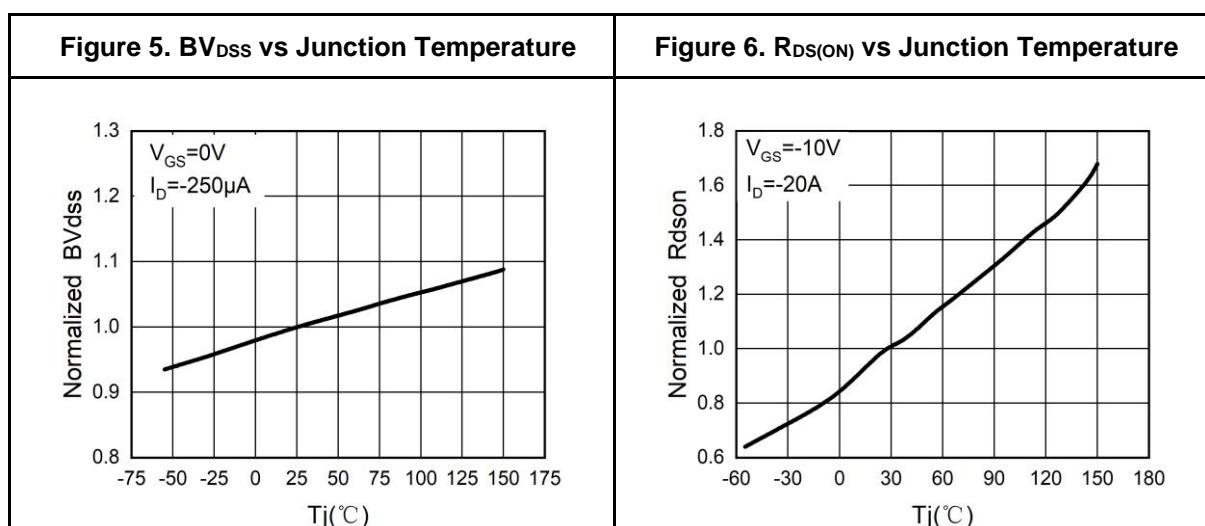
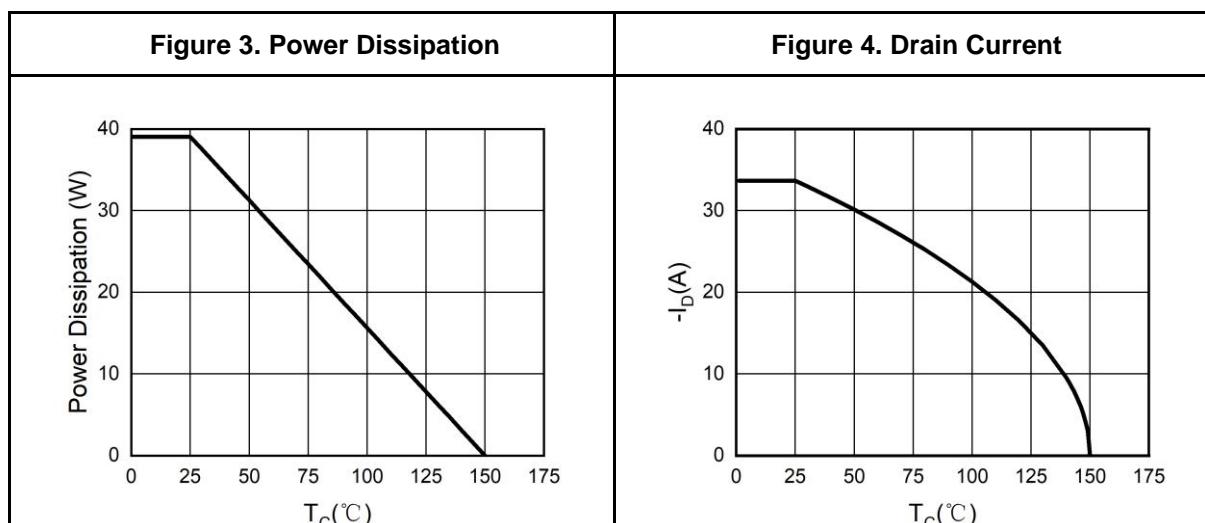
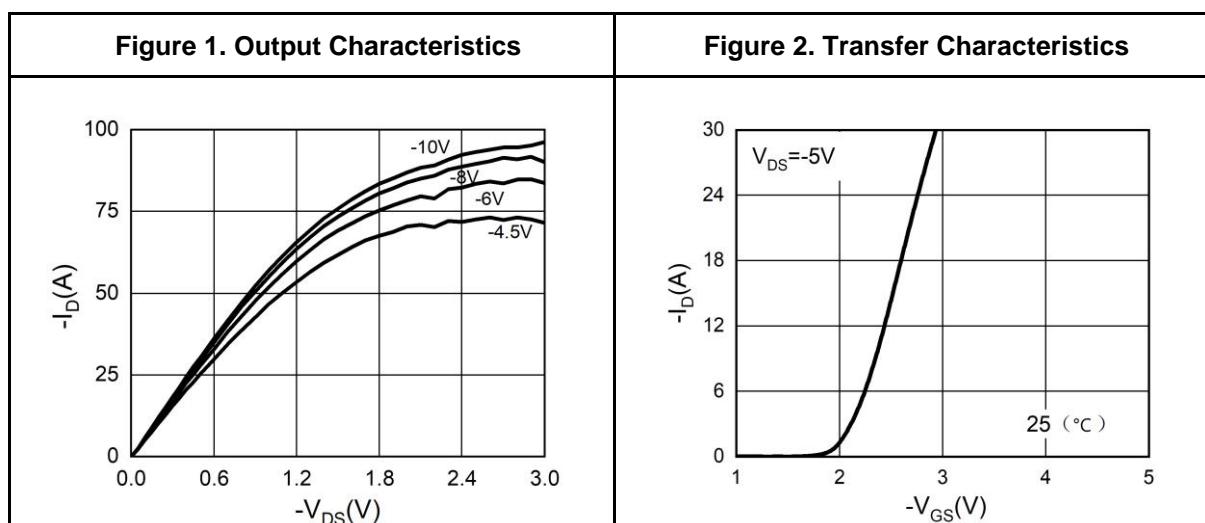
Notes 2.E_{AS} condition: $T_J=25^\circ\text{C}$, $V_{\text{DD}}=-40\text{V}$, $V_{\text{G}}=-10\text{V}$, $R_g=25\Omega$, $L=0.5\text{mH}$.

Notes 3.Repetitive Rating: Pulse width limited by maximum junction temperature.



40V N&P-Channel Trench Power MOSFET

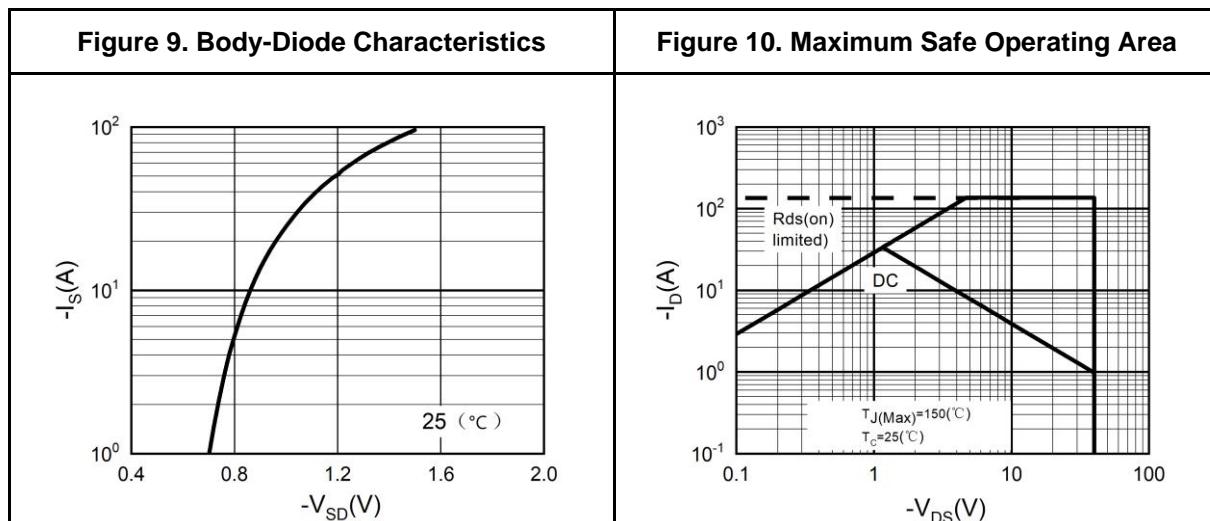
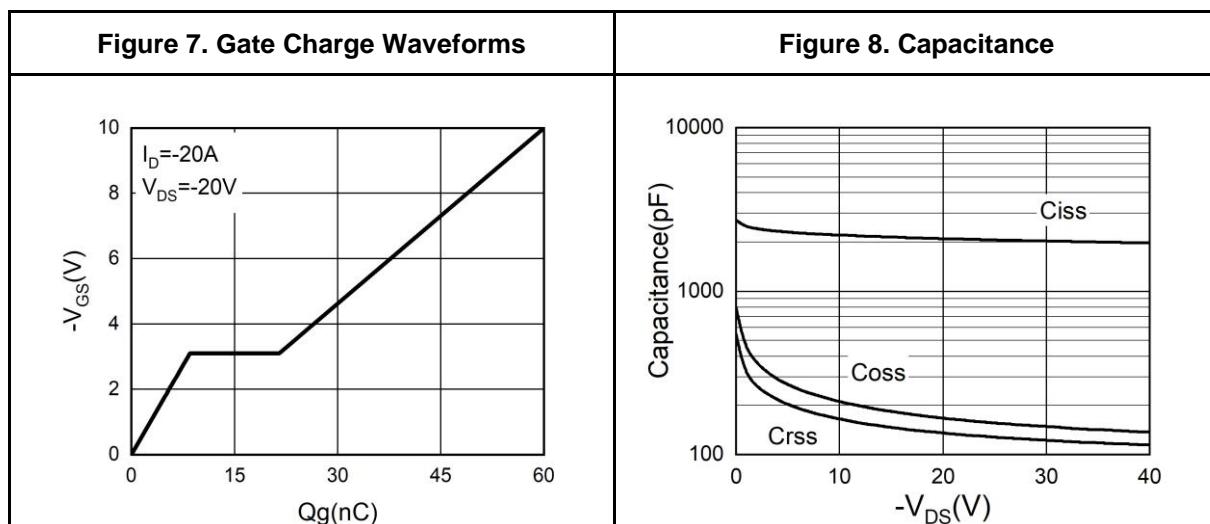
P-Channel Typical Electrical And Thermal Characteristics (Curves)





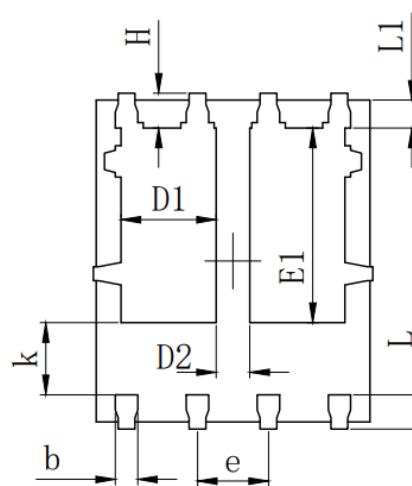
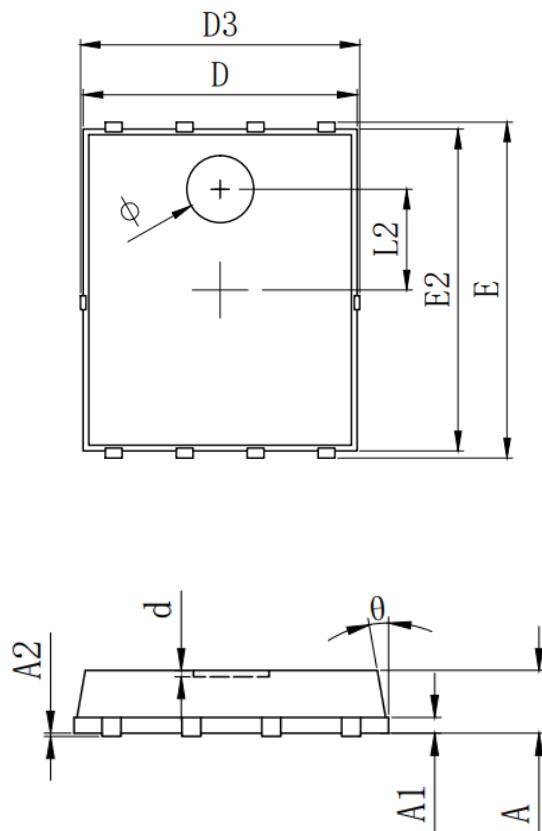
40V N&P-Channel Trench Power MOSFET

P-Channel Typical Electrical And Thermal Characteristics (Curves)





PDFN5X6-8L Package Information



SYMBOL	MILLIMETER		
	MIN	Typ.	MAX
A	0.900	1.000	1.100
A1	0.254 REF.		
A2	0~0.05		
D	4.824	4.900	4.976
D1	1.605	1.705	1.805
D2	0.500	0.600	0.700
D3	4.924	5.000	5.076
E	5.924	6.000	6.076
E1	3.375	3.475	3.575
E2	5.674	5.750	5.826
b	0.350	0.400	0.450
e	1.270 TYP.		
L	0.534	0.610	0.686
L1	0.424	0.500	0.576
L2	1.800 REF.		
k	1.190	1.290	1.390
H	0.549	0.625	0.701
θ	8°	10°	12°
Φ	1.100	1.200	1.300
d			0.100



Attention

This product described in this document can not be used in life support devices or systems, aircraft's control systems, and other applications whose failure can be reasonably expected to result in serious physical and/or material damage, apart from that when an application agreement is signed between customer and Wuxi Shangjia Semiconductor.

The performances and characteristics of this product in the independent testing state are displayed in this document. Wuxi Shangjia Semiconductor can't guarantee of the performances and characteristics of this described product that mounted in the customer's products or equipments as same as that in the independent testing state. So the customer should evaluate and test devices mounted in the customer's products or equipments.

Wuxi Shangjia Semiconductor reserves the right to improve the designs, functions and reliability of this product and modify any and all information described in this document without notice customer, apart from that when an notice agreement is signed between customer and Wuxi Shangjia Semiconductor.

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Wuxi Shangjia Semiconductor hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.