

# **60V N-Channel Trench Power MOSFET**

### **General Description**

The SJD68N058 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 10V. This device is suitable for use as a wide variety of applications.

#### Features

- Low Gate Charge
- 100% UIS Tested, 100% DVDS Tested
- High Power and current handing capability
- Lead free product is acquired

### Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

### Key Performance Parametes

Parameter	Value	Unit
V <sub>DS</sub>	68	V
R <sub>DS(ON)_TYP</sub>	5.6	mΩ
ID	82	А
Q <sub>G</sub>	90	nC



Schematic Diagram

TO-252(DPAK) top view

#### Package Marking and Ordering Information

Device/Ordering Code	Marking	Package	Packing	Reel Size	Tape width	Quantity
SJD68N058	SJD68N058	T0-252	Таре	١	١	2500 Pcs

### Table 1. Absolute Maximum Ratings ( $T_c=25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Limit	Unit
V <sub>DS</sub>	Drain-Source Voltage (V <sub>GS</sub> =0V)	68	V
Vgs	Gate-Source Voltage (V <sub>DS</sub> =0V)	±20	V
I	Drain Current-Continuous(Tc=25°C)	82	А
I <sub>D</sub>	Drain Current-Continuous(Tc=100℃)	52	А
IDM (pluse)	Drain Current-Continuous@ Current-Pulsed (Note 1)	328	А
P	Maximum Power Dissipation(Tc=25°C)	94	W
Po	Maximum Power Dissipation(Tc=100°C)	38	W
Eas	Avalanche energy (Note 2)	441	mJ
$T_J, T_{STG}$	Operating Junction and Storage Temperature Range	-55 To 150	°C

#### Table 2. Thermal Characteristic

Symbol	Parameter	Тур	Max	Unit
R JC	Thermal Resistance, Junction-to-Case		1.33	°C/W



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### Table 3. Electrical Characteristics (T\_J=25 $^{\circ}$ C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
On/Off States						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V I <sub>D</sub> =250µA	68			V
		V <sub>DS</sub> =30V, V <sub>GS</sub> =0V TJ=25℃			500	nA
IDSS	Zero Gate Voltage Drain Current	V <sub>DS</sub> =68V, V <sub>GS</sub> =0V T <sub>J</sub> =125℃			500	nA
Igss	Gate-Body Leakage Current	$V_{GS}=\pm 10V, V_{DS}=0V$			±100	nA
$V_{GS(th)}$	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250µA	2		4	V
gfs	Forward Transconductance	V <sub>DS</sub> =10V, I <sub>D</sub> =20A		36.5		S
RDS(ON)	Drain-Source On-State Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =20A T <sub>J</sub> =25℃		5.8	7	mΩ
Dynamic Chara	acteristics		•			
Ciss	Input Capacitance			4724		pF
Coss	Output Capacitance	V <sub>DS</sub> =30V,V <sub>GS</sub> =0V, f=1.0MHz		225		pF
Crss	Reverse Transfer Capacitance			207		pF
Rg	Gate resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1.0MHz		0.73		Ω
Switching Para	meters		•			
t <sub>d(on)</sub>	Turn-on Delay Time			9		nS
tr	Turn-on Rise Time	V <sub>GS</sub> =10V, V <sub>DS</sub> =30V, R <sub>L</sub> =1.5Ω, R <sub>GEN</sub> =6Ω		7		nS
$t_{d(off)}$	Turn-Off Delay Time			40		nS
tr	Turn-Off Fall Time			15		nS
Qg	Total Gate Charge			90		nC
Q <sub>gs</sub>	Gate-Source Charge	V <sub>GS</sub> =10V, V <sub>DS</sub> =30V, I <sub>D</sub> =20A		10		nC
Q <sub>gd</sub>	Gate-Drain Charge			18		nC
Source-Drain D	biode Characteristics					
I <sub>SD</sub>	Source-Drain Current (Body Diode)				82	Α
V <sub>SD</sub>	Forward on Voltage (Note 3)	V <sub>GS</sub> =0V, I <sub>S</sub> =20A		1	0.99	V
trr	Reverse Recovery Time	IF=20A, dI/dt=100A/ s		33		ns
Qrr	Reverse Recovery Charge	I⊧=20A, dI/dt=100A/ s		46		nC

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.

Notes 2.E<sub>AS</sub> condition:  $T_J=25^{\circ}C$ ,  $V_{DD}=40V$ ,  $V_G=10V$ ,  $Rg=25\Omega$ , L=0.5mH.

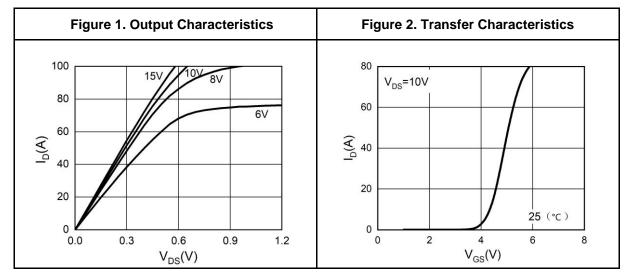
Notes 3.Repetitive Rating: Pulse width limited by maximum junction temperature.

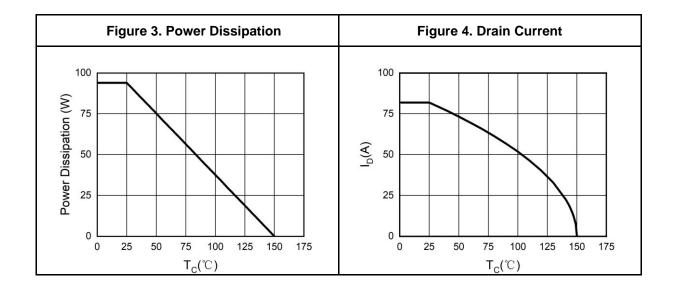


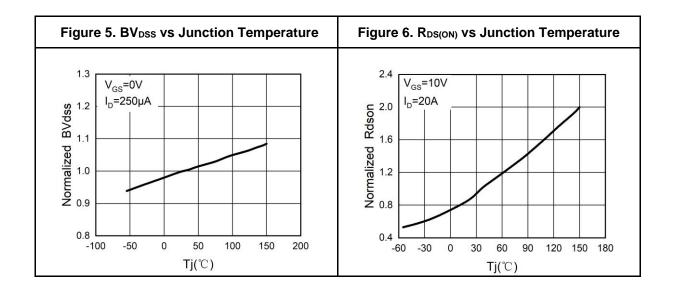
## SJD68N058

# **60V N-Channel Trench Power MOSFET**

### **Typical Electrical And Thermal Characteristics (Curves)**





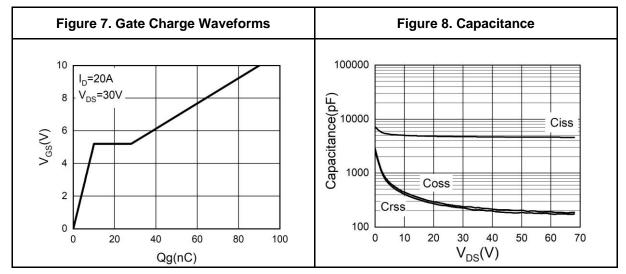


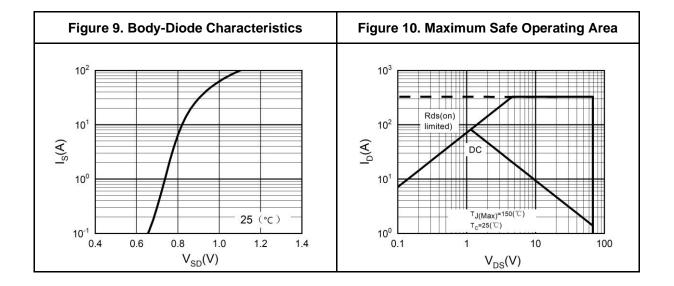


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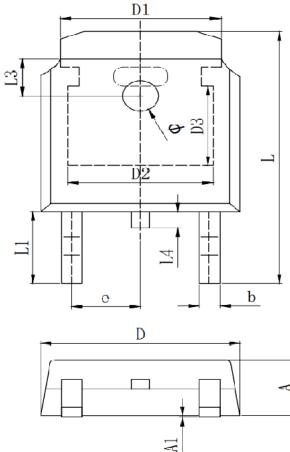


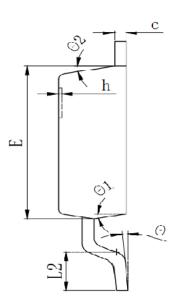




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# **TO-252 Package Information**





Symbol	Dimensions In Millimeters				
Symbol	Min.	Тур.	Max.		
A	2.200	2.300	2.400		
A1	0.000		0.127		
b	0.640	0.690	0.740		
c(电镀后)	0.460	0.520	0.580		
D	6.500	6.600	6.700		
D1		5.334 REF			
D2		4.826 REF			
D3	3.166 REF				
E	6.000	6.100	6.200		
е		2.286 TYP			
h	0.000	0.100	0.200		
L	9.900	10.100	10.300		
L1		2.888 REF			
L2	1.400	1.550	1.700		
L3		1.600 REF			
L4	0.600	0.800	1.000		
Φ	1.100	1.200	1.300		
θ	0°		8°		
θ1		9° TYP			
θ2		9° TYP			



### Attention

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