



60V P-Channel Trench Power MOSFET

General Description

The SJD60P730 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as -4.5V. This device is suitable for use as a wide variety of applications.

Features

- Low Gate Charge
- 100% UIS Tested, 100% DVDS Tested
- High Power and current handing capability
- Lead free product is acquired

Application

- High side switch for full bridge converter
- DC/DC converter for LCD display

Key Performance Parametes

Parameter	Value	Unit
V_{DS}	-60	V
$R_{DS(ON_TYP)}$	81	m Ω
I_D	-13.6	A
Q_G	23.7	nC



Package Marking and Ordering Information

Device/Ordering Code	Marking	Package	Packing	Reel Size	Tape width	Quantity
SJD60P730	SJD60P730	TO-252	Tape	\	\	2500 Pcs

Table 1. Absolute Maximum Ratings ($T_C=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Limit	Unit
V_{DS}	Drain-Source Voltage ($V_{GS}=0V$)	-60	V
V_{GS}	Gate-Source Voltage ($V_{DS}=0V$)	± 20	V
I_D	Drain Current-Continuous($T_C=25^\circ\text{C}$)	-13.6	A
	Drain Current-Continuous($T_C=100^\circ\text{C}$)	-9.4	A
I_{DM} (pluse)	Drain Current-Continuous@ Current-Pulsed (Note 1)	-54	A
P_D	Maximum Power Dissipation($T_C=25^\circ\text{C}$)	40.5	W
	Maximum Power Dissipation($T_C=100^\circ\text{C}$)	20.3	W
E_{AS}	Avalanche energy (Note 2)	56	mJ
T_J, T_{STG}	Operating Junction and Storage Temperature Range	-55 To 150	$^\circ\text{C}$

Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case		3.7	$^\circ\text{C/W}$



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Table 3. Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-60			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=-60V, V_{GS}=0V, T_J=25^\circ\text{C}$			-1	μA
		$V_{DS}=-60V, V_{GS}=0V, T_J=125^\circ\text{C}$			-100	μA
I_{GSS}	Gate-Body Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$			± 100	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1		-2.5	V
g_{FS}	Forward Transconductance	$V_{DS}=-5V, I_D=-10A$		15		S
$R_{DS(ON)}$	Drain-Source On-State Resistance	$V_{GS}=-10V, I_D=-10A, T_J=25^\circ\text{C}$		81	105	m Ω
$R_{DS(ON)}$	Drain-Source On-State Resistance	$V_{GS}=-4.5V, I_D=-8A, T_J=25^\circ\text{C}$		97	129	m Ω
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS}=-30V, V_{GS}=0V, f=1.0\text{MHz}$		1450		pF
C_{oss}	Output Capacitance			48		pF
C_{rss}	Reverse Transfer Capacitance			35		pF
Switching Parameters						
$t_{d(on)}$	Turn-on Delay Time	$V_{GS}=-10V, V_{DS}=-30V, R_L=3\Omega, R_{GEN}=3\Omega$		9.7		nS
t_r	Turn-on Rise Time			5.5		nS
$t_{d(off)}$	Turn-Off Delay Time			29		nS
t_f	Turn-Off Fall Time			6		nS
Q_g	Total Gate Charge	$V_{GS}=-10V, V_{DS}=-30V, I_D=-10A$		23.7		nC
Q_{gs}	Gate-Source Charge			2.1		nC
Q_{gd}	Gate-Drain Charge			7.2		nC
Source-Drain Diode Characteristics						
I_{SD}	Source-Drain Current (Body Diode)				-13.6	A
V_{SD}	Forward on Voltage (Note 3)	$V_{GS}=0V, I_S=-10A$			-1.2	V
t_{rr}	Reverse Recovery Time	$I_F=-10A, di/dt=100A/\mu s$		34		ns
Q_{rr}	Reverse Recovery Charge	$I_F=-10A, di/dt=100A/\mu s$		37		nC

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.

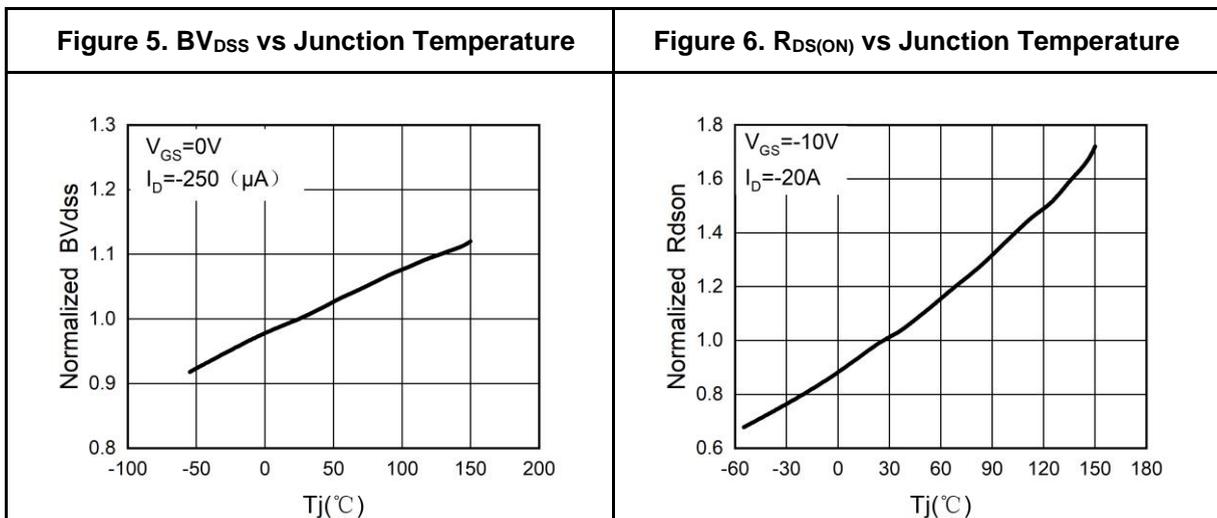
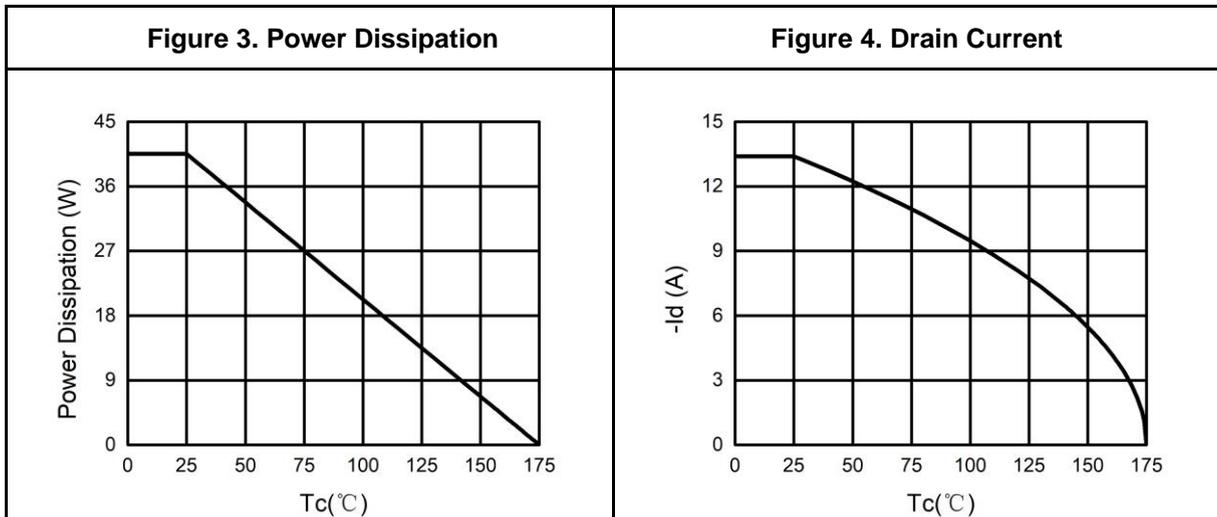
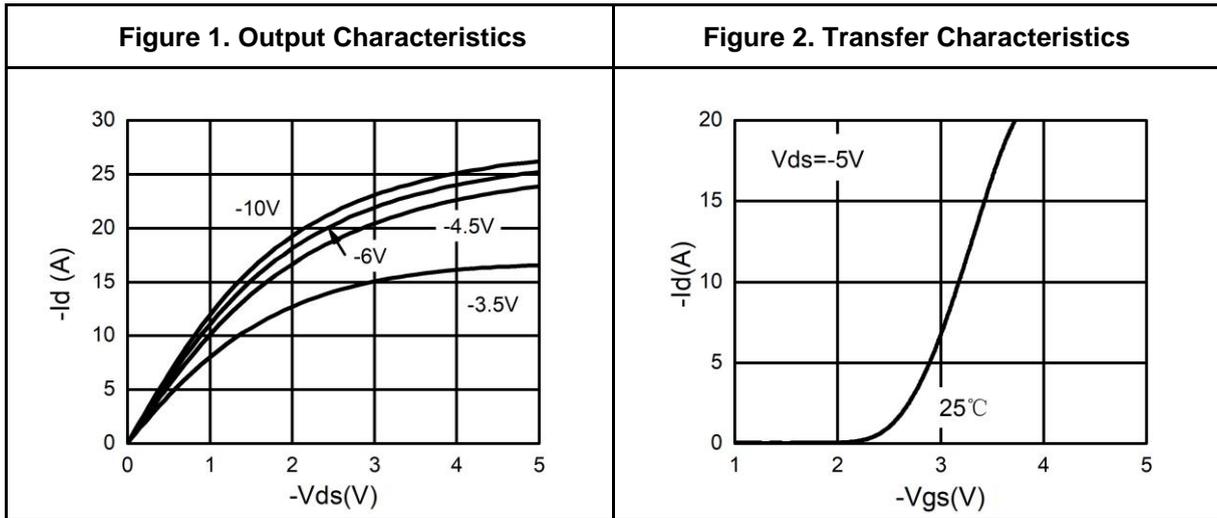
Notes 2.EAS condition: $T_J=25^\circ\text{C}, V_{DD}=40V, V_G=-10V, R_g=25\Omega, L=0.5\text{mH}$.

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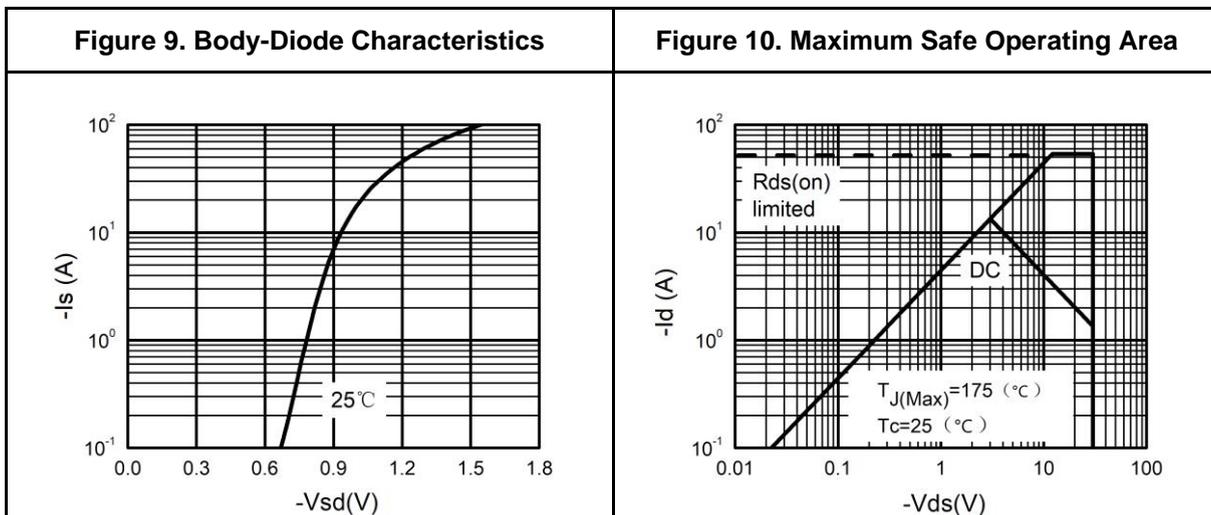
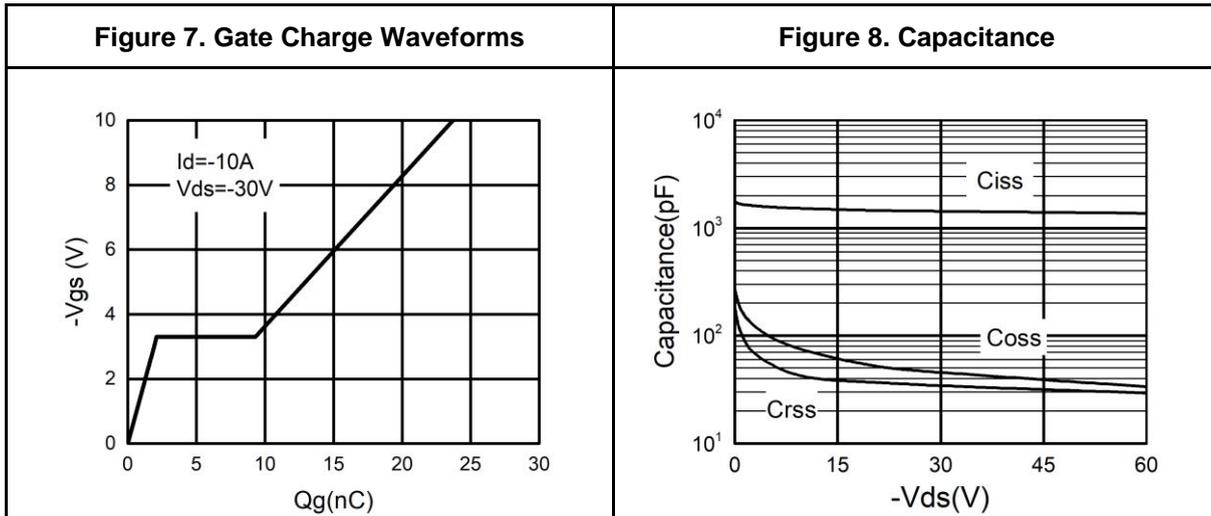
Typical Electrical And Thermal Characteristics (Curves)





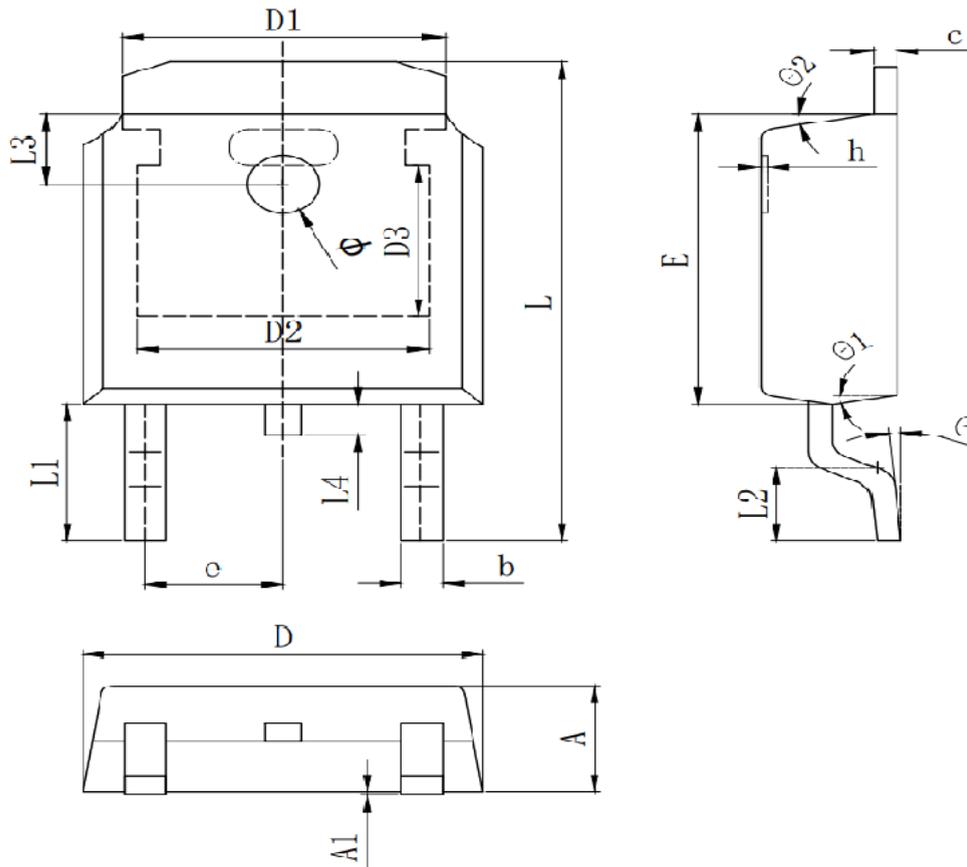
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Typical Electrical And Thermal Characteristics (Curves)





TO-252 Package Information



Symbol	Dimensions In Millimeters		
	Min.	Typ.	Max.
A	2.200	2.300	2.400
A1	0.000		0.127
b	0.640	0.690	0.740
c(电镀后)	0.460	0.520	0.580
D	6.500	6.600	6.700
D1		5.334 REF	
D2		4.826 REF	
D3		3.166 REF	
E	6.000	6.100	6.200
e		2.286 TYP	
h	0.000	0.100	0.200
L	9.900	10.100	10.300
L1		2.888 REF	
L2	1.400	1.550	1.700
L3		1.600 REF	
L4	0.600	0.800	1.000
Φ	1.100	1.200	1.300
θ	0°		8°
θ1		9° TYP	
θ2		9° TYP	



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