60V N-Channel Trench Power MOSFET

General Description

The SJD60N120 uses advanced trench technology to provide excellent R_{DS(ON)}, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a wide variety of applications.

Features

- Low Gate Charge
- 100% UIS Tested, 100% DVDS Tested
- High Power and current handing capability
- Lead free product is acquired

Application

- 48V E-bike controller
- Uninterruptible power supply
- Hard switched and high frequency circuits

Key Performance Parametes

Parameter	Value	Unit
V _{DS}	60	V
R _{DS(ON)_TYP}	12.6	mΩ
I _D	41	А
Q _G	45.4	nC



Package Marking and Ordering Information

Device/Ordering Code	Marking	Package	Packing	Reel Size	Tape width	Quantity
SJD60N120	SJD60N120	TO-252	Tape	\	/	2500 Pcs

Table 1. Absolute Maximum Ratings (T_C=25℃ unless otherwise noted)

Symbol	Parameter	Limit	Unit
V _{DS}	Drain-Source Voltage (V _{GS} =0V)	60	V
V _{GS}	Gate-Source Voltage (V _{DS} =0V)	±20	V
1-	Drain Current-Continuous(Tc=25°C)	41	А
I _D	Drain Current-Continuous(T _C =100℃)	26	А
IDM (pluse)	Drain Current-Continuous@ Current-Pulsed (Note 1)	164	А
D	Maximum Power Dissipation(T _C =25°C)	60	W
P _D	Maximum Power Dissipation(Tc=100°C)	24	W
Eas	Avalanche energy (Note 2)	110	mJ
TJ, TSTG	Operating Junction and Storage Temperature Range	-55 To 150	င

Table 2. Thermal Characteristic

Symbol	Parameter	Тур	Max	Unit
Rejc	Thermal Resistance, Junction-to-Case		2.1	°C/W



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Table 3. Electrical Characteristics (T_J=25℃ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
On/Off States						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V I _D =250μA	60			V
	7 0 1 1/1 5 1 0 1	V _{DS} =60V, V _{GS} =0V T _J =25°C			1	μA
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =60V, V _{GS} =0V T _J =125℃			100	μΑ
Igss	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V			±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	1		2	V
g FS	Forward Transconductance	V _{DS} =10V, I _D =20A		21		S
R _{DS(ON)}	Drain-Source On-State Resistance	V _{GS} =10V, I _D =20A T _J =25℃		12.6	16.3	mΩ
R _{DS(ON)}	Drain-Source On-State Resistance	V _{GS} =4.5V, I _D =15A T _J =25°C		15.2	20.2	mΩ
Dynamic Chara	acteristics		Į.	1		I
Ciss	Input Capacitance	V 90//V 0V		2098		pF
C_{oss}	Output Capacitance	V _{DS} =30V,V _{GS} =0V, f=1.0MHz		103		pF
Crss	Reverse Transfer Capacitance	f=1.0MHz		93		pF
Rg	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1.0MHz		1.5		Ω
Switching Para	meters		Į.	1		I
t _{d(on)}	Turn-on Delay Time			10		nS
t _r	Turn-on Rise Time	V _{GS} =10V, V _{DS} =30V,		7.6		nS
$t_{d(off)}$	Turn-Off Delay Time	R_L =3Ω, R_{GEN} =6Ω		56.4		nS
t _f	Turn-Off Fall Time			13.2		nS
Qg	Total Gate Charge			45.4		nC
Q _{gs}	Gate-Source Charge	V _{GS} =10V, V _{DS} =30V, I _D =10A		9.6		nC
Q_{gd}	Gate-Drain Charge			6.4		nC
Source-Drain D	Diode Characteristics		Į.			I
I _{SD}	Source-Drain Current (Body Diode)				41	Α
V _{SD}	Forward on Voltage (Note 3)	V _{GS} =0V, I _S =20A			1.2	V
t _{rr}	Reverse Recovery Time	I _F =10A, dI/dt=100A/μs		20		ns
Qrr	Reverse Recovery Charge	I _F =10A, dI/dt=100A/μs		19.8		nC
		1	1	1		

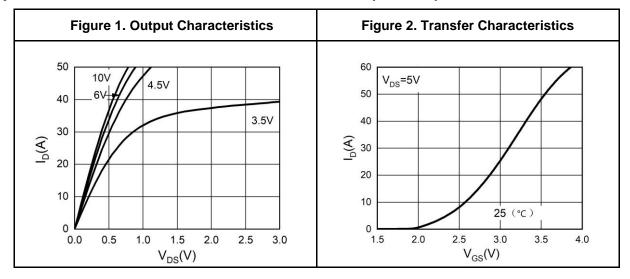
Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.

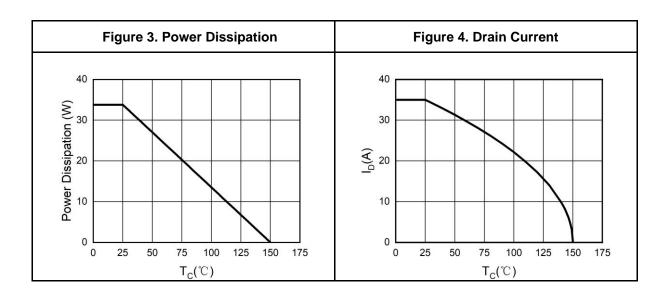
Notes 2.E_{AS} condition: $T_J=25^{\circ}C$, $V_{DD}=40V$, $V_G=10V$, $Rg=25\Omega$, L=0.5mH.

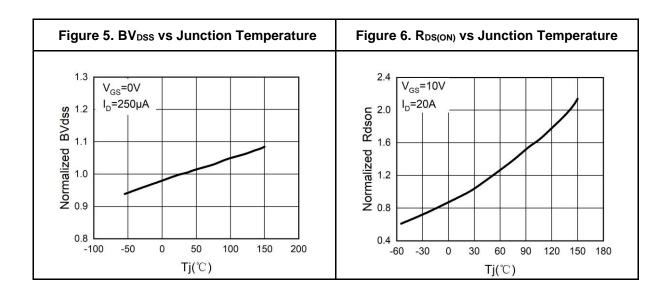
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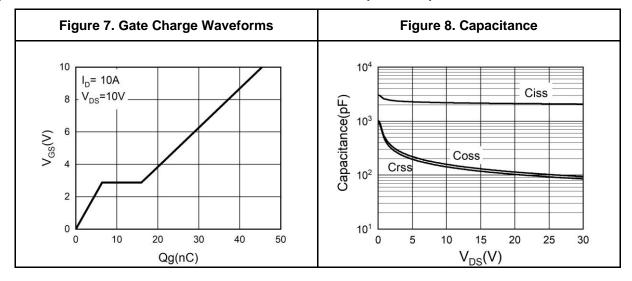
Typical Electrical And Thermal Characteristics (Curves)

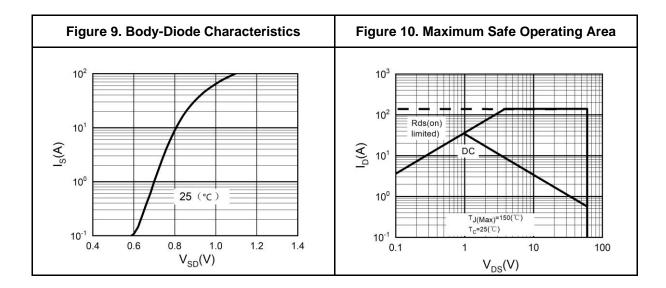






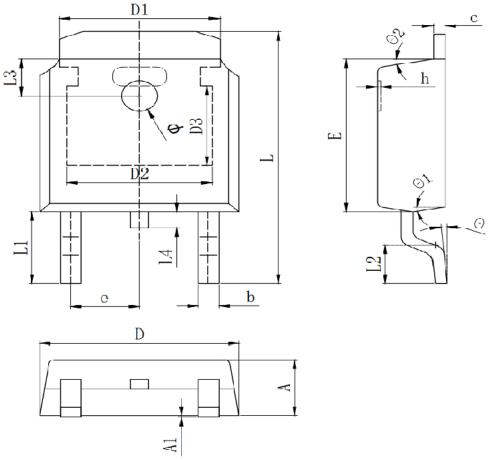
Typical Electrical And Thermal Characteristics (Curves)







TO-252 Package Information



Sumbal	Dimensions In Millimeters				
Symbol	Min.	Тур.	Max.		
А	2.200	2.300	2.400		
A1	0.000		0.127		
b	0.640	0.690	0.740		
c(电镀后)	0.460	0.520	0.580		
D	6.500	6.600	6.700		
D1		5.334 REF			
D2		4.826 REF			
D3		3.166 REF			
E	6.000	6.100	6.200		
е		2.286 TYP			
h	0.000	0.100	0.200		
L	9.900	10.100	10.300		
L1		2.888 REF			
L2	1.400	1.550	1.700		
L3		1.600 REF			
L4	0.600	0.800	1.000		
Ф	1.100	1.200	1.300		
θ	0°		8°		
θ1	9° TYP				
θ2	9° TYP				

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