

**General Description**

The SJD4606 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as  $\pm 4.5V$ . This device is suitable for use as a wide variety of applications.

**Features**

- Low Gate Charge
- High Power and current handing capability
- Lead free product is acquired

**Application**

- Battery Protection
- Power Management
- Load Switch

**Key Performance Parametes**

Parameter	Value	Value	Unit
$V_{DS}$	30	-30	V
$R_{DS(ON)}\_TYP$	12.7	30.4	mΩ
$I_D$	33	-21	A
$Q_G$	14	16	nC

**Package Marking and Ordering Information**

Device/Ordering Code	Marking	Package	Packing	Reel Size	Tape width	Quantity
SJD4606	SJD4606	TO-252-4L	Tape	\	\	2500 Pcs

**Table 1. Absolute Maximum Ratings ( $T_A=25^\circ C$  unless otherwise noted)**

Symbol	Parameter	N Limit	P Limit	Unit
$V_{DS}$	Drain-Source Voltage ( $V_{GS}=0V$ )	30	-30	V
$V_{GS}$	Gate-Source Voltage ( $V_{DS}=0V$ )	$\pm 20$	$\pm 20$	V
$I_D$	Drain Current-Continuous( $T_c=25^\circ C$ )	33	-21	A
	Drain Current-Continuous( $T_c=100^\circ C$ )	21	-13	A
$I_{DM}$ (pulse)	Drain Current-Continuous@ Current-Pulsed (Note 1)	132	-84	A
$P_D$	Maximum Power Dissipation( $T_c=25^\circ C$ )	27	26.6	W
	Maximum Power Dissipation( $T_c=100^\circ C$ )	11	10.6	W
$E_{AS}$	Avalanche energy (Note 2)	36	30	mJ
$T_J, T_{STG}$	Operating Junction and Storage Temperature Range	-55 To 150		°C

**Table 2. Thermal Characteristic**

Symbol	Parameter	N Max	P Max	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to- Case	4.6	4.7	°C/W



## 30V N&amp;P-Channel Trench Power MOSFET

Table 3. N-Channel Electrical Characteristics ( $T_J=25^\circ\text{C}$  unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>On/Off States</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ $I_{\text{D}}=250\mu\text{A}$	30			V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{DS}}=30\text{V}$ , $V_{\text{GS}}=0\text{V}$ $T_J=25^\circ\text{C}$			1	$\mu\text{A}$
		$V_{\text{DS}}=30\text{V}$ , $V_{\text{GS}}=0\text{V}$ $T_J=125^\circ\text{C}$			100	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$			$\pm 100$	nA
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$ , $I_{\text{D}}=250\mu\text{A}$	1.0		2.5	V
$g_{\text{FS}}$	Forward Transconductance	$V_{\text{DS}}=5\text{V}$ , $I_{\text{D}}=3\text{A}$		6.4		S
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance	$V_{\text{GS}}=10\text{V}$ , $I_{\text{D}}=3\text{A}$ $T_J=25^\circ\text{C}$		12.7	15.9	$\text{m}\Omega$
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance	$V_{\text{GS}}=4.5\text{V}$ , $I_{\text{D}}=2\text{A}$ $T_J=25^\circ\text{C}$		19.2	25.5	$\text{m}\Omega$
<b>Dynamic Characteristics</b>						
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=15\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1.0\text{MHz}$		600		pF
$C_{\text{oss}}$	Output Capacitance			75		pF
$C_{\text{rss}}$	Reverse Transfer Capacitance			61		pF
$R_g$	Gate resistance	$V_{\text{GS}}=0\text{V}$ , $V_{\text{DS}}=0\text{V}$ , $f=1.0\text{MHz}$		1.2		$\Omega$
<b>Switching Parameters</b>						
$t_{\text{d}(\text{on})}$	Turn-on Delay Time	$V_{\text{GS}}=10\text{V}$ , $V_{\text{DS}}=15\text{V}$ , $R_L=5\Omega$ , $R_{\text{GEN}}=3\Omega$		4.5		nS
$t_r$	Turn-on Rise Time			2.5		nS
$t_{\text{d}(\text{off})}$	Turn-Off Delay Time			14.5		nS
$t_f$	Turn-Off Fall Time			3.5		nS
$Q_g$	Total Gate Charge	$V_{\text{GS}}=10\text{V}$ , $V_{\text{DS}}=15\text{V}$ , $I_{\text{D}}=3\text{A}$		14		nC
$Q_{\text{gs}}$	Gate-Source Charge			2		nC
$Q_{\text{gd}}$	Gate-Drain Charge			3		nC
<b>Source-Drain Diode Characteristics</b>						
$I_{\text{SD}}$	Source-Drain Current (Body Diode)				33	A
$V_{\text{SD}}$	Forward on Voltage (Note 3)	$V_{\text{GS}}=0\text{V}$ , $I_{\text{S}}=3\text{A}$			1.2	V
$t_{\text{rr}}$	Reverse Recovery Time	$I_F=3\text{A}$ , $dI/dt=500\text{A}/\mu\text{s}$		10		ns
$Q_{\text{rr}}$	Reverse Recovery Charge	$I_F=3\text{A}$ , $dI/dt=500\text{A}/\mu\text{s}$		5		nC

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.

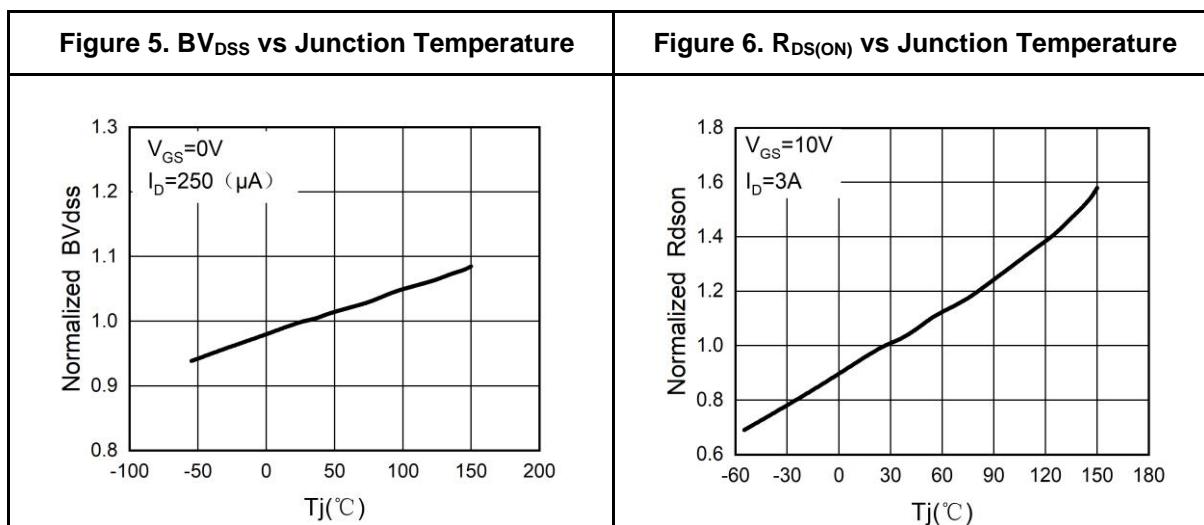
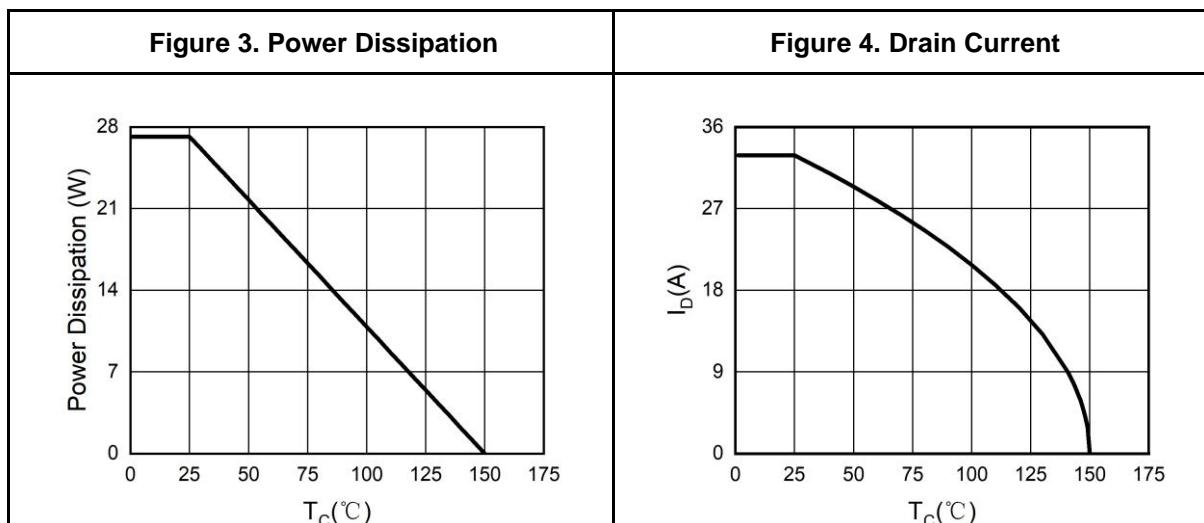
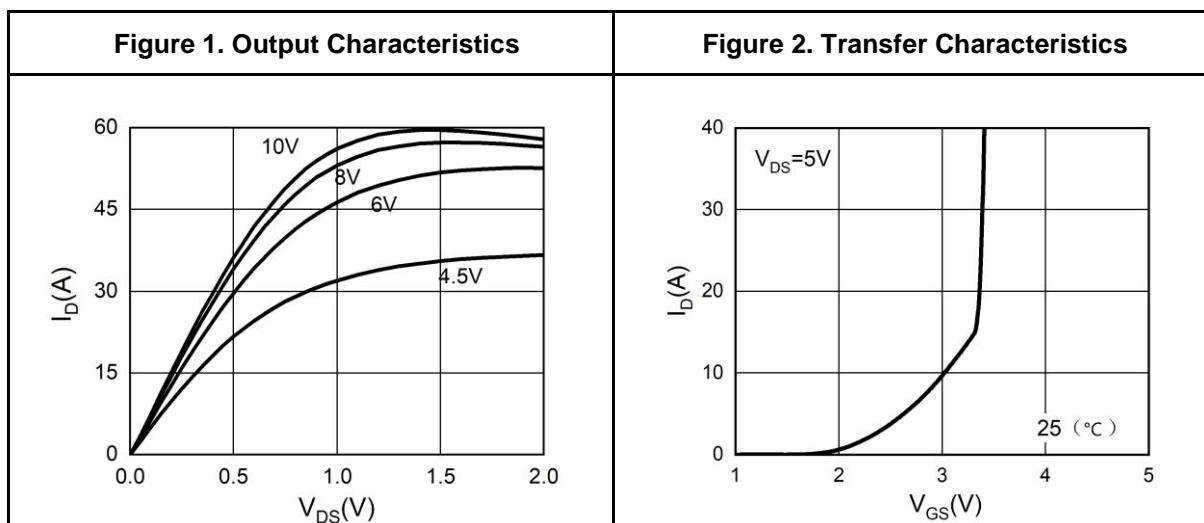
Notes 2.E<sub>AS</sub> condition:  $T_J=25^\circ\text{C}$ ,  $V_{\text{DD}}=30\text{V}$ ,  $V_G=10\text{V}$ ,  $R_g=25\Omega$ ,  $L=0.5\text{mH}$ .

Notes 3.Repetitive Rating: Pulse width limited by maximum junction temperature.



## 30V N&amp;P-Channel Trench Power MOSFET

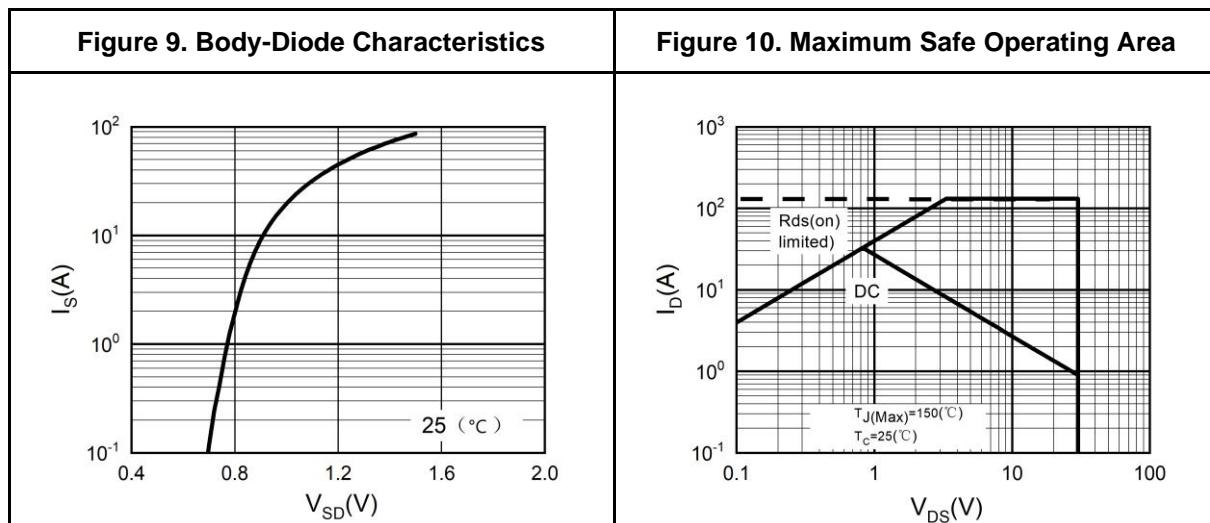
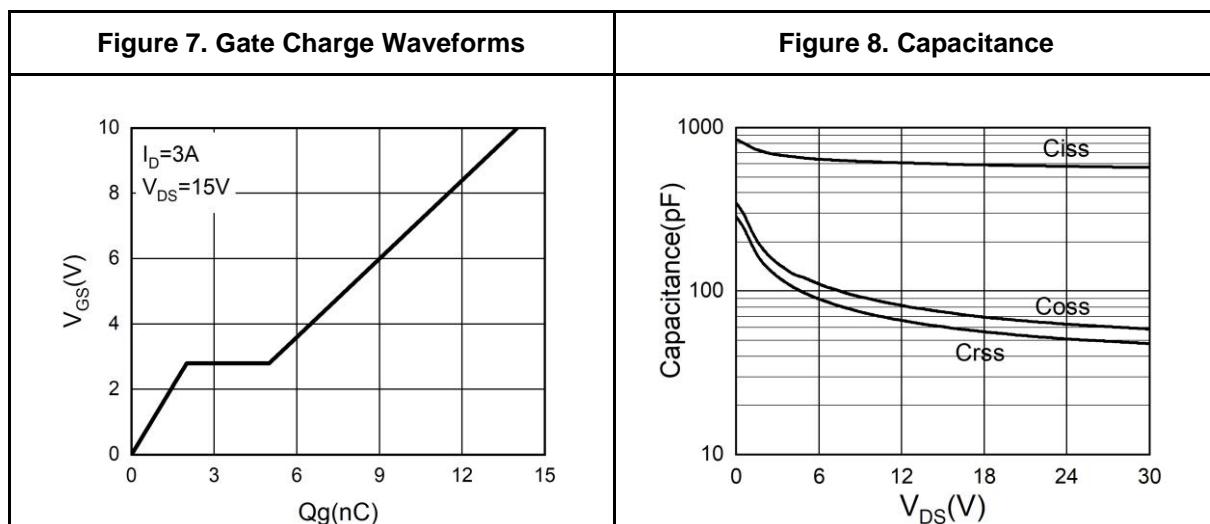
## N-Channel Typical Electrical And Thermal Characteristics (Curves)





## 30V N&amp;P-Channel Trench Power MOSFET

## N-Channel Typical Electrical And Thermal Characteristics (Curves)





## 30V N&amp;P-Channel Trench Power MOSFET

Table 4. P-Channel Electrical Characteristics ( $T_J=25^\circ\text{C}$  unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>On/Off States</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-30			V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{DS}}=-30\text{V}, V_{\text{GS}}=0\text{V} T_J=25^\circ\text{C}$			-1	$\mu\text{A}$
		$V_{\text{DS}}=-30\text{V}, V_{\text{GS}}=0\text{V} T_J=125^\circ\text{C}$			-100	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$			$\pm 100$	nA
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-1		-2.5	V
$g_{\text{FS}}$	Forward Transconductance	$V_{\text{DS}}=-5\text{V}, I_{\text{D}}=-2\text{A}$		4.8		S
$R_{\text{DS(ON)}}$	Drain-Source On-State Resistance	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-2\text{A} T_J=25^\circ\text{C}$		30.4	38	$\text{m}\Omega$
$R_{\text{DS(ON)}}$	Drain-Source On-State Resistance	$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-1.5\text{A} T_J=25^\circ\text{C}$		41.3	54.9	$\text{m}\Omega$
<b>Dynamic Characteristics</b>						
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=-15\text{V}, V_{\text{GS}}=0\text{V}, f=1.0\text{MHz}$		617		pF
$C_{\text{oss}}$	Output Capacitance			79		pF
$C_{\text{rss}}$	Reverse Transfer Capacitance			63		pF
$R_g$	Gate resistance	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=0\text{V}, f=1.0\text{MHz}$		6.5		$\Omega$
<b>Switching Parameters</b>						
$t_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{GS}}=-10\text{V}, V_{\text{DS}}=-15\text{V}, R_L=7.5\Omega, R_{\text{GEN}}=3\Omega$		7.5		nS
$t_r$	Turn-on Rise Time			5.5		nS
$t_{\text{d(off)}}$	Turn-Off Delay Time			19		nS
$t_f$	Turn-Off Fall Time			7		nS
$Q_g$	Total Gate Charge	$V_{\text{GS}}=-10\text{V}, V_{\text{DS}}=-15\text{V}, I_{\text{D}}=-2\text{A}$		16		nC
$Q_{\text{gs}}$	Gate-Source Charge			2		nC
$Q_{\text{gd}}$	Gate-Drain Charge			4		nC
<b>Source-Drain Diode Characteristics</b>						
$I_{\text{SD}}$	Source-Drain Current (Body Diode)				-21	A
$V_{\text{SD}}$	Forward on Voltage (Note 3)	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=-2\text{A}$			-1.2	V
$t_{\text{rr}}$	Reverse Recovery Time	$I_F=3\text{A}, dI/dt=500\text{A}/\mu\text{s}$		15		ns
$Q_{\text{rr}}$	Reverse Recovery Charge	$I_F=3\text{A}, dI/dt=500\text{A}/\mu\text{s}$		10		nC

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.

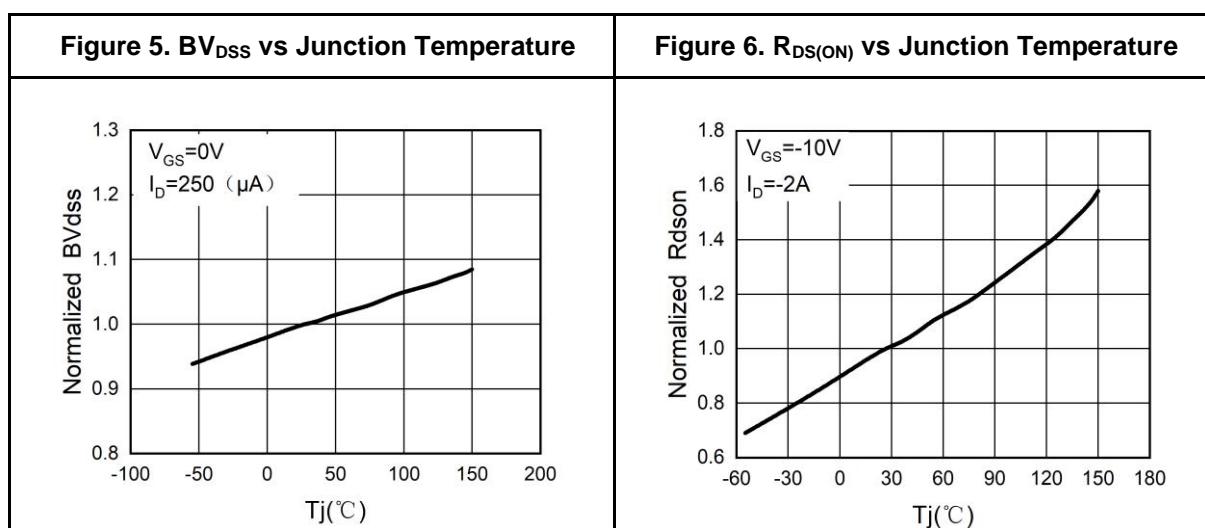
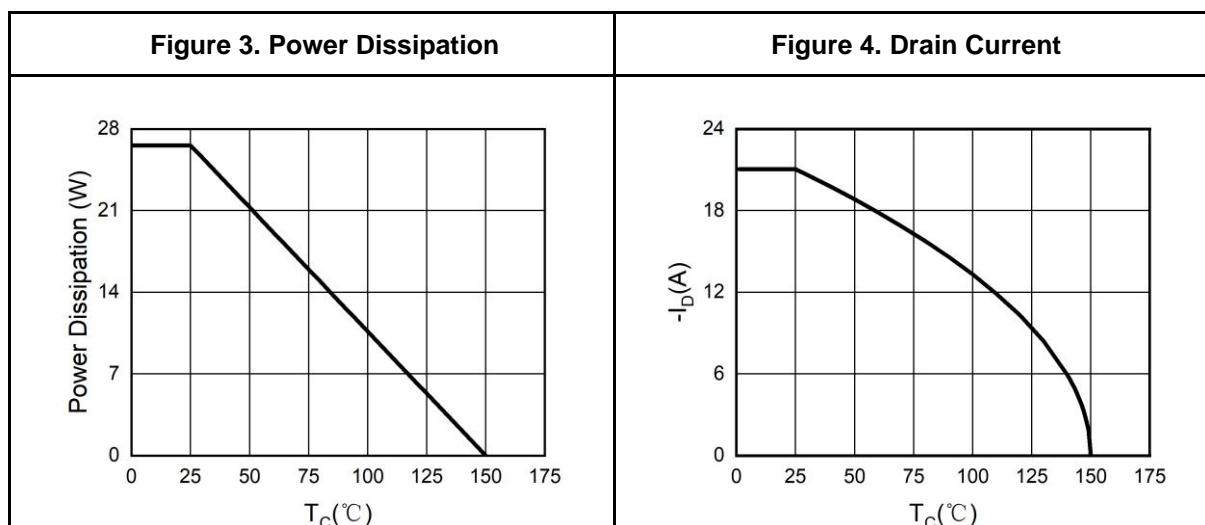
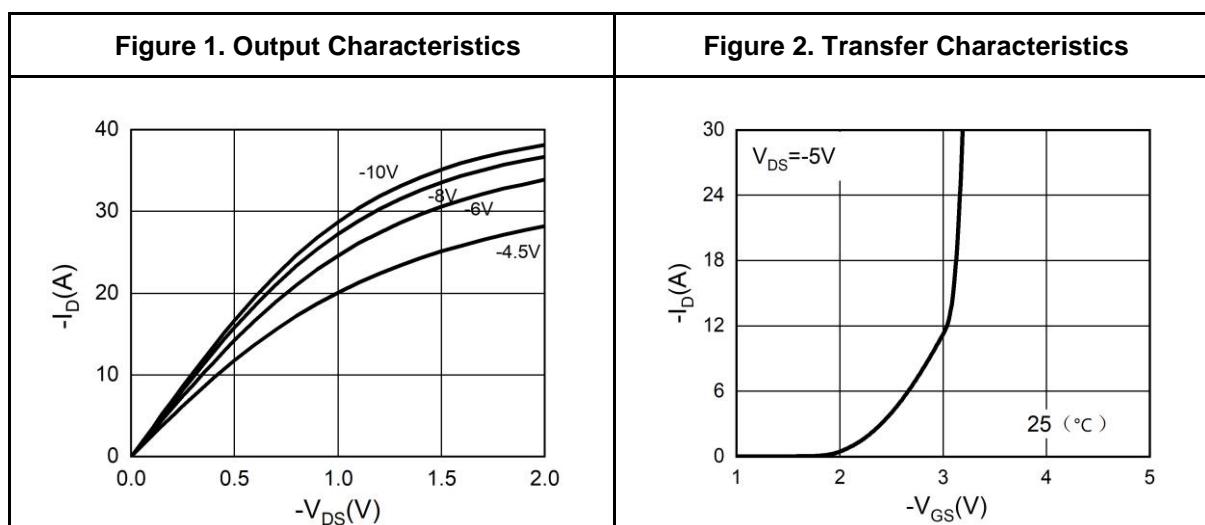
Notes 2.E<sub>AS</sub> condition:  $T_J=25^\circ\text{C}, V_{\text{DD}}=-30\text{V}, V_{\text{G}}=-10\text{V}, R_g=25\Omega, L=0.5\text{mH}$ .

Notes 3.Repetitive Rating: Pulse width limited by maximum junction temperature.



## 30V N&amp;P-Channel Trench Power MOSFET

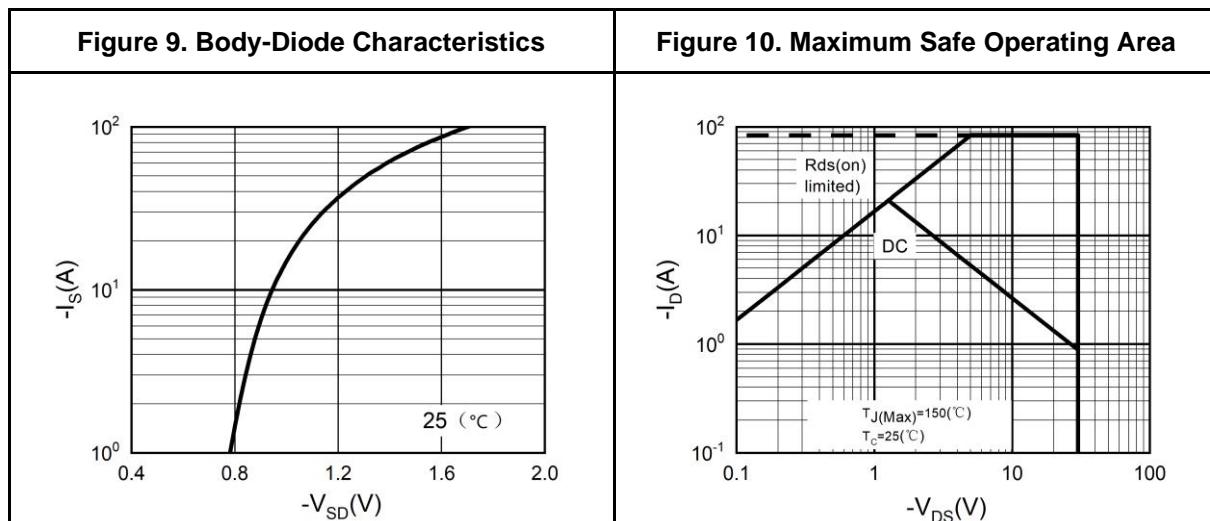
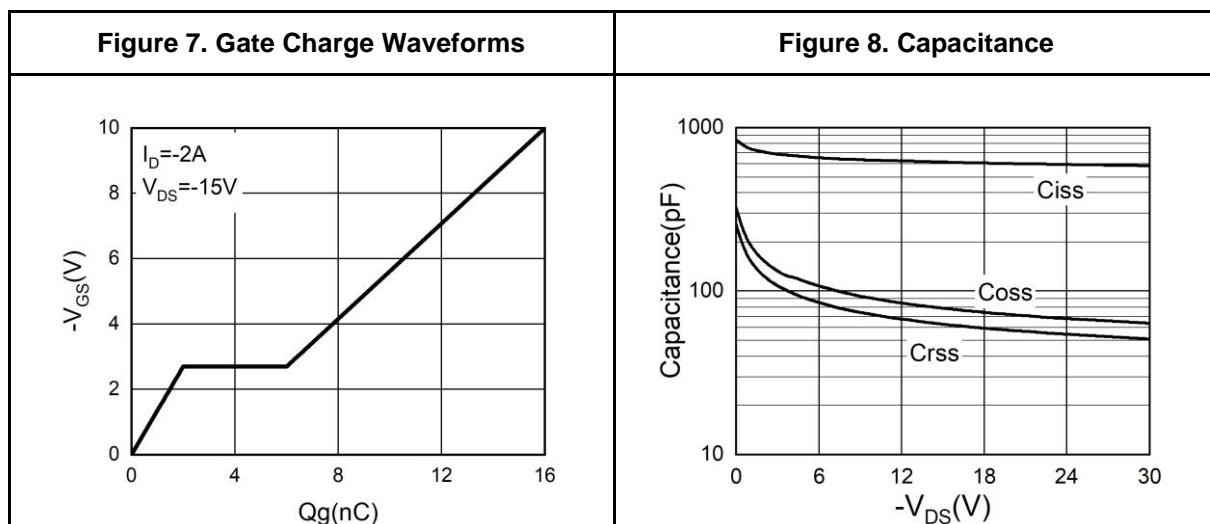
## P-Channel Typical Electrical And Thermal Characteristics (Curves)





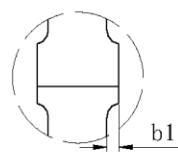
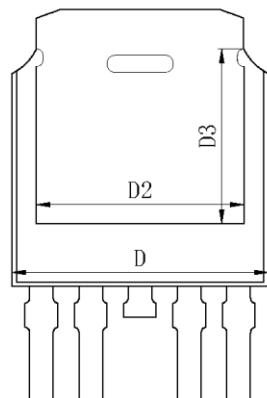
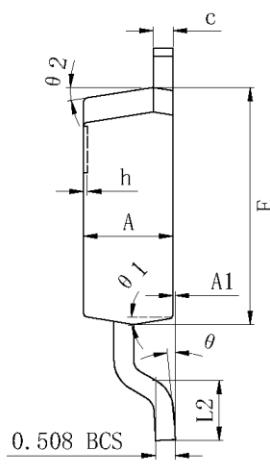
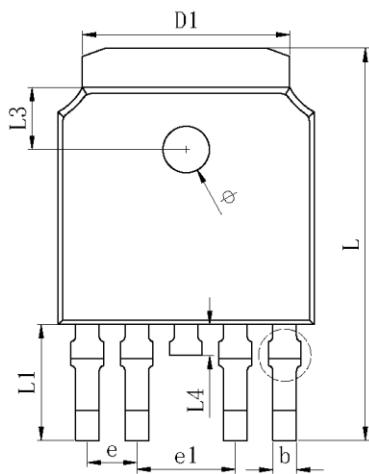
## 30V N&amp;P-Channel Trench Power MOSFET

## P-Channel Typical Electrical And Thermal Characteristics (Curves)





TO-252-4L Package Information



SYMBOL	MILLIMETER		
	MIN	Typ.	MAX
A	2.200	2.300	2.400
A1	0.000		0.127
b	0.550	0.600	0.650
b1	0.000		0.120
c(电镀后)	0.460	0.520	0.580
D	6.500	6.600	6.700
D1	5.334	REF	
D2	5.346	REF	
D3	4.490	REF	
E	6.000	6.100	6.200
e		1.270	TYP
e1		2.540	TYP
h	0.000	0.100	0.200
L	9.900	10.100	10.300
L1		2.988	REF
L2	1.400	1.550	1.700
L3		1.600	REF
L4	0.700	0.800	0.900
phi	1.100	1.200	1.300
theta	0°	9°	TYP
theta_1		9°	TYP
theta_2		9°	TYP



## Attention

This product described in this document can not be used in life support devices or systems, aircraft's control systems, and other applications whose failure can be reasonably expected to result in serious physical and/or material damage, apart from that when an application agreement is signed between customer and Wuxi Shangjia Semiconductor.

The performances and characteristics of this product in the independent testing state are displayed in this document. Wuxi Shangjia Semiconductor can't guarantee of the performances and characteristics of this described product that mounted in the customer's products or equipments as same as that in the independent testing state. So the customer should evaluate and test devices mounted in the customer's products or equipments.

Wuxi Shangjia Semiconductor reserves the right to improve the designs, functions and reliability of this product and modify any and all information described in this document without notice customer, apart from that when an notice agreement is signed between customer and Wuxi Shangjia Semiconductor.

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Wuxi Shangjia Semiconductor hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.