



30V P-Channel Trench Power MOSFET

General Description

The SJD30P310 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as -4.5V. This device is suitable for use as a wide variety of applications.

Features

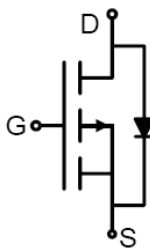
- Low Gate Charge
- 100% UIS Tested, 100% DVDS Tested
- High Power and current handling capability
- Lead free product is acquired

Application

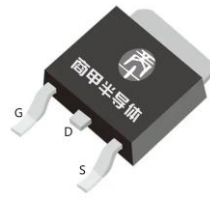
- PWM Applications
- Load Switch
- Power Management

Key Performance Parameters

Parameter	Value	Unit
V_{DS}	-30	V
$R_{DS(ON_TYP)}$	37.6	mΩ
I_D	-11.8	A
Q_G	9	nC



Schematic Diagram



TO-252(DPAK) top view



Package Marking and Ordering Information

Device/Ordering Code	Marking	Package	Packing	Reel Size	Tape width	Quantity
SJD30P310	SJD30P310	TO-252	Tape	\	\	2500 Pcs

Table 1. Absolute Maximum Ratings ($T_c=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Limit	Unit
V_{DS}	Drain-Source Voltage ($V_{GS}=0V$)	-30	V
V_{GS}	Gate-Source Voltage ($V_{DS}=0V$)	± 20	V
I_D	Drain Current-Continuous($T_c=25^\circ\text{C}$)	-11.8	A
	Drain Current-Continuous($T_c=100^\circ\text{C}$)	-7.4	A
I_{DM} (pluse)	Drain Current-Continuous@ Current-Pulsed (Note 1)	-47.2	A
P_D	Maximum Power Dissipation($T_c=25^\circ\text{C}$)	10.4	W
	Maximum Power Dissipation($T_c=100^\circ\text{C}$)	4.2	W
E_{AS}	Avalanche energy (Note 2)	25	mJ
T_J, T_{STG}	Operating Junction and Storage Temperature Range	-55 To 150	$^\circ\text{C}$

Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to- Case		12	$^\circ\text{C/W}$



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Table 3. Electrical Characteristics ($T_J=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V I _D =-250μA	-30			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =-30V, V _{GS} =0V T _J =25℃			-1	μA
		V _{DS} =-30V, V _{GS} =0V T _J =125℃			-100	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V			±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250μA	-1		-2.5	V
g _{FS}	Forward Transconductance	V _{DS} =-5V, I _D =-2A		4.5		S
R _{DS(ON)}	Drain-Source On-State Resistance	V _{GS} =-10V, I _D =-2A T _J =25℃		37.6	47	mΩ
R _{DS(ON)}	Drain-Source On-State Resistance	V _{GS} =-4.5V, I _D =-1.5A T _J =25℃		49.3	65.6	mΩ
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =-15V,V _{GS} =0V, f=1.0MHz		588		pF
C _{oss}	Output Capacitance			80		pF
C _{rss}	Reverse Transfer Capacitance			65		pF
R _g	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1.0MHz		6.7		Ω
Switching Parameters						
t _{d(on)}	Turn-on Delay Time	V _{GS} =-10V, V _{DS} =-15V, R _L =7.5Ω, R _{GEN} =3Ω		8		nS
t _r	Turn-on Rise Time			16		nS
t _{d(off)}	Turn-Off Delay Time			45		nS
t _f	Turn-Off Fall Time			34		nS
Q _g	Total Gate Charge	V _{GS} =-10V, V _{DS} =-15V, I _D =-2A		9		nC
Q _{gs}	Gate-Source Charge			2		nC
Q _{gd}	Gate-Drain Charge			2		nC
Source-Drain Diode Characteristics						
I _{SD}	Source-Drain Current (Body Diode)				-11.8	A
V _{SD}	Forward on Voltage (Note 3)	V _{GS} =0V, I _S =-20A			-1.2	V
t _{rr}	Reverse Recovery Time	I _F =-10A, dI/dt=-100A/μs		8		ns
Q _{rr}	Reverse Recovery Charge	I _F =-10A, dI/dt=-100A/μs		3		nC

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.

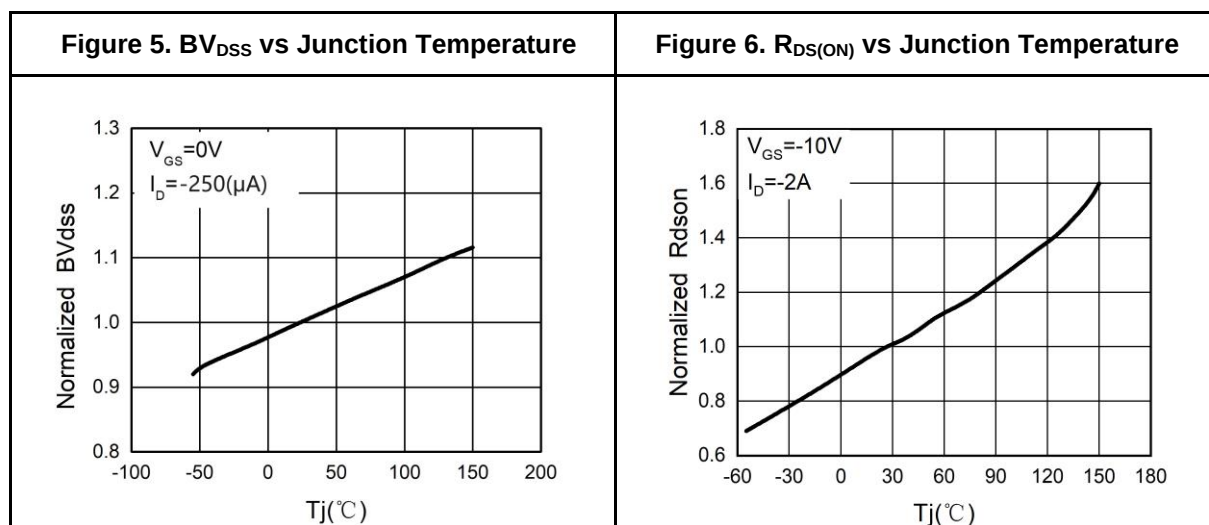
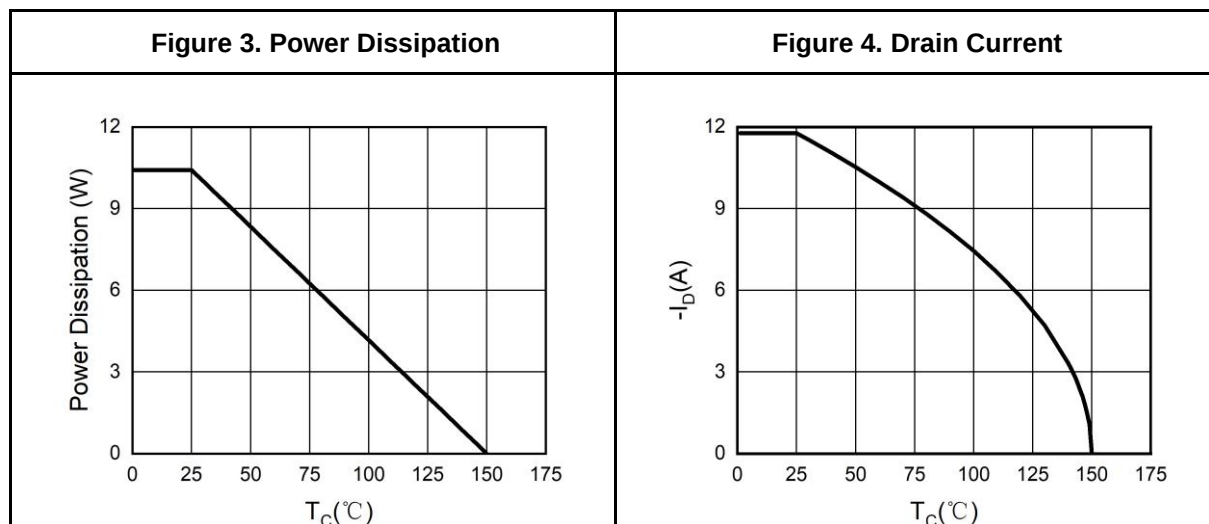
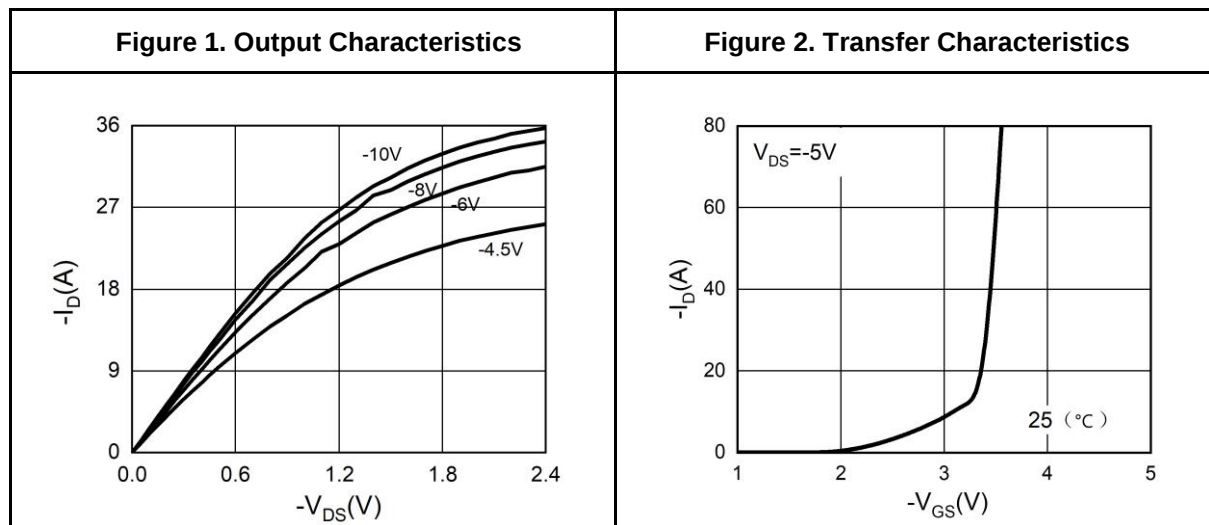
Notes 2.EAS condition: $T_J=25^{\circ}\text{C}, V_{DD}=-30V, V_G=-10V, R_g=25\Omega, L=0.5\text{mH}$.

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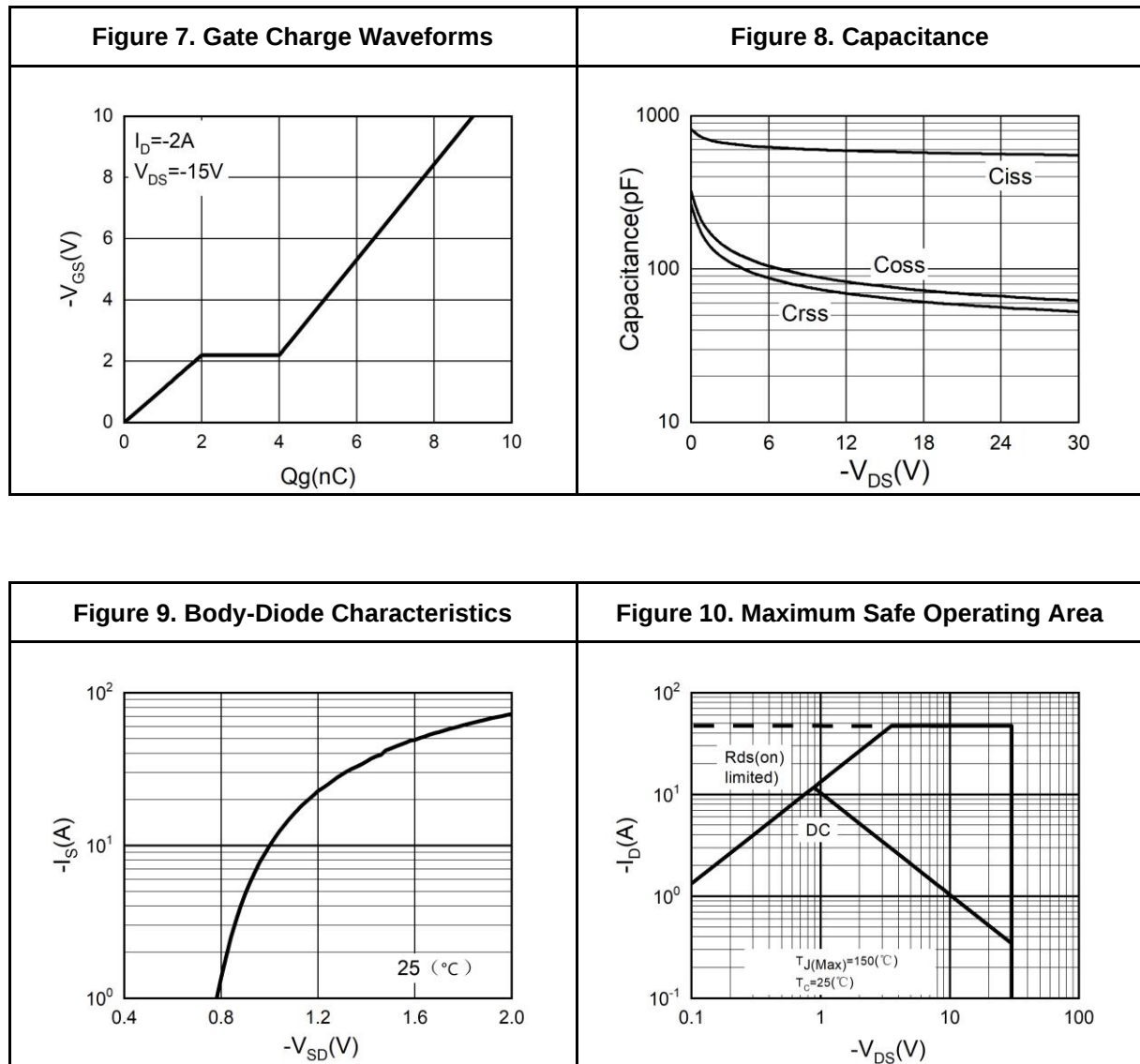
Typical Electrical And Thermal Characteristics (Curves)





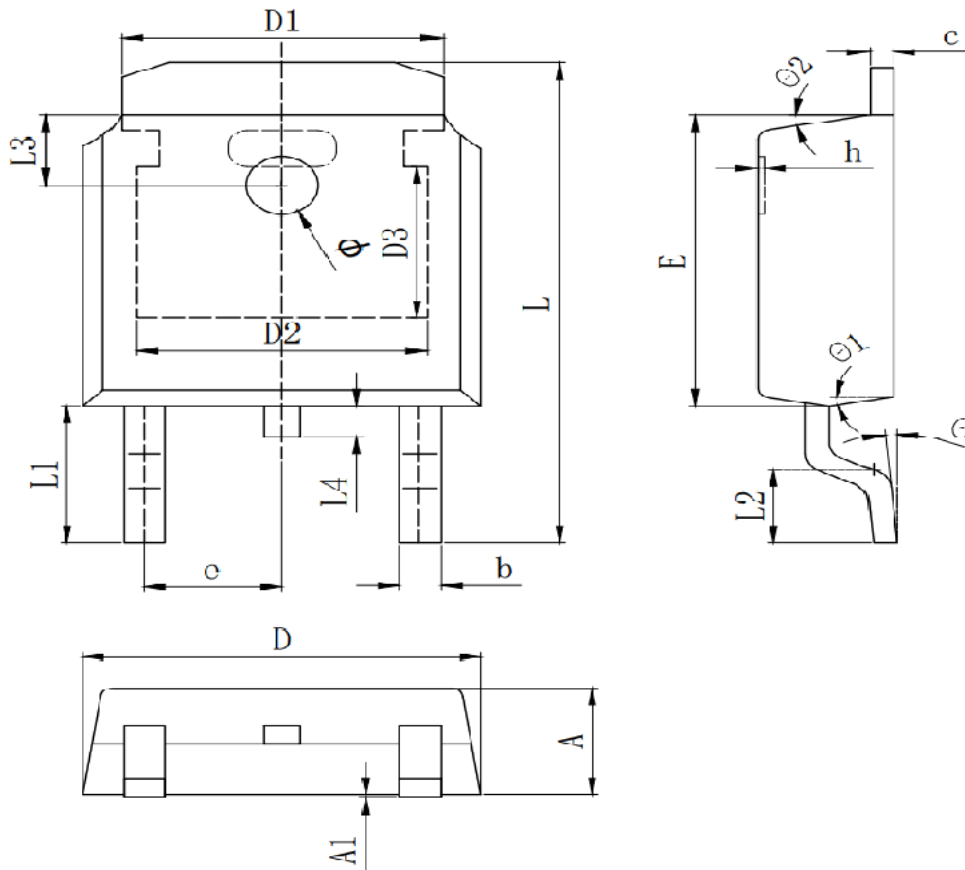
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Typical Electrical And Thermal Characteristics (Curves)





TO-252 Package Information



Symbol	Dimensions In Millimeters		
	Min.	Typ.	Max.
A	2.200	2.300	2.400
A1	0.000		0.127
b	0.640	0.690	0.740
c(电镀后)	0.460	0.520	0.580
D	6.500	6.600	6.700
D1	5.334 REF		
D2	4.826 REF		
D3	3.166 REF		
E	6.000	6.100	6.200
e	2.286 TYP		
h	0.000	0.100	0.200
L	9.900	10.100	10.300
L1	2.888 REF		
L2	1.400	1.550	1.700
L3	1.600 REF		
L4	0.600	0.800	1.000
Φ	1.100	1.200	1.300
θ	0°		8°
θ1	9° TYP		
θ2	9° TYP		



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