



General Description

The SJD30NP245 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a wide variety of applications.

Features

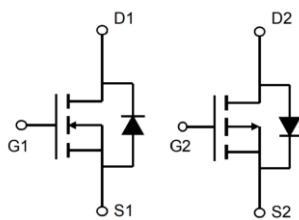
- Low Gate Charge
- 100% UIS Tested, 100% DVDS Tested
- High Power and current handing capability
- Lead free product is acquired

Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

Key Performance Parametes

Parameter	Value	Value	Unit
V_{DS}	30	-30	V
$R_{DS(ON)}_{TYP}$	8	21.2	mΩ
I_D	56	-27	A
Q_G	19.2	21.7	nC



Schematic Diagram



TO-252-4L top view

Package Marking and Ordering Information

Device/Ordering Code	Marking	Package	Packing	Reel Size	Tape width	Quantity
SJD30NP245	SJD30NP245	TO-252-4L	Tape	\	\	2500 Pcs

Table 1. Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	N Limit	P Limit	Unit
V_{DS}	Drain-Source Voltage ($V_{GS}=0\text{V}$)	30	-30	V
V_{GS}	Gate-Source Voltage ($V_{DS}=0\text{V}$)	± 20	± 20	V
I_D	Drain Current-Continuous($T_c=25^\circ\text{C}$)	56	-27	A
	Drain Current-Continuous($T_c=100^\circ\text{C}$)	35	-27	A
I_{DM} (pulse)	Drain Current-Continuous@ Current-Pulsed (Note 1)	224	-108	A
P_D	Maximum Power Dissipation($T_c=25^\circ\text{C}$)	50	31	W
	Maximum Power Dissipation($T_c=100^\circ\text{C}$)	20	12.6	W
E_{AS}	Avalanche energy (Note 2)	64	95	mJ
T_J , T_{STG}	Operating Junction and Storage Temperature Range	-55 To 150		°C

Table 2. Thermal Characteristic

Symbol	Parameter	N Limit	P Limit	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	2.5	3.97	°C/W



30V N&P-Channel Trench Power MOSFET

Table 3. N-Channel Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ $I_{\text{D}}=250\mu\text{A}$	30			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=30\text{V}$, $V_{\text{GS}}=0\text{V}$ $T_J=25^\circ\text{C}$			1	μA
		$V_{\text{DS}}=30\text{V}$, $V_{\text{GS}}=0\text{V}$ $T_J=125^\circ\text{C}$			100	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$			± 100	nA
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_{\text{D}}=250\mu\text{A}$	1		2.5	V
g_{FS}	Forward Transconductance	$V_{\text{DS}}=5\text{V}$, $I_{\text{D}}=10\text{A}$		18		S
$R_{\text{DS(ON)}}$	Drain-Source On-State Resistance	$V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=10\text{A}$ $T_J=25^\circ\text{C}$		8	10.4	$\text{m}\Omega$
$R_{\text{DS(ON)}}$	Drain-Source On-State Resistance	$V_{\text{GS}}=4.5\text{V}$, $I_{\text{D}}=8\text{A}$ $T_J=25^\circ\text{C}$		10.2	13.6	$\text{m}\Omega$
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{\text{DS}}=15\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1.0\text{MHz}$		968		pF
C_{oss}	Output Capacitance			121		pF
C_{rss}	Reverse Transfer Capacitance			99.2		pF
R_g	Gate resistance	$V_{\text{GS}}=0\text{V}$, $V_{\text{DS}}=0\text{V}$, $f=1.0\text{MHz}$		2.5		Ω
Switching Parameters						
$t_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{GS}}=10\text{V}$, $V_{\text{DS}}=15\text{V}$, $R_L=1.5\Omega$, $R_{\text{GEN}}=3\Omega$		7.6		nS
t_r	Turn-on Rise Time			15.8		nS
$t_{\text{d(off)}}$	Turn-Off Delay Time			27.6		nS
t_f	Turn-Off Fall Time			4		nS
Q_g	Total Gate Charge	$V_{\text{GS}}=10\text{V}$, $V_{\text{DS}}=15\text{V}$, $I_{\text{D}}=10\text{A}$		19.2		nC
Q_{gs}	Gate-Source Charge			4.2		nC
Q_{gd}	Gate-Drain Charge			2.5		nC
Source-Drain Diode Characteristics						
I_{SD}	Source-Drain Current (Body Diode)				56	A
V_{SD}	Forward on Voltage (Note 3)	$V_{\text{GS}}=0\text{V}$, $I_{\text{S}}=10\text{A}$			1.2	V
t_{rr}	Reverse Recovery Time	$I_F=10\text{A}$, $dI/dt=500\text{A}/\mu\text{s}$		19.4		ns
Q_{rr}	Reverse Recovery Charge	$I_F=10\text{A}$, $dI/dt=500\text{A}/\mu\text{s}$		11.6		nC

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.

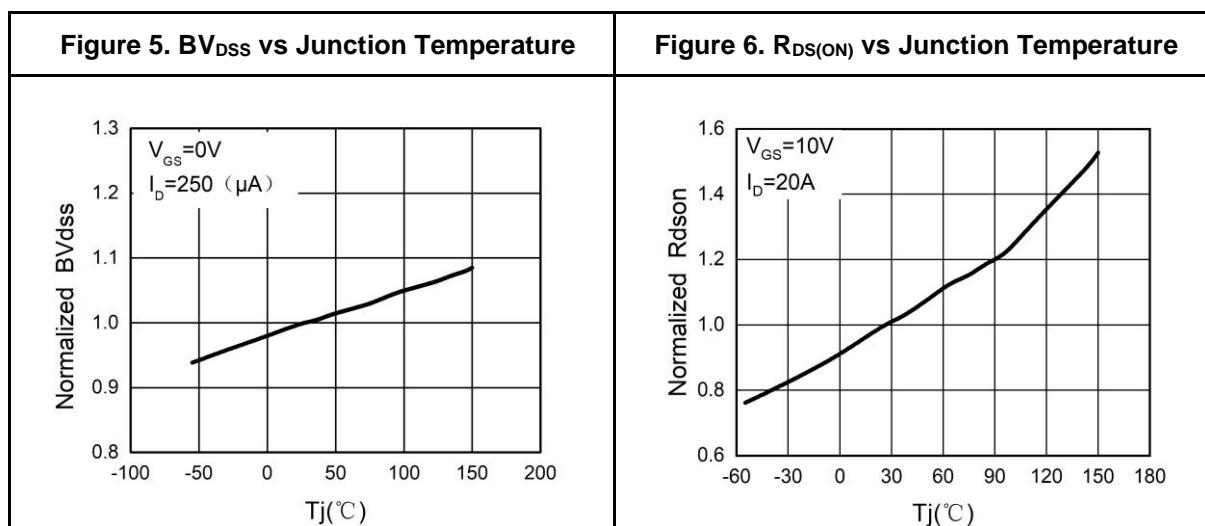
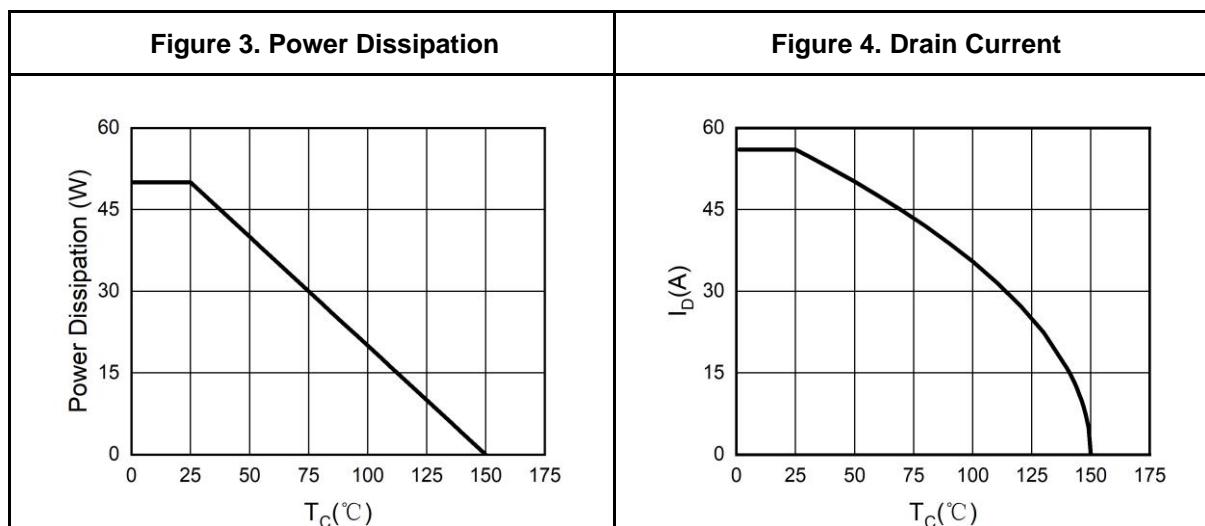
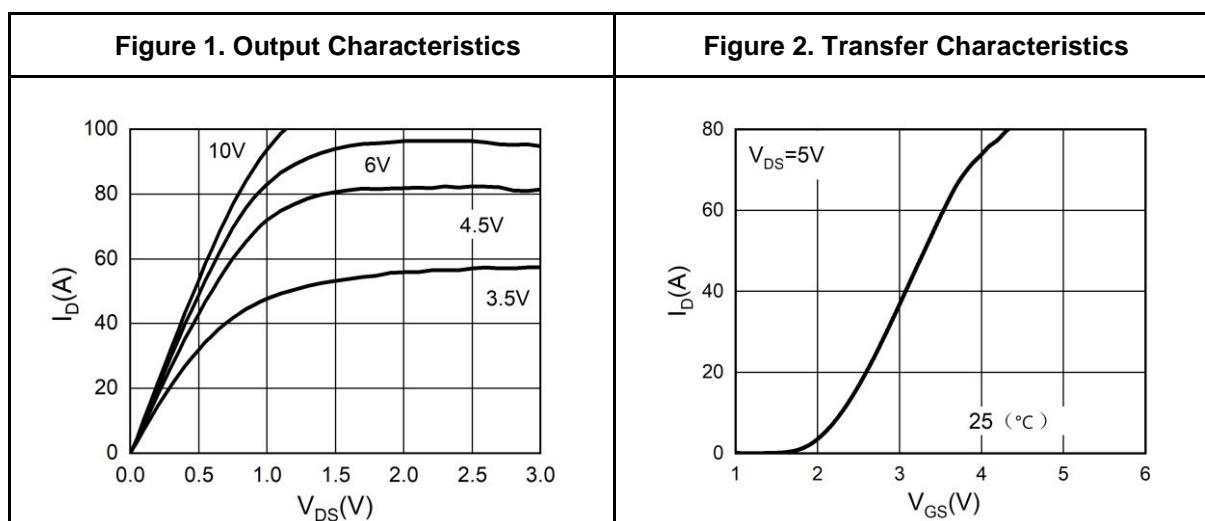
Notes 2.E_{AS} condition: $T_J=25^\circ\text{C}$, $V_{\text{DD}}=30\text{V}$, $V_G=10\text{V}$, $R_g=25\Omega$, $L=0.5\text{mH}$.

Notes 3.Repetitive Rating: Pulse width limited by maximum junction temperature.



30V N&P-Channel Trench Power MOSFET

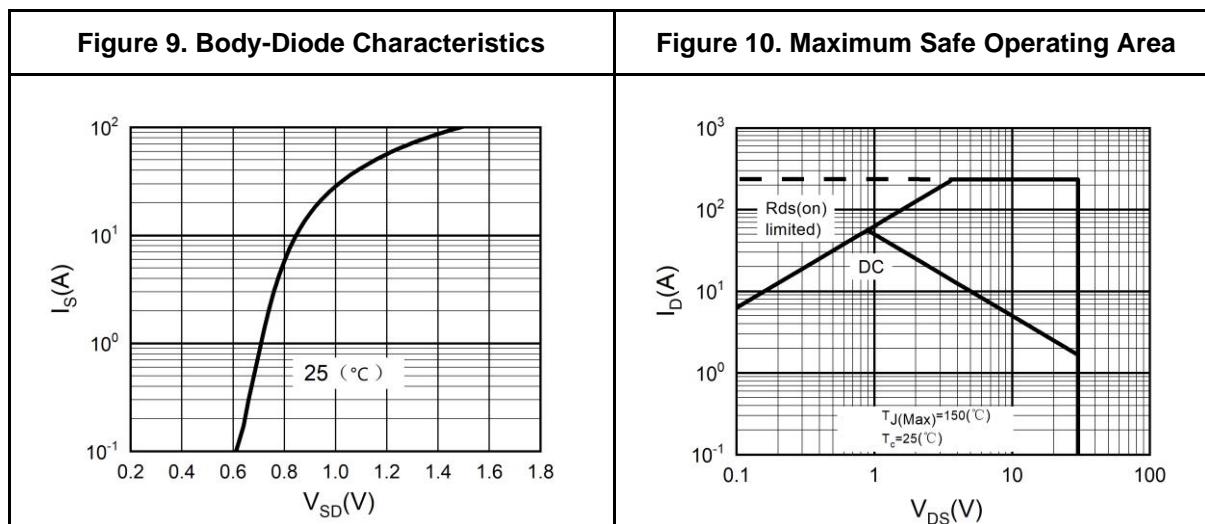
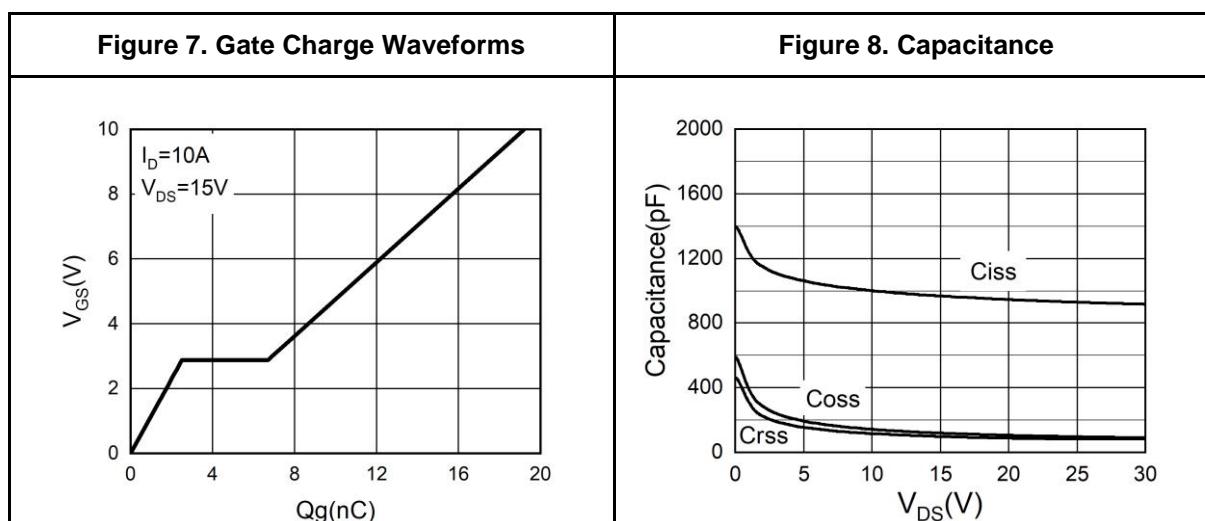
N-Channel Typical Electrical And Thermal Characteristics (Curves)





30V N&P-Channel Trench Power MOSFET

N-Channel Typical Electrical And Thermal Characteristics (Curves)





30V N&P-Channel Trench Power MOSFET

Table 4. P-Channel Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-30			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=-30\text{V}, V_{\text{GS}}=0\text{V} T_J=25^\circ\text{C}$			-1	μA
		$V_{\text{DS}}=-30\text{V}, V_{\text{GS}}=0\text{V} T_J=125^\circ\text{C}$			-100	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$			± 100	nA
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-1		-2.5	V
g_{FS}	Forward Transconductance	$V_{\text{DS}}=-5\text{V}, I_{\text{D}}=-5\text{A}$		20		S
$R_{\text{DS(ON)}}$	Drain-Source On-State Resistance	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-7.5\text{A} T_J=25^\circ\text{C}$		21.2	27.5	$\text{m}\Omega$
$R_{\text{DS(ON)}}$	Drain-Source On-State Resistance	$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-5\text{A} T_J=25^\circ\text{C}$		32.5	43.2	$\text{m}\Omega$
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{\text{DS}}=-15\text{V}, V_{\text{GS}}=0\text{V}, f=1.0\text{MHz}$		1135		pF
C_{oss}	Output Capacitance			184		pF
C_{rss}	Reverse Transfer Capacitance			117		pF
R_g	Gate resistance	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=0\text{V}, f=1.0\text{MHz}$		4.9		Ω
Switching Parameters						
$t_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{GS}}=-10\text{V}, V_{\text{DS}}=-15\text{V}, R_L=2\Omega, R_{\text{GEN}}=3\Omega$		12		nS
t_r	Turn-on Rise Time			14		nS
$t_{\text{d(off)}}$	Turn-Off Delay Time			195		nS
t_f	Turn-Off Fall Time			95		nS
Q_g	Total Gate Charge	$V_{\text{GS}}=-10\text{V}, V_{\text{DS}}=-15\text{V}, I_{\text{D}}=-7.5\text{A}$		21.7		nC
Q_{gs}	Gate-Source Charge			1.4		nC
Q_{gd}	Gate-Drain Charge			4.1		nC
Source-Drain Diode Characteristics						
I_{SD}	Source-Drain Current (Body Diode)				-27	A
V_{SD}	Forward on Voltage (Note 3)	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=-7\text{A}$			-1.2	V
t_{rr}	Reverse Recovery Time	$I_F=-7\text{A}, dI/dt=-100\text{A}/\mu\text{s}$		36		ns
Q_{rr}	Reverse Recovery Charge	$I_F=-7\text{A}, dI/dt=-100\text{A}/\mu\text{s}$		34		nC

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.

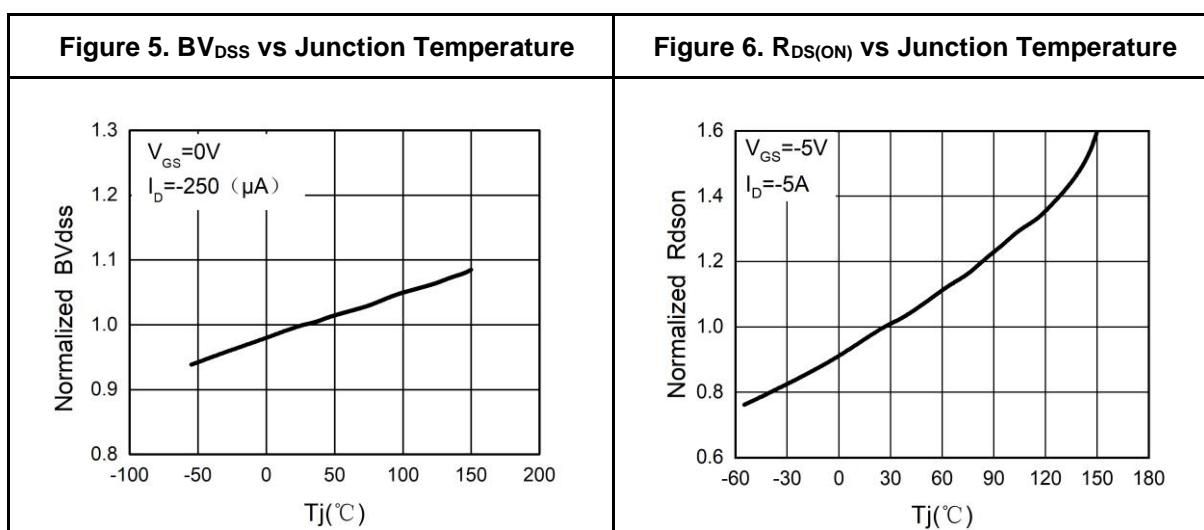
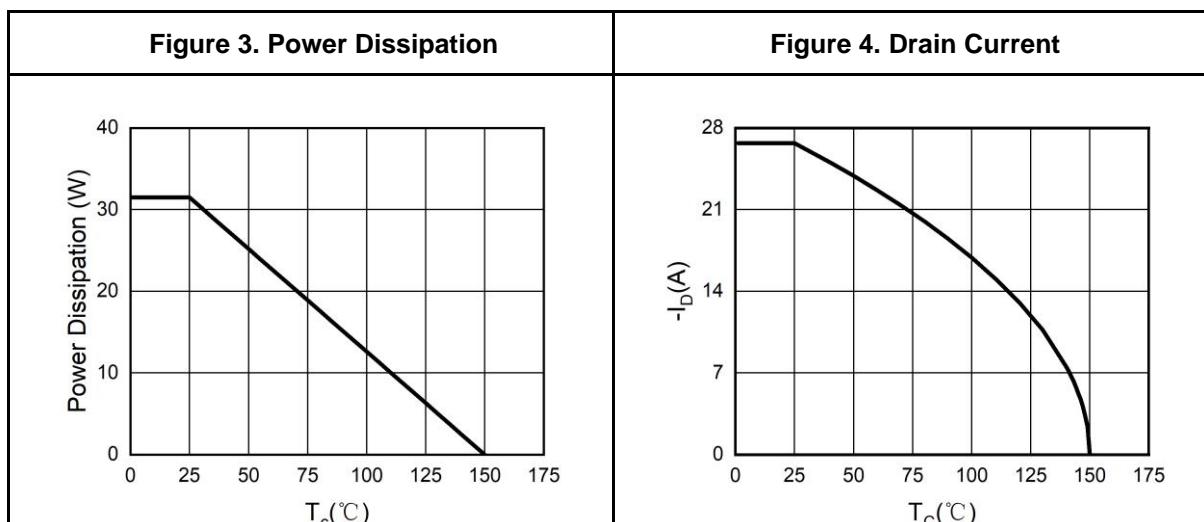
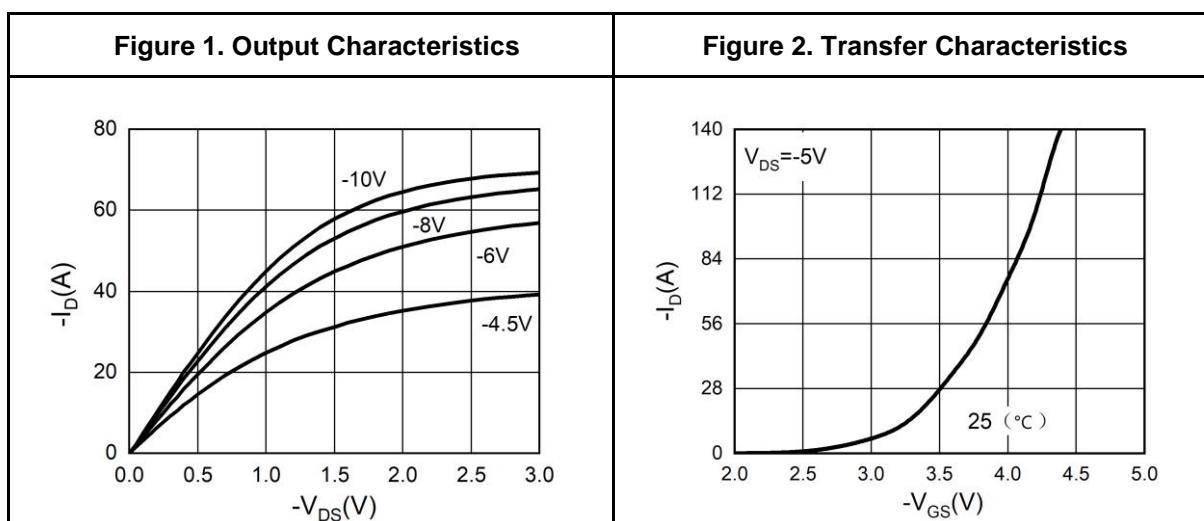
Notes 2.E_{AS} condition: $T_J=25^\circ\text{C}, V_{\text{DD}}=-30\text{V}, V_{\text{G}}=-10\text{V}, R_g=25\Omega, L=0.5\text{mH}$.

Notes 3.Repetitive Rating: Pulse width limited by maximum junction temperature.



30V N&P-Channel Trench Power MOSFET

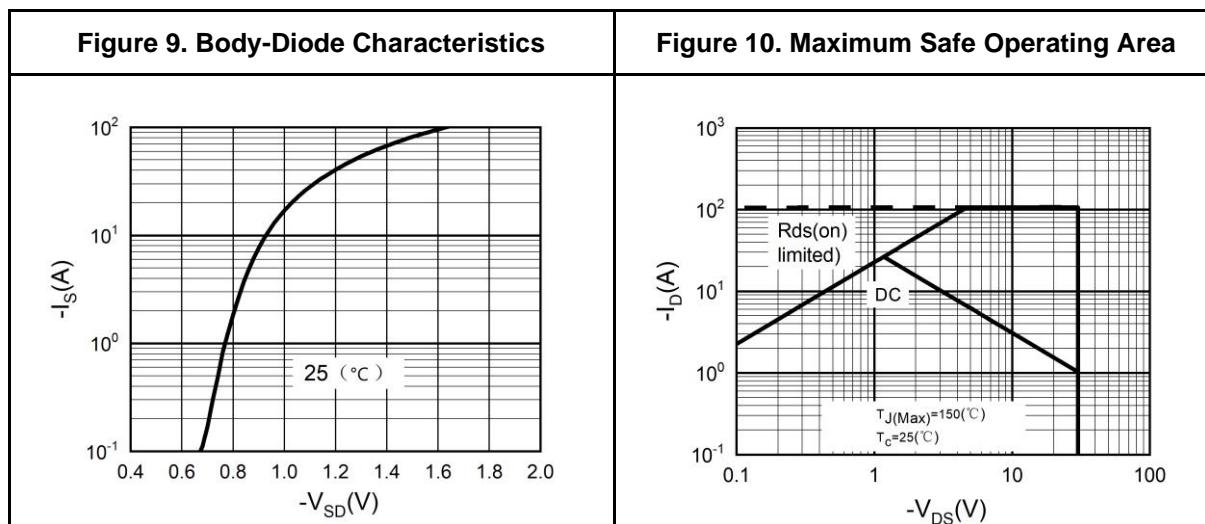
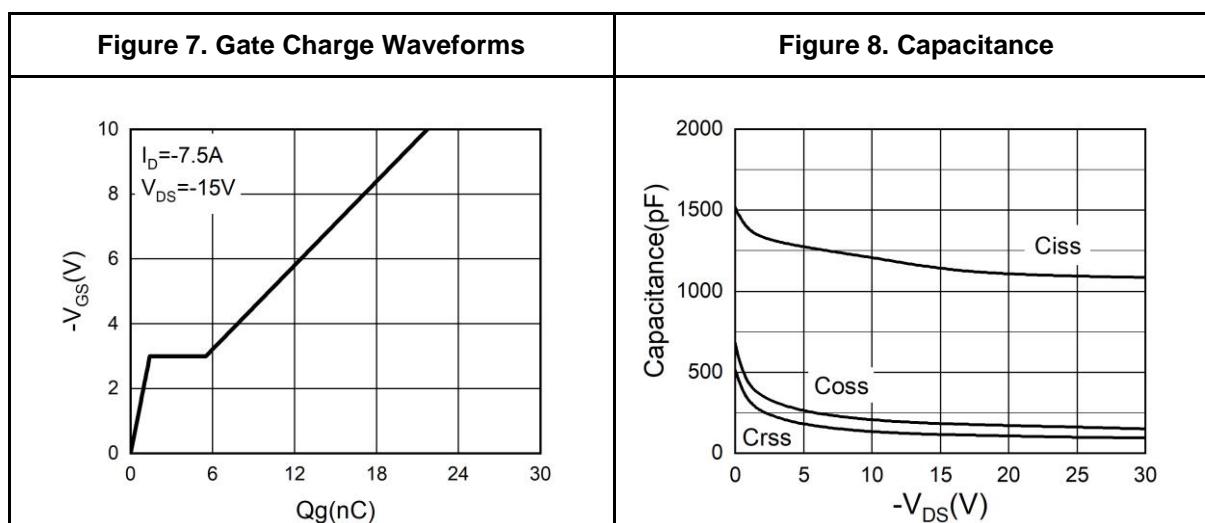
P-Channel Typical Electrical And Thermal Characteristics (Curves)





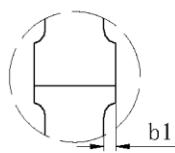
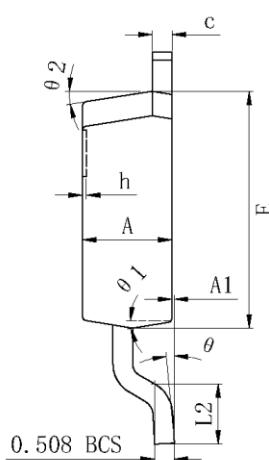
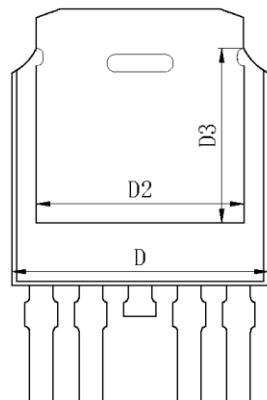
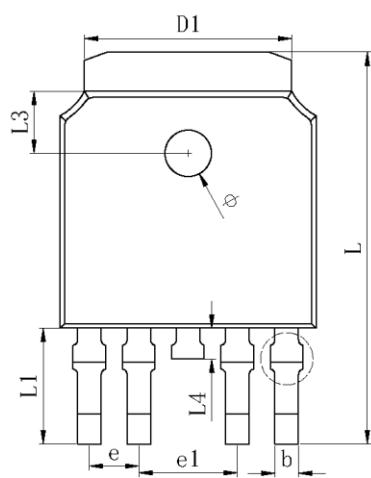
30V N&P-Channel Trench Power MOSFET

P-Channel Typical Electrical And Thermal Characteristics (Curves)





TO-252-4L Package Information



SYMBOL	MILLIMETER		
	MIN	Typ.	MAX
A	2.200	2.300	2.400
A1	0.000		0.127
b	0.550	0.600	0.650
b1	0.000		0.120
c(电镀后)	0.460	0.520	0.580
D	6.500	6.600	6.700
D1	5.334	REF	
D2	5.346	REF	
D3	4.490	REF	
E	6.000	6.100	6.200
e		1.270 TYP	
e1		2.540 TYP	
h	0.000	0.100	0.200
L	9.900	10.100	10.300
L1		2.988	REF
L2	1.400	1.550	1.700
L3		1.600	REF
L4	0.700	0.800	0.900
phi	1.100	1.200	1.300
theta	0°	9° TYP	8°
theta1		9° TYP	
theta2		9° TYP	



Attention

This product described in this document can not be used in life support devices or systems, aircraft's control systems, and other applications whose failure can be reasonably expected to result in serious physical and/or material damage, apart from that when an application agreement is signed between customer and Wuxi Shangjia Semiconductor.

The performances and characteristics of this product in the independent testing state are displayed in this document. Wuxi Shangjia Semiconductor can't guarantee of the performances and characteristics of this described product that mounted in the customer's products or equipments as same as that in the independent testing state. So the customer should evaluate and test devices mounted in the customer's products or equipments.

Wuxi Shangjia Semiconductor reserves the right to improve the designs, functions and reliability of this product and modify any and all information described in this document without notice customer, apart from that when an notice agreement is signed between customer and Wuxi Shangjia Semiconductor.

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Wuxi Shangjia Semiconductor hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.