General Description

The SJD02P6000 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as -10V. This device is suitable for use as a wide variety of applications.

Features

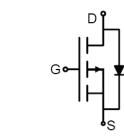
- Low Gate Charge
- 100% UIS Tested, 100% DVDS Tested
- High Power and current handing capability
- Lead free product is acquired

Application

- PWM Applications
- Load Switch
- Power Management

Key Performance Parametes

Parameter	Value	Unit
V _{DS}	-200	V
R _{DS(ON)_TYP}	642	mΩ
I _D	-4.5	Α
Q _G	80	nC











Package Marking and Ordering Information

Device/Ordering Code	Marking	Package	Packing	Reel Size	Tape width	Quantity
SJD02P6000	SJD02P6000	TO-252	Tape	\	\	2500 Pcs

Table 1. Absolute Maximum Ratings (T_C=25℃ unless otherwise noted)

Symbol	Parameter	Limit	Unit
V _{DS}	Drain-Source Voltage (V _{GS} =0V)	-200	V
V _{GS}	Gate-Source Voltage (V _{DS} =0V)	±20	V
1-	Drain Current-Continuous(Tc=25℃)		А
I _D	Drain Current-Continuous(T _C =100℃)	-2.8	А
I _{DM} (pluse)	Drain Current-Continuous@ Current-Pulsed (Note 1)	-18	А
D-	Maximum Power Dissipation(T _C =25°C)	37.8	W
P _D	Maximum Power Dissipation(Tc=100°C)	15.2	W
Eas	Avalanche energy (Note 2)	81	mJ
T _J , T _{STG}	Operating Junction and Storage Temperature Range	-55 To 150	c

Table 2. Thermal Characteristic

Symbol	Parameter	Тур	Max	Unit
ReJC	Thermal Resistance, Junction-to-Case		3.3	°C/W



Table 3. Electrical Characteristics (T_J=25℃ unless otherwise noted)

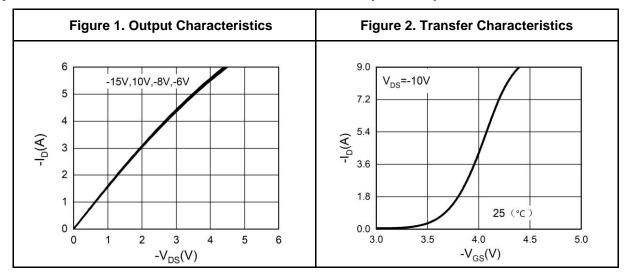
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
On/Off States						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V I _D =-250μA	-200			V
	Zana Oata Valta na Duain Oumant	V _{DS} =-200V, V _{GS} =0V T _J =25°C			-1	μA
IDSS	Zero Gate Voltage Drain Current	V _{DS} =-200V, V _{GS} =0V T _J =125℃			-100	μA
Igss	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V			±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250μA	-2		-4	V
g FS	Forward Transconductance	V _{DS} =-10V, I _D =-3A		5.5		S
R _{DS(ON)}	Drain-Source On-State Resistance	V _{GS} =-10V, I _D =-2A T _J =25℃		642	834	mΩ
Dynamic Charac	teristics					
Ciss	Input Capacitance			2145		pF
Coss	Output Capacitance	V _{DS} =-25V,V _{GS} =0V, f=1.0MHz		49.3		pF
Crss	Reverse Transfer Capacitance			40.4		pF
Rg	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1.0MHz		5.4		Ω
Switching Param	neters					
t _{d(on)}	Turn-on Delay Time			17.3		nS
tr	Turn-on Rise Time	V _{GS} =-10V, V _{DS} =-50V,		4.2		nS
t _{d(off)}	Turn-Off Delay Time	R _L =16Ω, R _{GEN} =3Ω		49.6		nS
t _f	Turn-Off Fall Time			28		nS
Qg	Total Gate Charge			80		nC
Q_{gs}	Gate-Source Charge	V _{GS} =-10V, V _{DS} =-50V, I _D =-3A		15		nC
Q_{gd}	Gate-Drain Charge			28		nC
Source-Drain Did	ode Characteristics					
I _{SD}	Source-Drain Current (Body Diode)				-4.5	А
V _{SD}	Forward on Voltage (Note 3)	V _{GS} =0V, I _S =-3A			-1.2	V
t _{rr}	Reverse Recovery Time	I _F =-3A, dI/dt=-100A/μs		63.6		ns
Q _{rr}	Reverse Recovery Charge	I _F =-3A, dI/dt=-100A/μs		194		nC

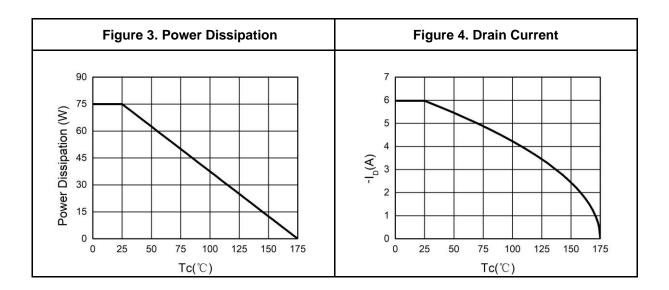
Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.

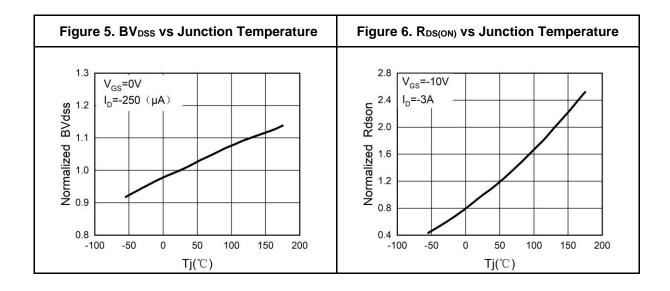
Notes 2.E_{AS} condition: T_J =25 $^{\circ}$ C, V_{DD} =-50V, V_G =-10V, Rg=25 Ω , L=0.5mH.

Notes 3.Repetitive Rating: Pulse width limited by maximum junction temperature.

Typical Electrical And Thermal Characteristics (Curves)

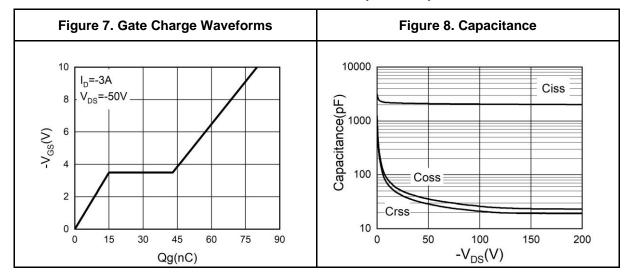


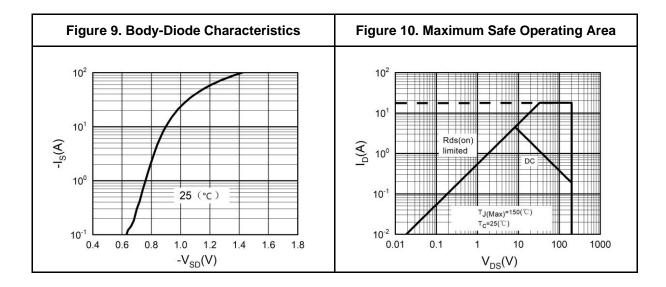






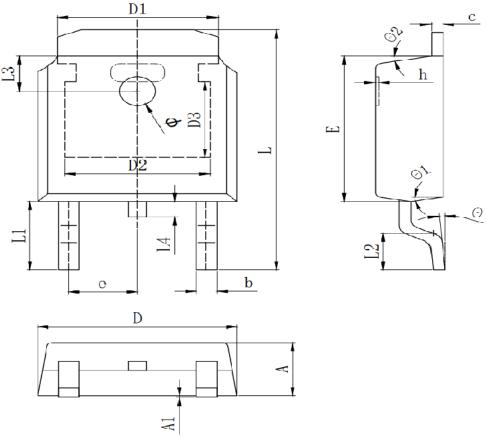
Typical Electrical And Thermal Characteristics (Curves)







TO-252 Package Information



Symbol	Dimensions In Millimeters				
Symbol	Min.	Тур.	Max.		
А	2.200	2.300	2.400		
A1	0.000		0.127		
b	0.640	0.690	0.740		
c(电镀后)	0.460	0.520	0.580		
D	6.500	6.600	6.700		
D1		5.334 REF			
D2		4.826 REF			
D3		3.166 REF			
E	6.000	6.100	6.200		
е	2.286 TYP				
h	0.000	0.100	0.200		
L	9.900	10.100	10.300		
L1		2.888 REF			
L2	1.400	1.550	1.700		
L3		1.600 REF			
L4	0.600	0.800	1.000		
Ф	1.100	1.200	1.300		
θ	0°		8°		
θ1	9° TYP				
θ2	9° TYP				



Attention

This product described in this document can not be used in life support devices or systems, aircraft's control systems, and other applications whose failure can be reasonably expected to result in serious physical and/or material damage, apart from that when an application agreement is signed between customer and Wuxi Shangjia Semiconductor.

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