



General Description

The SJD02N2490 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 10V. This device is suitable for use as a wide variety of applications.

Features

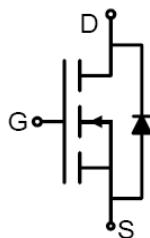
- Low Gate Charge
- 100% UIS Tested, 100% DVDS Tested
- High Power and current handing capability
- Lead free product is acquired

Application

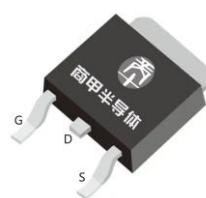
- PWM Applications
- Load Switch
- Power Management

Key Performance Parametes

Parameter	Value	Unit
V_{DS}	200	V
$R_{DS(ON)}_{TYP}$	235	mΩ
I_D	9	A
Q_G	29	nC



Schematic Diagram



TO-252 top view

Package Marking and Ordering Information

Device/Ordering Code	Marking	Package	Packing	Reel Size	Tape width	Quantity
SJD02N2490	D02N2490	TO-252	Tape	\	\	2500 Pcs

Table 1. Absolute Maximum Ratings ($T_c=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Limit	Unit
V_{DS}	Drain-Source Voltage ($V_{GS}=0\text{V}$)	200	V
V_{GS}	Gate-Source Voltage ($V_{DS}=0\text{V}$)	± 20	V
I_D	Drain Current-Continuous($T_c=25^\circ\text{C}$)	9	A
	Drain Current-Continuous($T_c=100^\circ\text{C}$)	5.7	A
I_{DM} (pulse)	Drain Current-Continuous@ Current-Pulsed (Note 1)	36	A
P_D	Maximum Power Dissipation($T_c=25^\circ\text{C}$)	60	W
	Maximum Power Dissipation($T_c=100^\circ\text{C}$)	23.8	W
E_{AS}	Avalanche energy (Note 2)	10	mJ
T_J, T_{STG}	Operating Junction and Storage Temperature Range	-55 To 150	°C

Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case		2.1	°C/W



200V N-Channel Trench Power MOSFET

Table 3. Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=250\mu\text{A}$	200			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=200\text{V}$, $V_{\text{GS}}=0\text{V}$			1	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$			± 100	nA
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_{\text{D}}=250\mu\text{A}$	1		3	V
g_{FS}	Forward Transconductance	$V_{\text{DS}}=5\text{V}$, $I_{\text{D}}=4\text{A}$		8		S
$R_{\text{DS(ON)}}$	Drain-Source On-State Resistance	$V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=4\text{A}$		235	282	$\text{m}\Omega$
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{\text{DS}}=25\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1.0\text{MHz}$		1442		pF
C_{oss}	Output Capacitance			28		pF
C_{rss}	Reverse Transfer Capacitance			10.7		pF
R_g	Gate resistance	$V_{\text{GS}}=0\text{V}$, $V_{\text{DS}}=0\text{V}$, $f=1.0\text{MHz}$		0.6		Ω
Switching Parameters						
$t_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{GS}}=10\text{V}$, $V_{\text{DS}}=100\text{V}$, $R_L=25\Omega$, $R_{\text{GEN}}=5\Omega$		13		nS
t_r	Turn-on Rise Time			10		nS
$t_{\text{d(off)}}$	Turn-Off Delay Time			40		nS
t_f	Turn-Off Fall Time			9		nS
Q_g	Total Gate Charge	$V_{\text{GS}}=10\text{V}$, $V_{\text{DS}}=100\text{V}$, $I_{\text{D}}=4\text{A}$		29		nC
Q_{gs}	Gate-Source Charge			4.6		nC
Q_{gd}	Gate-Drain Charge			9.8		nC
Source-Drain Diode Characteristics						
I_{SD}	Source-Drain Current (Body Diode)				9	A
V_{SD}	Forward on Voltage (Note 2)	$V_{\text{GS}}=0\text{V}$, $I_{\text{S}}=4\text{A}$			1.2	V
t_{rr}	Reverse Recovery Time	$I_F=4\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$		80		ns
Q_{rr}	Reverse Recovery Charge	$I_F=4\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$		282		nC

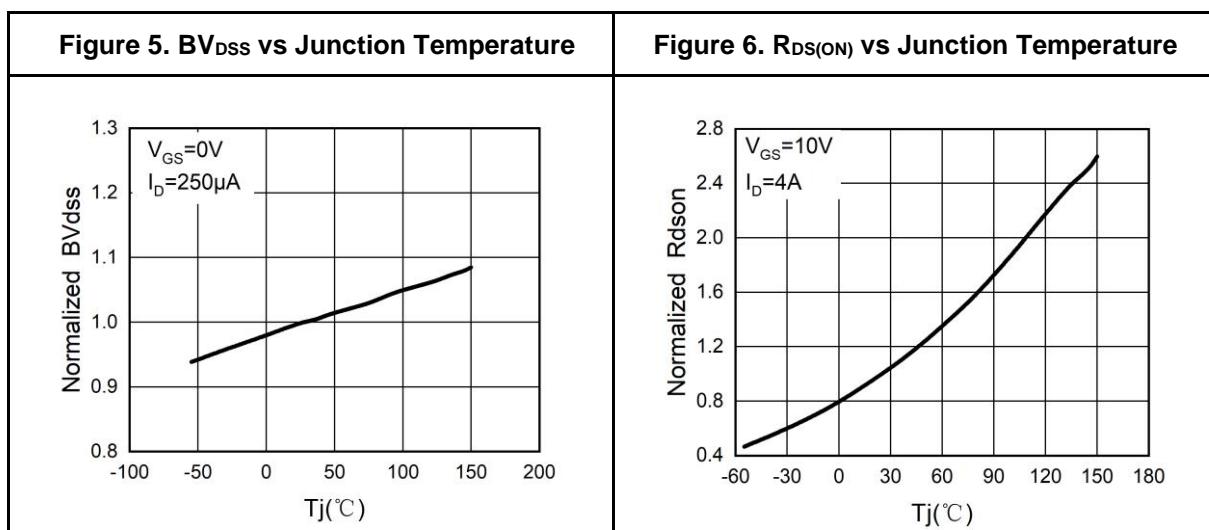
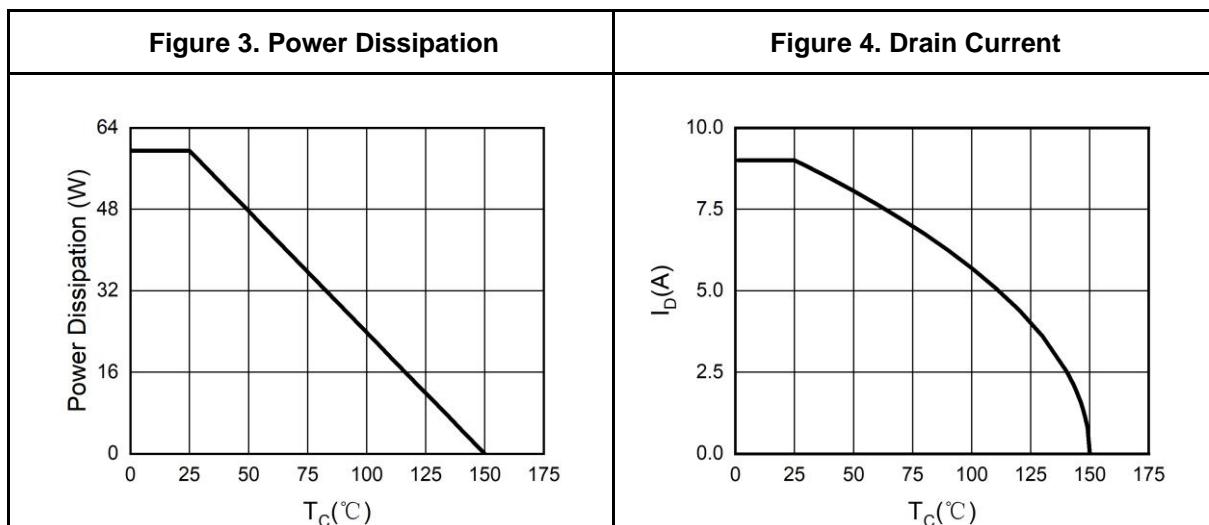
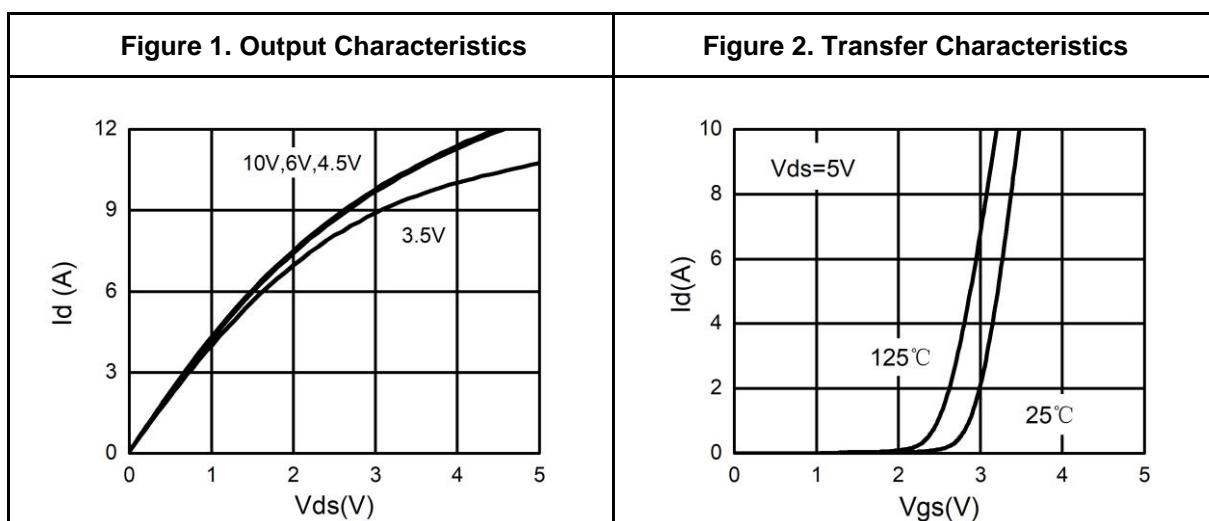
Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.

Notes 2.E_{AS} condition: $T_J=25^\circ\text{C}$, $V_{\text{DD}}=60\text{V}$, $V_{\text{G}}=10\text{V}$, $R_g=25\Omega$, $L=0.5\text{mH}$.

Notes 3.Repetitive Rating: Pulse width limited by maximum junction temperature.

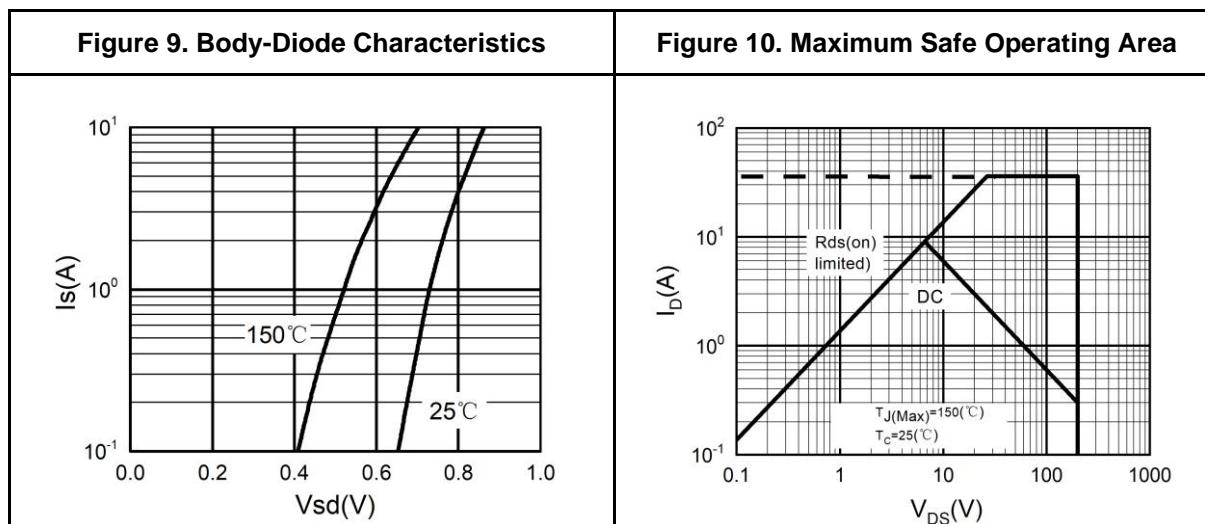
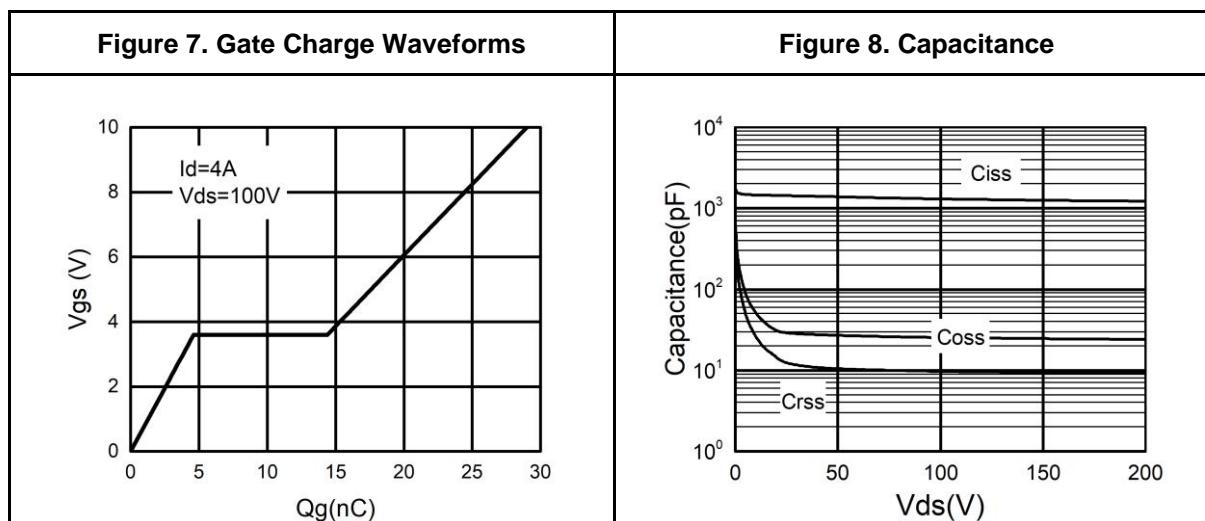


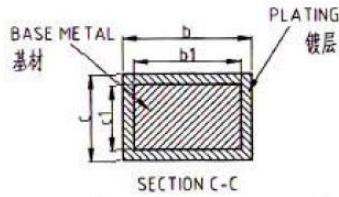
Typical Electrical And Thermal Characteristics (Curves)





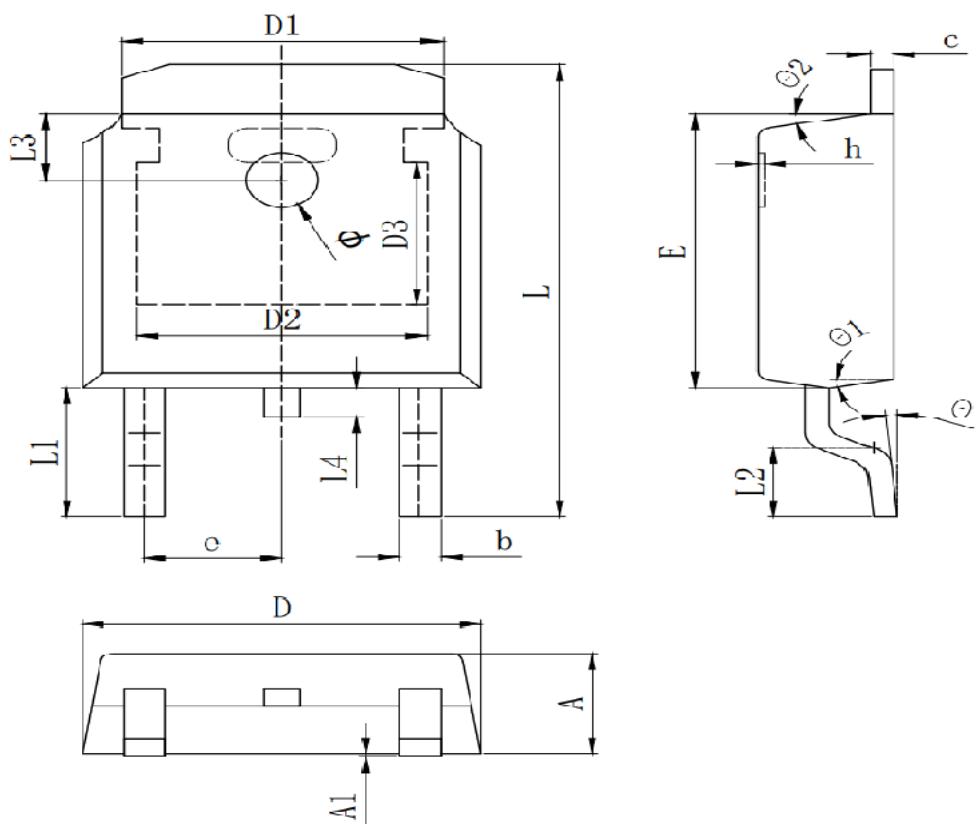
Typical Electrical And Thermal Characteristics (Curves)





NOTES
ALL DIMENSIONS REFER TO JEDEC STANDARD TO-252 AA.
DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS.

L ₂	0.5185C	
L ₃	0.90	- 1.25
L ₄	0.60	1.00
L ₅	1.70	1.80 1.90
θ	0°	- 8°
θ ₁	5°	7° 9°
θ ₂	5°	7° 9°
K	0.40REF	



Symbol	Dimensions In Millimeters		
	Min.	Typ.	Max.
A	2.200	2.300	2.400
A1	0.000		0.127
b	0.640	0.690	0.740
c(电镀后)	0.460	0.520	0.580
D	6.500	6.600	6.700
D1	5.334 REF		
D2	4.826 REF		
D3	3.166 REF		
E	6.000	6.100	6.200
e	2.286 TYP		
h	0.000	0.100	0.200
L	9.900	10.100	10.300
L1	2.888 REF		
L2	1.400	1.550	1.700
L3	1.600 REF		
L4	0.600	0.800	1.000
Φ	1.100	1.200	1.300
θ	0°		8°
θ ₁	9° TYP		
θ ₂	9° TYP		



Attention

This product described in this document can not be used in life support devices or systems, aircraft's control systems, and other applications whose failure can be reasonably expected to result in serious physical and/or material damage, apart from that when an application agreement is signed between customer and Wuxi shangjia Semiconductor.

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