



# 100V N&P-Channel Trench Power MOSFET

## General Description

The SJD01NP235 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a wide variety of applications.

### Features

- Low Gate Charge
- High Power and current handing capability
- Lead free product is acquired

### Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

### Key Performance Parametes

Parameter	Value	Value	Unit
$V_{DS}$	100	-100	V
$R_{DS(ON\_TYP)}$	95	152	mΩ
$I_D$	13	-11	A
$Q_G$	20	22.7	nC



## Package Marking and Ordering Information

Device/Ordering Code	Marking	Package	Packing	Reel Size	Tape width	Quantity
SJD01NP235	SJD01NP235	TO-252-4L	Tape	\	\	2500 Pcs

**Table 1. Absolute Maximum Ratings ( $T_c=25^\circ\text{C}$  unless otherwise noted)**

Symbol	Parameter	N Limit	P Limit	Unit
$V_{DS}$	Drain-Source Voltage ( $V_{GS}=0V$ )	100	-100	V
$V_{GS}$	Gate-Source Voltage ( $V_{DS}=0V$ )	$\pm 20$	$\pm 20$	V
$I_D$	Drain Current-Continuous( $T_c=25^\circ\text{C}$ )	13	-11	A
	Drain Current-Continuous( $T_c=100^\circ\text{C}$ )	8	-6.8	A
$I_{DM}$ (pluse)	Drain Current-Continuous@ Current-Pulsed (Note 1)	52	-44	A
$P_D$	Maximum Power Dissipation( $T_c=25^\circ\text{C}$ )	36	48	W
	Maximum Power Dissipation( $T_c=100^\circ\text{C}$ )	14	19	W
$E_{AS}$	Avalanche energy (Note 2)	12	64	mJ
$T_J, T_{STG}$	Operating Junction and Storage Temperature Range	-55 To 150		$^\circ\text{C}$

**Table 2. Thermal Characteristic**

Symbol	Parameter	N Limit	P Limit	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	3.5	2.6	$^\circ\text{C/W}$



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**Table 3. N-Channel Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise noted)**

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>On/Off States</b>						
B <sub>V</sub> DSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	100			V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V T <sub>J</sub> =25°C			1	μA
		V <sub>DS</sub> =100V, V <sub>GS</sub> =0V T <sub>J</sub> =125°C			100	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V			±100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1		2.5	V
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> =5V, I <sub>D</sub> =4A		8		S
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =4A T <sub>J</sub> =25°C		95	119	mΩ
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance	V <sub>GS</sub> =4.5V, I <sub>D</sub> =3A T <sub>J</sub> =25°C		98	130	mΩ
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =50V, V <sub>GS</sub> =0V, f=1.0MHz		813		pF
C <sub>oss</sub>	Output Capacitance			27		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			23		pF
R <sub>g</sub>	Gate resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1.0MHz		1.9		Ω
<b>Switching Parameters</b>						
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>GS</sub> =10V, V <sub>DS</sub> =50V, R <sub>L</sub> =12.5Ω, R <sub>GEN</sub> =3Ω		8.5		nS
t <sub>r</sub>	Turn-on Rise Time			6		nS
t <sub>d(off)</sub>	Turn-Off Delay Time			29		nS
t <sub>f</sub>	Turn-Off Fall Time			3		nS
Q <sub>g</sub>	Total Gate Charge	V <sub>GS</sub> =10V, V <sub>DS</sub> =50V, I <sub>D</sub> =4A		20		nC
Q <sub>gs</sub>	Gate-Source Charge			2.3		nC
Q <sub>gd</sub>	Gate-Drain Charge			5.3		nC
<b>Source-Drain Diode Characteristics</b>						
I <sub>SD</sub>	Source-Drain Current (Body Diode)				13	A
V <sub>SD</sub>	Forward on Voltage <sup>(Note 3)</sup>	V <sub>GS</sub> =0V, I <sub>S</sub> =4A			1.2	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>F</sub> =4A, dI/dt=100A/μs		59		ns
Q <sub>rr</sub>	Reverse Recovery Charge	I <sub>F</sub> =4A, dI/dt=100A/μs		101		nC

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.

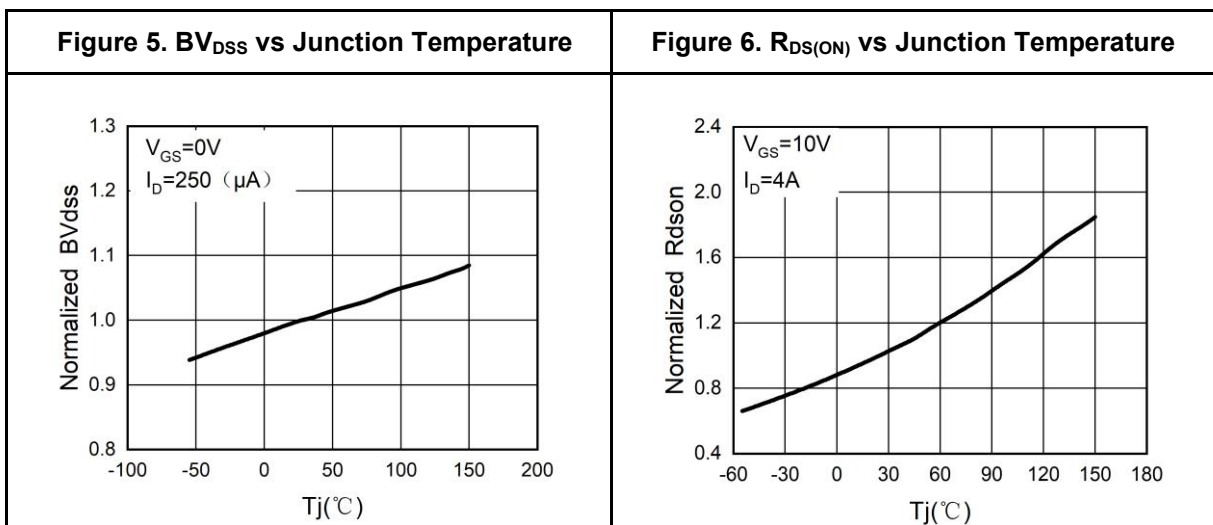
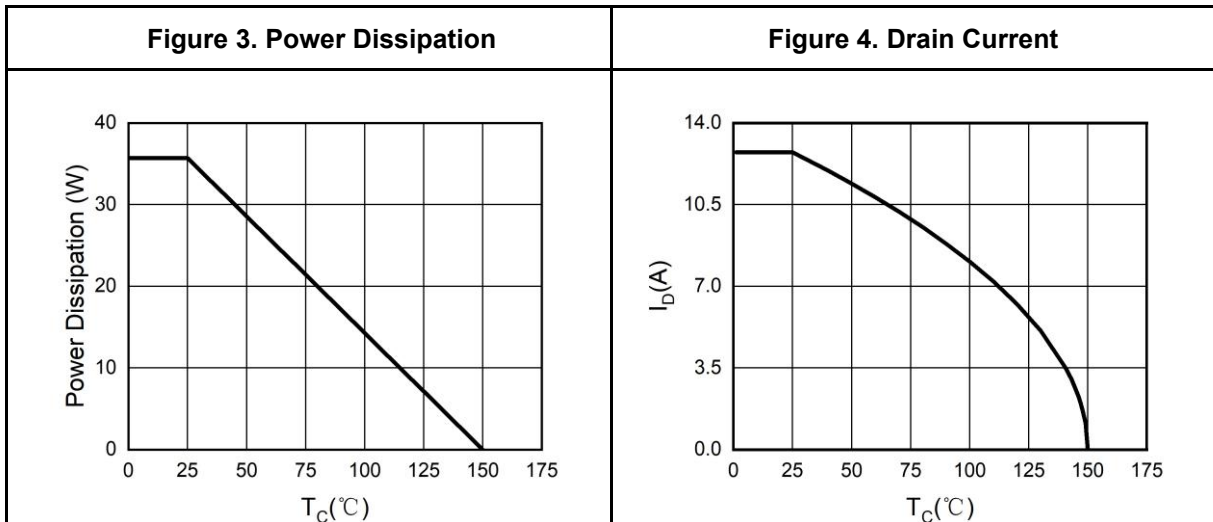
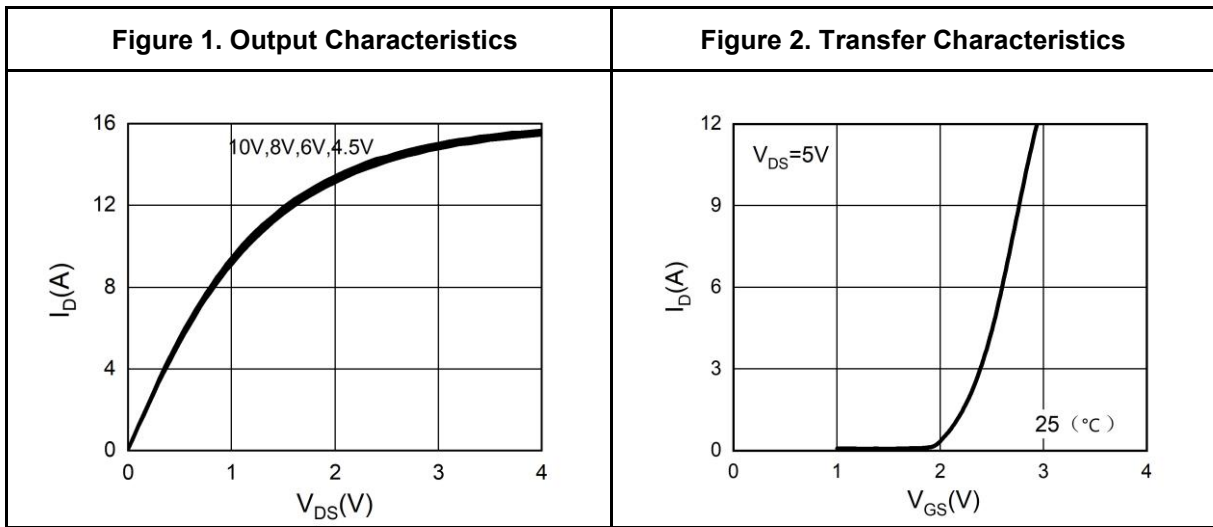
Notes 2.EAS condition: T<sub>J</sub>=25°C, V<sub>DD</sub>=50V, V<sub>G</sub>=10V, R<sub>g</sub>=25Ω, L=0.5mH.

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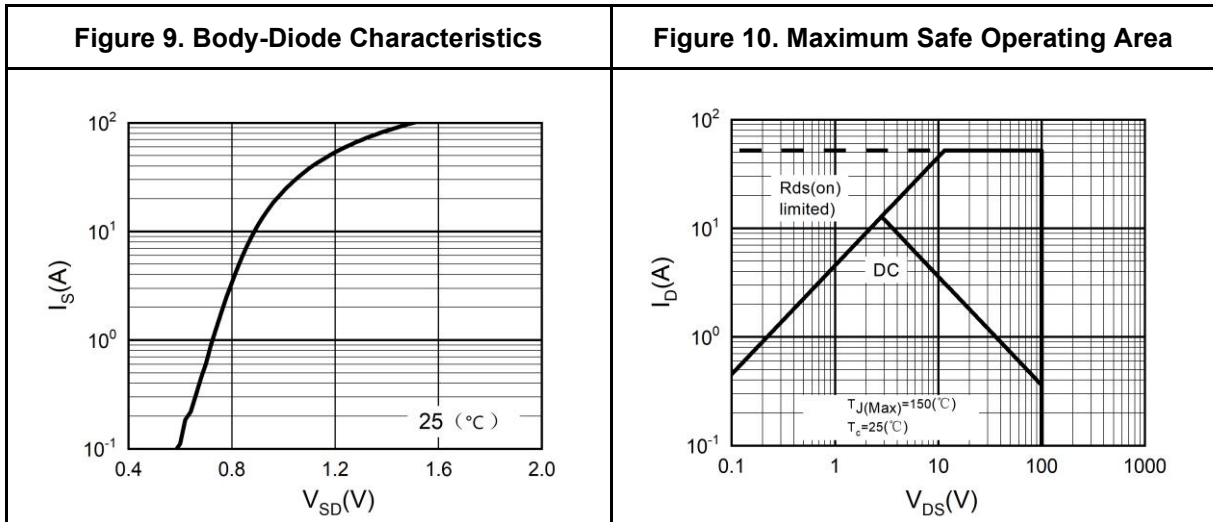
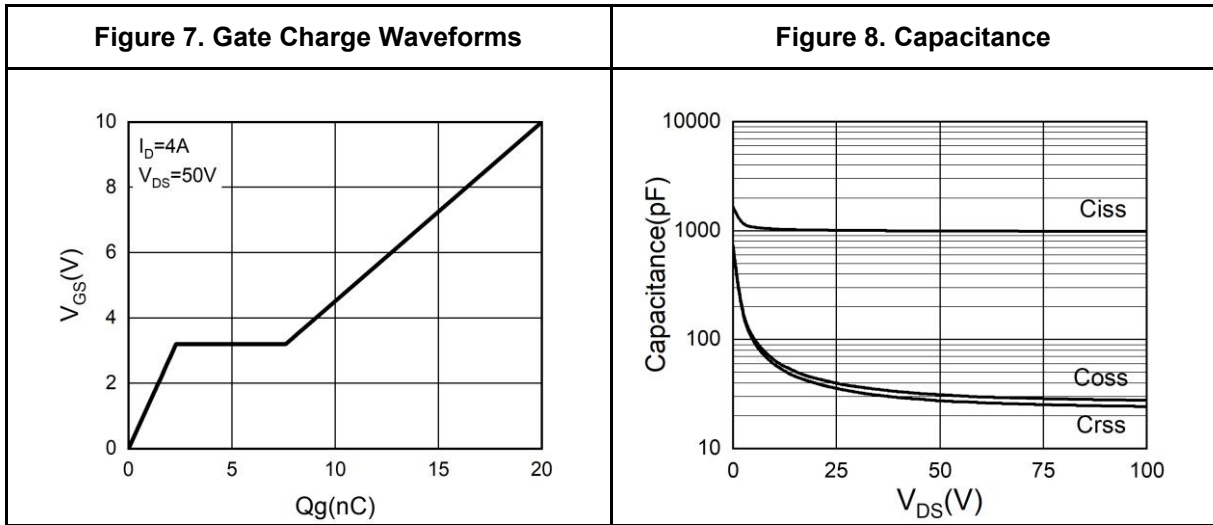
## N-Channel Typical Electrical And Thermal Characteristics (Curves)





# 100V N&P-Channel Trench Power MOSFET

## N-Channel Typical Electrical And Thermal Characteristics (Curves)





# 100V N&P-Channel Trench Power MOSFET

**Table 4. P-Channel Electrical Characteristics ( $T_J=25^\circ\text{C}$  unless otherwise noted)**

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>On/Off States</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-100			V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=-100V, V_{GS}=0V, T_J=25^\circ\text{C}$			-1	$\mu A$
		$V_{DS}=-100V, V_{GS}=0V, T_J=125^\circ\text{C}$			-100	$\mu A$
$I_{GSS}$	Gate-Body Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$			$\pm 100$	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1		-2.5	V
$g_{FS}$	Forward Transconductance	$V_{DS}=-5V, I_D=-5A$		12		S
$R_{DS(on)}$	Drain-Source On-State Resistance	$V_{GS}=-10V, I_D=-5A, T_J=25^\circ\text{C}$		152	190	m $\Omega$
$R_{DS(on)}$	Drain-Source On-State Resistance	$V_{GS}=-4.5V, I_D=-4A, T_J=25^\circ\text{C}$		159	212	m $\Omega$
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS}=-50V, V_{GS}=0V, f=1.0\text{MHz}$		1980		pF
$C_{oss}$	Output Capacitance			47		pF
$C_{rss}$	Reverse Transfer Capacitance			39		pF
$R_g$	Gate resistance	$V_{GS}=0V, V_{DS}=0V, f=1.0\text{MHz}$		5		$\Omega$
<b>Switching Parameters</b>						
$t_{d(on)}$	Turn-on Delay Time	$V_{GS}=-10V, V_{DS}=-50V, R_L=10\Omega, R_{GEN}=3\Omega$		14		nS
$t_r$	Turn-on Rise Time			18		nS
$t_{d(off)}$	Turn-Off Delay Time			50		nS
$t_f$	Turn-Off Fall Time			18		nS
$Q_g$	Total Gate Charge	$V_{GS}=-10V, V_{DS}=-50V, I_D=-5A$		22.7		nC
$Q_{gs}$	Gate-Source Charge			3.1		nC
$Q_{gd}$	Gate-Drain Charge			4.4		nC
<b>Source-Drain Diode Characteristics</b>						
$I_{SD}$	Source-Drain Current (Body Diode)				-11	A
$V_{SD}$	Forward on Voltage (Note 3)	$V_{GS}=0V, I_S=-5A$			-1.2	V
$t_{rr}$	Reverse Recovery Time	$I_F=-5A, dI/dt=-100A/\mu s$		35		ns
$Q_{rr}$	Reverse Recovery Charge	$I_F=-5A, dI/dt=-100A/\mu s$		46		nC

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.

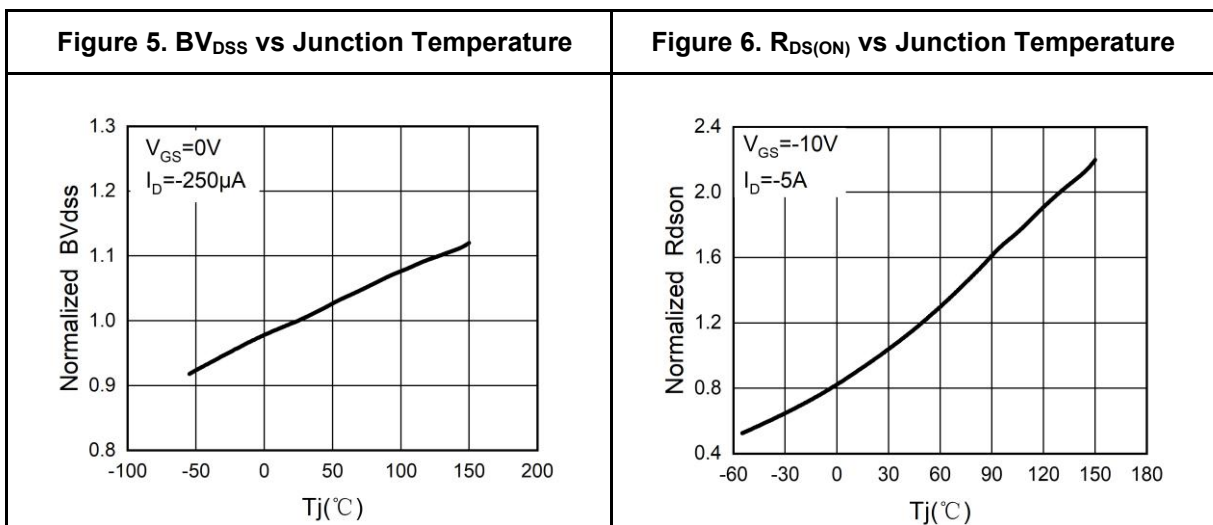
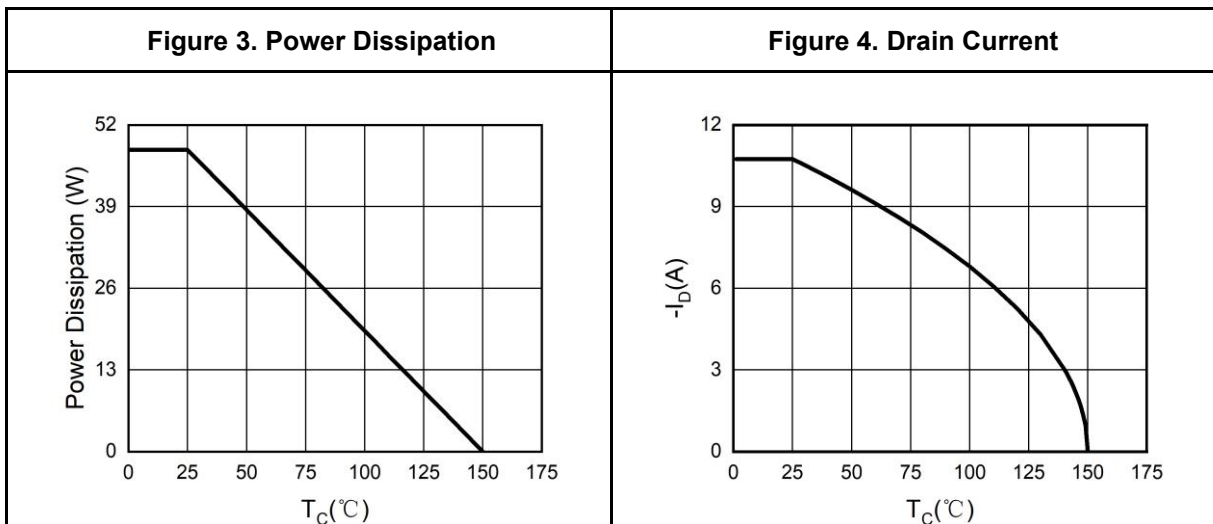
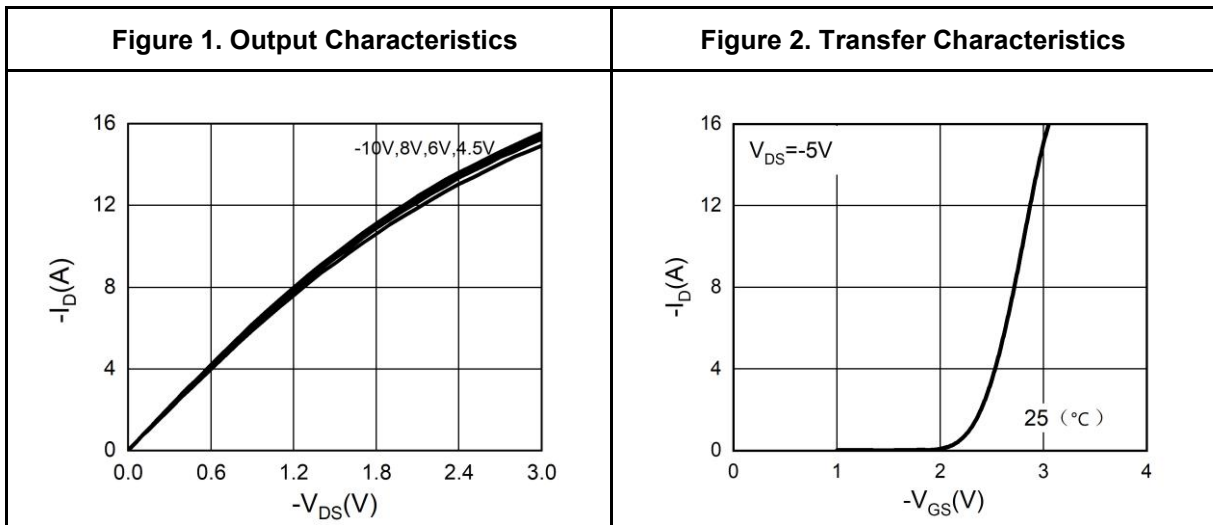
Notes 2.EAS condition:  $T_J=25^\circ\text{C}, V_{DD}=-40V, V_G=-10V, R_g=25\Omega, L=0.5\text{mH}$ .

Notes 3.Repetitive Rating: Pulse width limited by maximum junction temperature.



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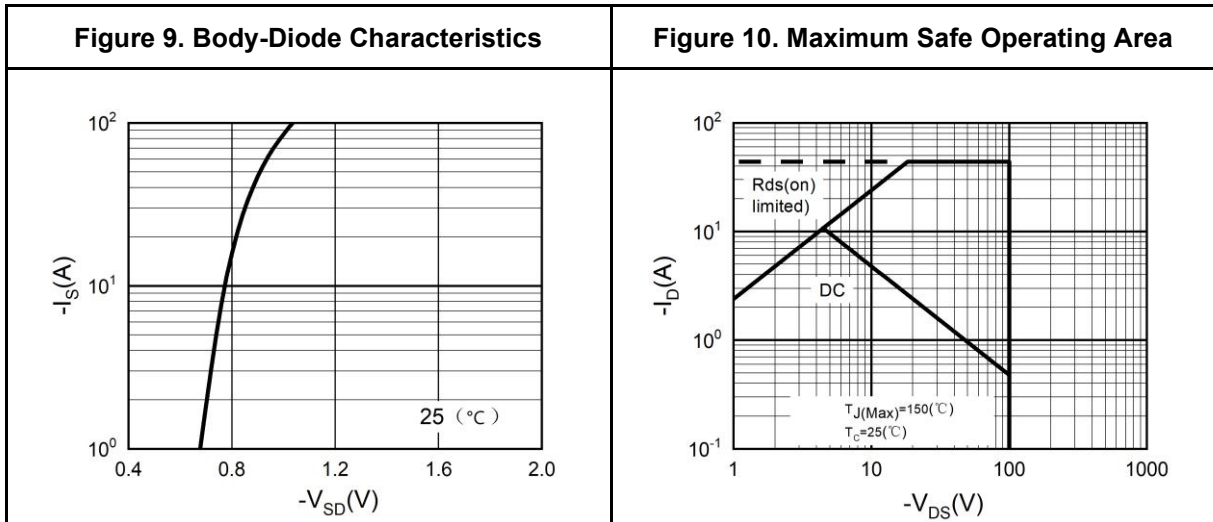
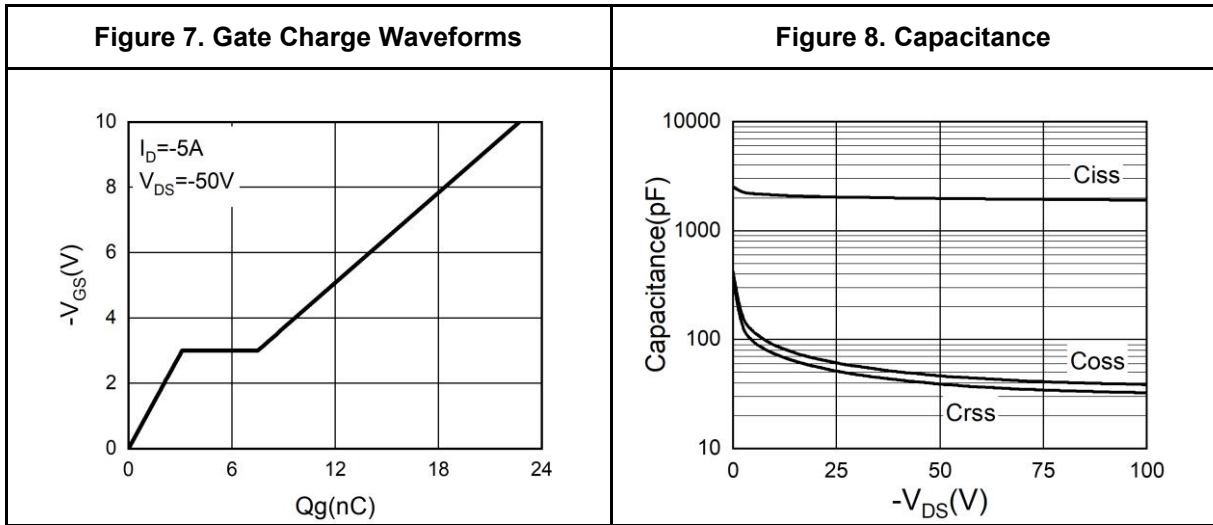
## P-Channel Typical Electrical And Thermal Characteristics (Curves)





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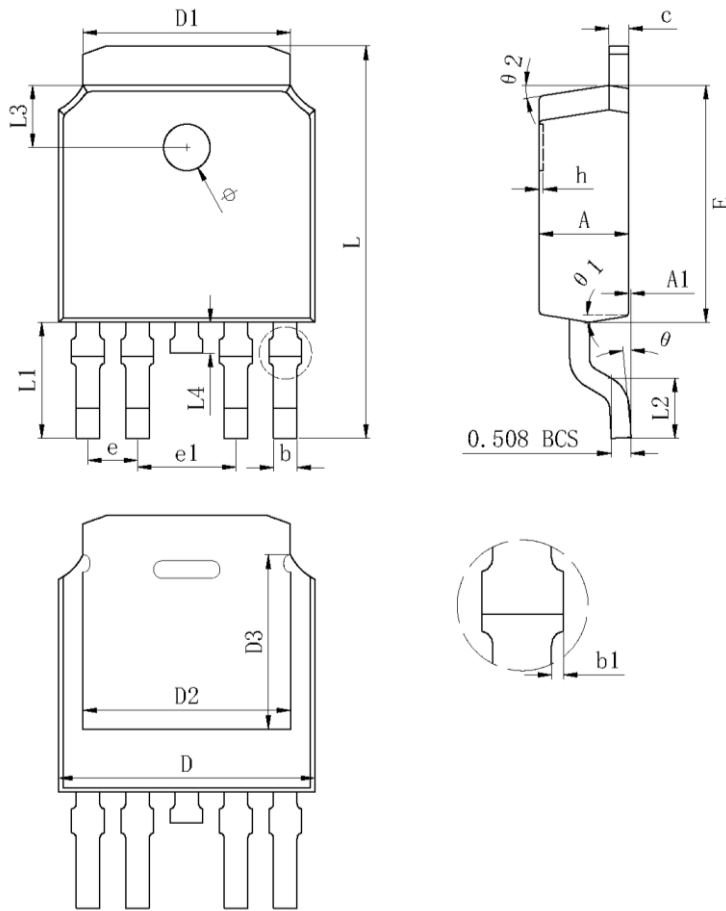
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TO-252-4L Package Information



SYMBOL	MILLIMETER		
	MIN	Typ.	MAX
A	2.200	2.300	2.400
A1	0.000		0.127
b	0.550	0.600	0.650
b1	0.000		0.120
c(电镀后)	0.460	0.520	0.580
D	6.500	6.600	6.700
D1	5.334 REF		
D2	5.346 REF		
D3	4.490 REF		
E	6.000	6.100	6.200
e	1.270 TYP		
e1	2.540 TYP		
h	0.000	0.100	0.200
L	9.900	10.100	10.300
L1	2.988 REF		
L2	1.400	1.550	1.700
L3	1.600 REF		
L4	0.700	0.800	0.900
phi	1.100	1.200	1.300
theta	0°		8°
theta 1	9° TYP		
theta 2	9° TYP		



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### Attention

This product described in this document can not be used in life support devices or systems, aircraft's control systems, and other applications whose failure can be reasonably expected to result in serious physical and/or material damage, apart from that when an application agreement is signed between customer and Wuxi Shangjia Semiconductor.

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