



200V N-Channel Trench Power MOSFET

General Description

The SJB02N5000 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a wide variety of applications.

Features

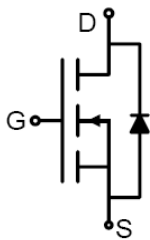
- Low Gate Charge
- 100% UIS Tested, 100% DVDS Tested
- High Power and current handling capability
- Lead free product is acquired

Application

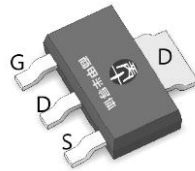
- PWM Applications
- Load Switch
- Power Management

Key Performance Parametes

Parameter	Value	Unit
V_{DS}	200	V
$R_{DS(ON_TYP)}$	471	m Ω
I_D	1.3	A
Q_G	16.8	nC



Schematic Diagram



SOT-223-3L top view



Package Marking and Ordering Information

Device/Ordering Code	Marking	Package	Reel Size	Tape width	Quantity
SJB02N5000	SJB02N5000	SOT-223-3L			

Table 1. Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Limit	Unit
V_{DS}	Drain-Source Voltage ($V_{GS}=0V$)	200	V
V_{GS}	Gate-Source Voltage ($V_{DS}=0V$)	± 20	V
I_D	Drain Current-Continuous($T_A=25^\circ\text{C}$)	1.3	A
	Drain Current-Continuous($T_A=100^\circ\text{C}$)	0.86	A
I_{DM} (pluse)	Drain Current-Continuous@ Current-Pulsed (Note 1)	5.2	A
P_D	Maximum Power Dissipation($T_A=25^\circ\text{C}$)	2.5	W
	Maximum Power Dissipation($T_A=100^\circ\text{C}$)	1	W
E_{AS}	Avalanche energy (Note 2)	10.6	mJ
T_J, T_{STG}	Operating Junction and Storage Temperature Range	-55 To 150	$^\circ\text{C}$

Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient		50	$^\circ\text{C/W}$



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Table 3. Electrical Characteristics ($T_J=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V I _D =250μA	200			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =200V, V _{GS} =0V T _J =25℃			1	μA
		V _{DS} =200V, V _{GS} =0V T _J =125℃			±100	nA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V			±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	1		3	V
g _{FS}	Forward Transconductance	V _{DS} =5V, I _D =2A		6		S
R _{DS(ON)}	Drain-Source On-State Resistance	V _{GS} =10V, I _D =2A T _J =25℃		471	611	mΩ
R _{DS(ON)}	Drain-Source On-State Resistance	V _{GS} =4.5V, I _D =2A T _J =25℃		478.6	636.5	mΩ
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =100V,V _{GS} =0V, f=1.0MHz		742		pF
C _{oss}	Output Capacitance			14		pF
C _{rss}	Reverse Transfer Capacitance			5		pF
Switching Parameters						
t _{d(on)}	Turn-on Delay Time	V _{GS} =10V, V _{DS} =100V, R _L =50Ω, R _{GEN} =2.5Ω		10		nS
t _r	Turn-on Rise Time			13		nS
t _{d(off)}	Turn-Off Delay Time			16		nS
t _f	Turn-Off Fall Time			14		nS
Q _g	Total Gate Charge	V _{GS} =10V, V _{DS} =100V, I _D =2A		16.8		nC
Q _{gs}	Gate-Source Charge			2.4		nC
Q _{gd}	Gate-Drain Charge			6.8		nC
Source-Drain Diode Characteristics						
I _{SD}	Source-Drain Current (Body Diode)				1.3	A
V _{SD}	Forward on Voltage ^(Note 2)	V _{GS} =0V, I _S =2A			1.2	V
t _{rr}	Reverse Recovery Time	I _F =2A, dI/dt=100A/μs		86		ns
Q _{rr}	Reverse Recovery Charge	I _F =2A, dI/dt=100A/μs		290		nC

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.

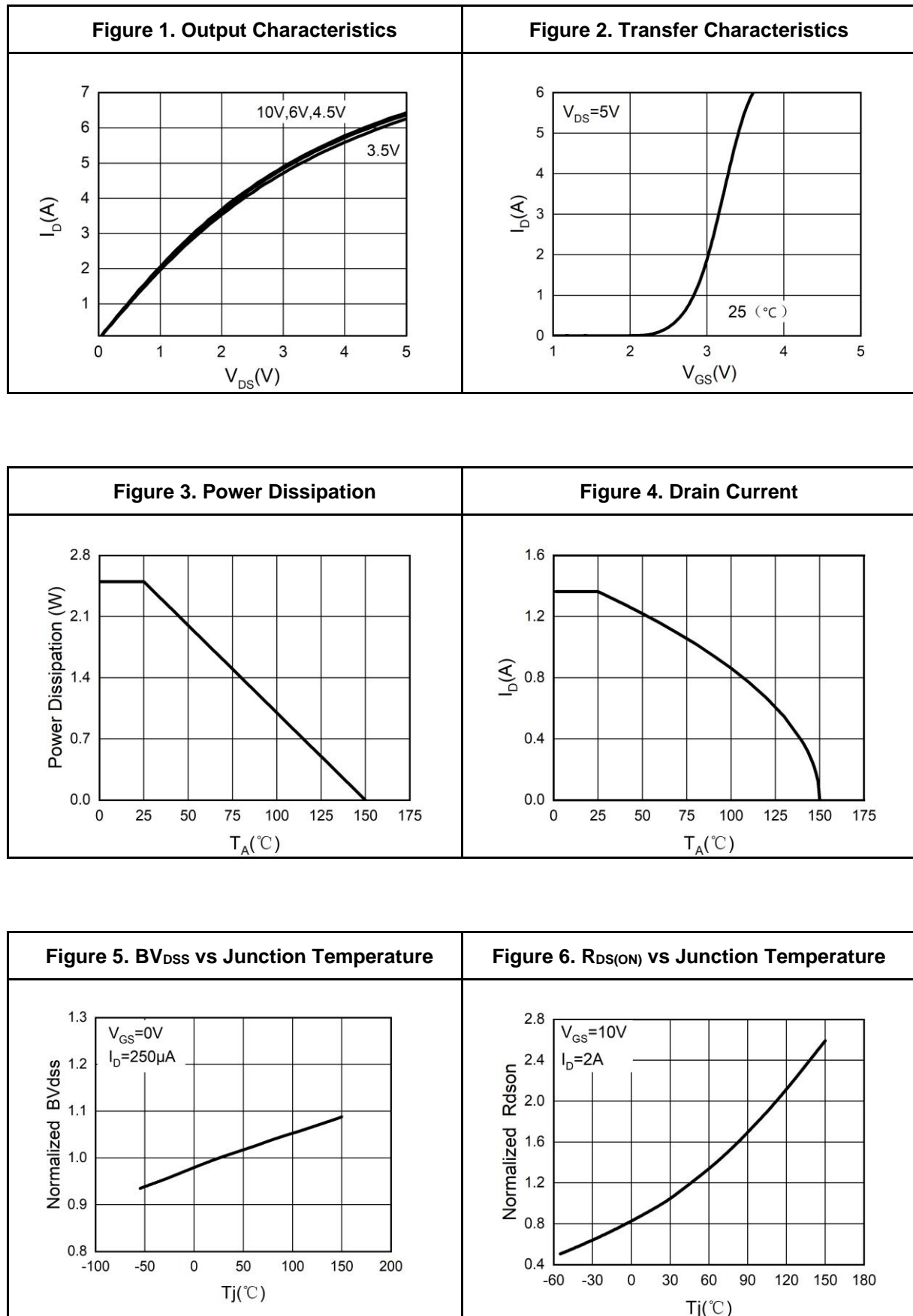
Notes 2.EAS condition: $T_J=25^{\circ}\text{C}, V_{DD}=40V, V_G=10V, R_g=25\Omega, L=0.5mH$.

Notes 3.Repetitive Rating: Pulse width limited by maximum junction temperature.



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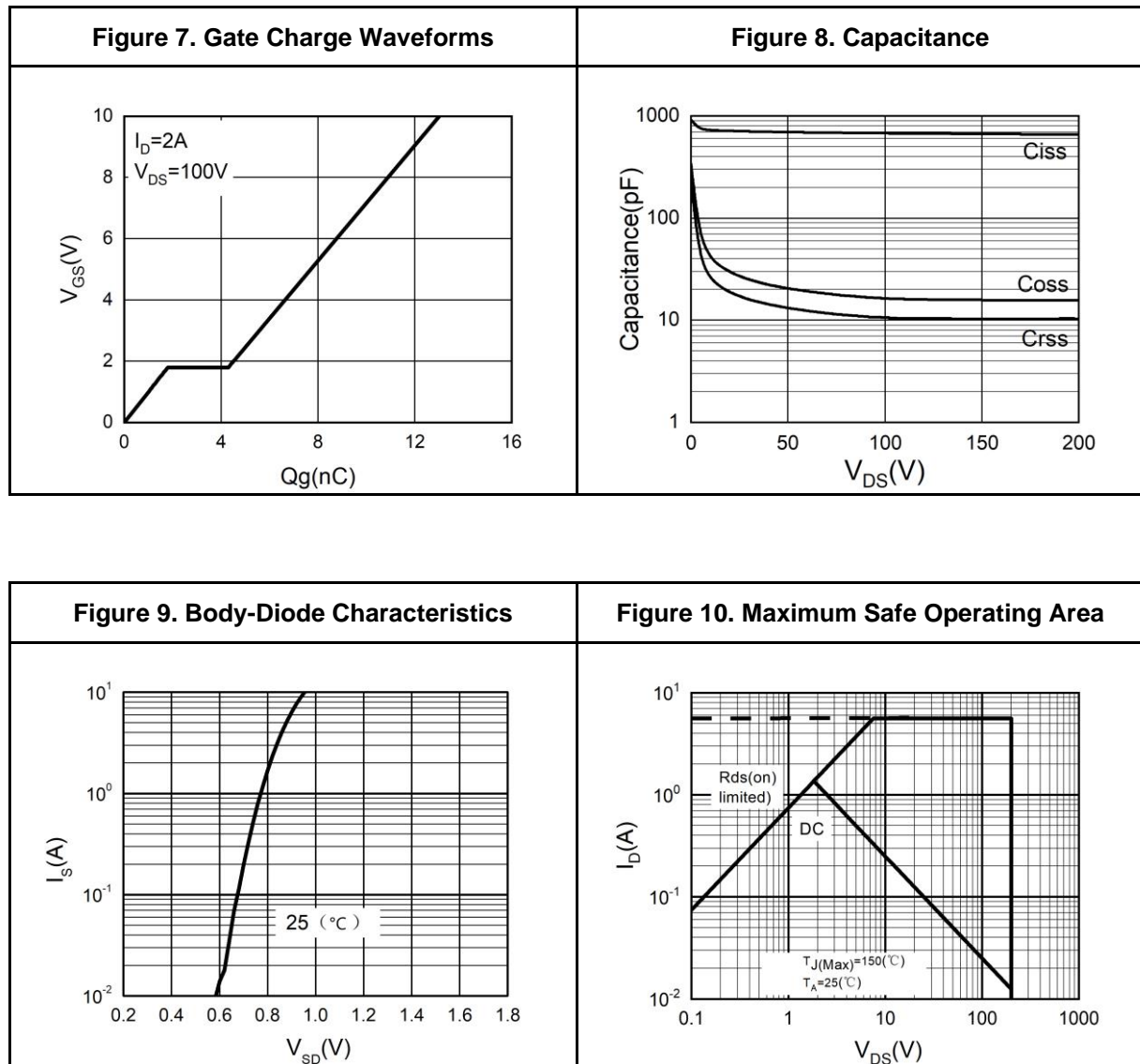
Typical Electrical And Thermal Characteristics (Curves)





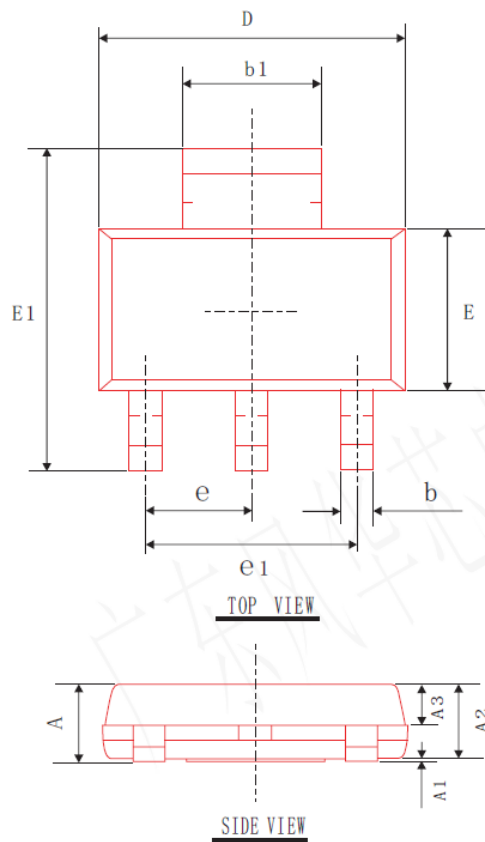
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Typical Electrical And Thermal Characteristics (Curves)





SOT-223-3L Package Information



COMMON DIMENSIONS
(UNITS OF MEASURE=mm)

SYMBOL	MIN	NOM	MAX
A	—	—	1.80
A1	0.00	0.05	0.10
A2	1.50	1.60	1.70
A3	0.85	0.90	0.95
b	0.66	0.70	0.80
b1	2.96	3.00	3.10
c	0.25	0.30	0.35
D	6.30	6.50	6.70
E	3.30	3.50	3.70
E1	6.80	7.00	7.20
e1	4.40	4.60	4.80
L	0.90	—	1.15
θ	0°	5°	10°
e	2.3 BSC		



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