

Unit

V

mΩ

А

nC

# **80V N-Channel Trench Power MOSFET**

Value

80

4.3

148

193

Key Performance Parametes

### **General Description**

The SJ80N039 uses advanced trench technology to provide excellent R<sub>DS(ON)</sub>, low gate charge and operation with gate voltages as low as 10V. This device is suitable for use as a wide variety of applications.

Parameter

R<sub>DS(ON)\_TYP</sub>

Vos

ΙD

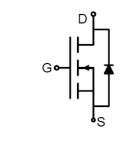
QG

#### Features

- Low Gate Charge
- 100% UIS Tested, 100% DVDS Tested
- High Power and current handing capability
- Lead free product is acquired

### Application

- PWM Applications
- Load Switch
- Power Management







Schematic Diagram

TO-220 top view

### **Package Marking and Ordering Information**

Device/Ordering Code	Marking	Package	Packing	Reel Size	Tape width	Quantity
SJ80N039	SJ80N039	TO-220	Tube	١	١	1000 Pcs

#### Table 1. Absolute Maximum Ratings (T<sub>c</sub>=25℃ unless otherwise noted)

Symbol	Parameter	Limit	Unit
V <sub>DS</sub>	Drain-Source Voltage (V <sub>GS</sub> =0V)	80	V
V <sub>GS</sub>	Gate-Source Voltage (V <sub>DS</sub> =0V)	±20	V
1	Drain Current-Continuous(Tc=25°C)	148	А
lo	Drain Current-Continuous(Tc=100℃)	93	А
DM (pluse)	Drain Current-Continuous@ Current-Pulsed (Note 1)	592	А
Р	Maximum Power Dissipation(T_c=25 $^\circ\!\mathrm{C}$ )	250	W
PD	Maximum Power Dissipation(Tc=100°C)	100	W
E <sub>AS</sub>	Avalanche energy (Note 2)	756	mJ
T <sub>J</sub> , T <sub>STG</sub>	Operating Junction and Storage Temperature Range	-55 To 150	C

#### Table 2. Thermal Characteristic

Symbol	Parameter	Тур	Max	Unit
R <sub>θ</sub> JC	Thermal Resistance, Junction-to-Case		0.5	°C/W

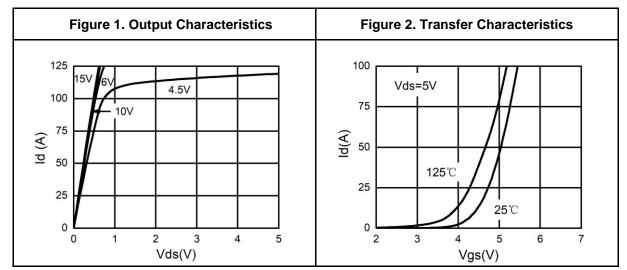
Table 3.	Electrical	Characteristics	<b>(T</b> J <b>=25°</b> ℃	unless otherwise noted)
----------	------------	-----------------	---------------------------	-------------------------

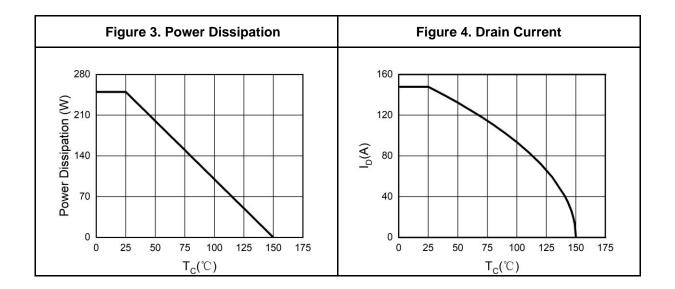
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
On/Off States						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V I <sub>D</sub> =250µA	80			V
IDSS	Zero Gate Voltage Drain Current	V <sub>DS</sub> =80V, V <sub>GS</sub> =0V			1	μA
lgss	Gate-Body Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$			±100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250µA	2		4	V
<b>g</b> fs	Forward Transconductance	V <sub>DS</sub> =10V, I <sub>D</sub> =15A		27.5		S
Rds(on)	Drain-Source On-State Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =40A	4.3		5.2	mΩ
Dynamic Chara	cteristics		1		L	
Ciss	Input Capacitance			8247		pF
Coss	Output Capacitance	V <sub>DS</sub> =25V,V <sub>GS</sub> =0V, f=1.0MHz		499		pF
Crss	Reverse Transfer Capacitance			350		pF
Rg	Gate resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1.0MHz		0.55		Ω
Switching Para	meters					
t <sub>d(on)</sub>	Turn-on Delay Time			55		nS
tr	Turn-on Rise Time	V <sub>GS</sub> =10V, V <sub>DS</sub> =40V,		40		nS
$t_{d(off)}$	Turn-Off Delay Time	$R_L=1\Omega$ , $R_{GEN}=3\Omega$		115		nS
t <sub>f</sub>	Turn-Off Fall Time			47		nS
Qg	Total Gate Charge			193		nC
Q <sub>gs</sub>	Gate-Source Charge	V <sub>GS</sub> =10V, V <sub>DS</sub> =40V, I <sub>D</sub> =40A		32		nC
$Q_{gd}$	Gate-Drain Charge			72		nC
Source-Drain D	iode Characteristics		1	1	1	
Isd	Source-Drain Current (Body Diode)				148	Α
Vsd	Forward on Voltage (Note 3)	V <sub>GS</sub> =0V, I <sub>S</sub> =40A			1.2	V
trr	Reverse Recovery Time	I⊧=20A, dI/dt=500A/μs		16		ns
Qrr	Reverse Recovery Charge	I⊧=20A, dI/dt=500A/μs		70		nC

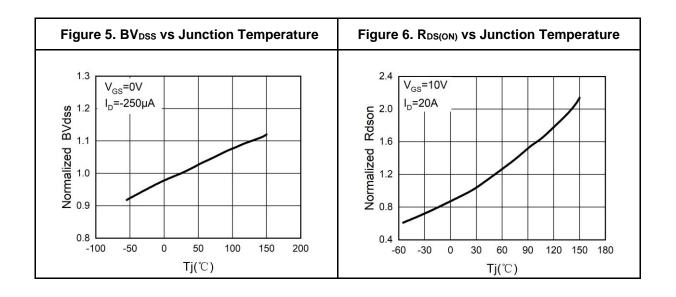
Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature. Notes 2.E<sub>AS</sub> condition:  $T_J=25^\circ$ C, $V_{DD}=40V$ , $V_G=10V$ , Rg=25 $\Omega$ , L=0.5mH. Notes 3.Repetitive Rating: Pulse width limited by maximum junction temperature.



### **Typical Electrical And Thermal Characteristics (Curves)**





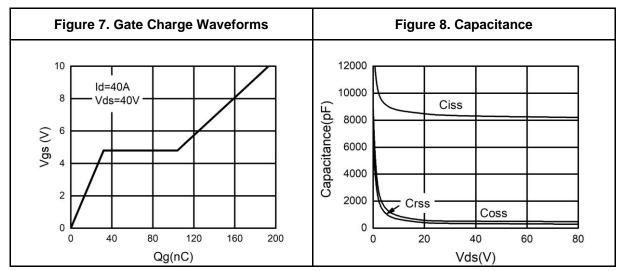


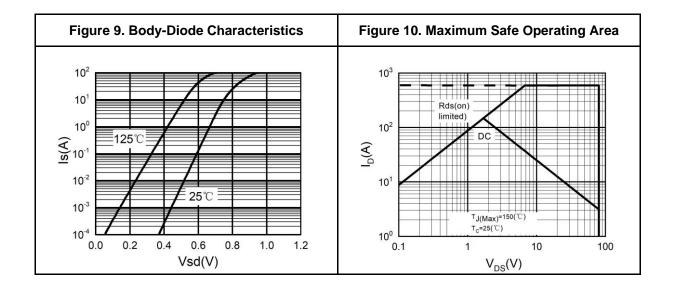


# **80V N-Channel Trench Power MOSFET**

SJ80N039

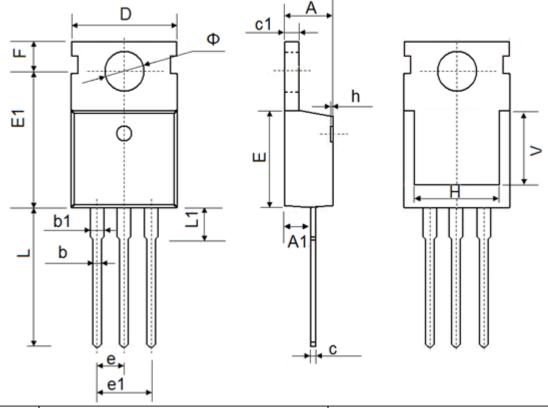
### Typical Electrical And Thermal Characteristics (Curves)





# 80V N-Channel Trench Power MOSFET

## **TO-220 Package Information**



ymbol	Dimens	sions In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Мах	
А	4.300	4.700	0.169	0.185	
A1	2.200	2.600	0.087	0.102	
b	0.700	0.950	0.028	0.037	
b1	1.170	1.410	0.046	0.056	
С	0.450	0.650	0.018	0.026	
c1	1.200	1.400	0.047	0.055	
D	9.600	10.400	0.378	0.409	
Е	8.8500	9.750	0.348	0.384	
E1	12.650	12.950	0.498	0.510	
е	2.540	TYP.	0.100TYP.		
e1	4.980	5.180	0.196	0.204	
F	2.650	2.950	0.104	0.116	
Н	7.900	8.100	0.311	0.319	
h	0.000	0.300	0.000	0.012	
L	12.750	14.300	0.502	0.563	
L1	2.850	3.950	0.112	0.156	
V	7.500	REF.	0.295 R	REF.	
Φ	3.400	4.000	0.134	0.157	



### Attention

This product described in this document can not be used in life support devices or systems, aircraft's control systems, and other applications whose failure can be reasonably expected to result in serious physical and/or material damage, apart from that when an application agreement is signed between customer and Wuxi Shangjia Semiconductor.

The performances and characteristics of this product in the independent testing state are displayed in this document. Wuxi Shangjia Semiconductor can't guarantee of the performances and characteristics of this described product that mounted in the customer's products or equipments as same as that in the independent testing state. So the customer should evaluate and test devices mounted in the customer's products or equipments.

Wuxi Shangjia Semiconductor reserves the right to improve the designs, functions and reliability of this product and modify any and all information described in this document without notice customer, apart from that when an notice agreement is signed between customer and Wuxi Shangjia Semiconductor.

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Wuxi Shangjia Semiconductor hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.